



US00D476959S

(12) **United States Design Patent**
Yamada et al.

(10) **Patent No.:** **US D476,959 S**

(45) **Date of Patent:** **** Jul. 8, 2003**

(54) **SEMICONDUCTOR DEVICE**

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(73) Assignee: **Mitsubishi Denki Kabushiki Kaisha**, Tokyo (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/164,710**

(22) Filed: **Jul. 31, 2002**

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/110, 123, D13/125, 179, 182; 174/16.3, 52.1, 52.2, 52.4, 261; 211/4.17, 162; 361/718, 728, 747, 748, 760, 774, 784, 785, 813, 820

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(57) **CLAIM**

The ornamental design for a semiconductor device, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a semiconductor device, showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a right side elevational view thereof; and,

FIG. 7 is a left side elevational view thereof.

1 Claim, 3 Drawing Sheets

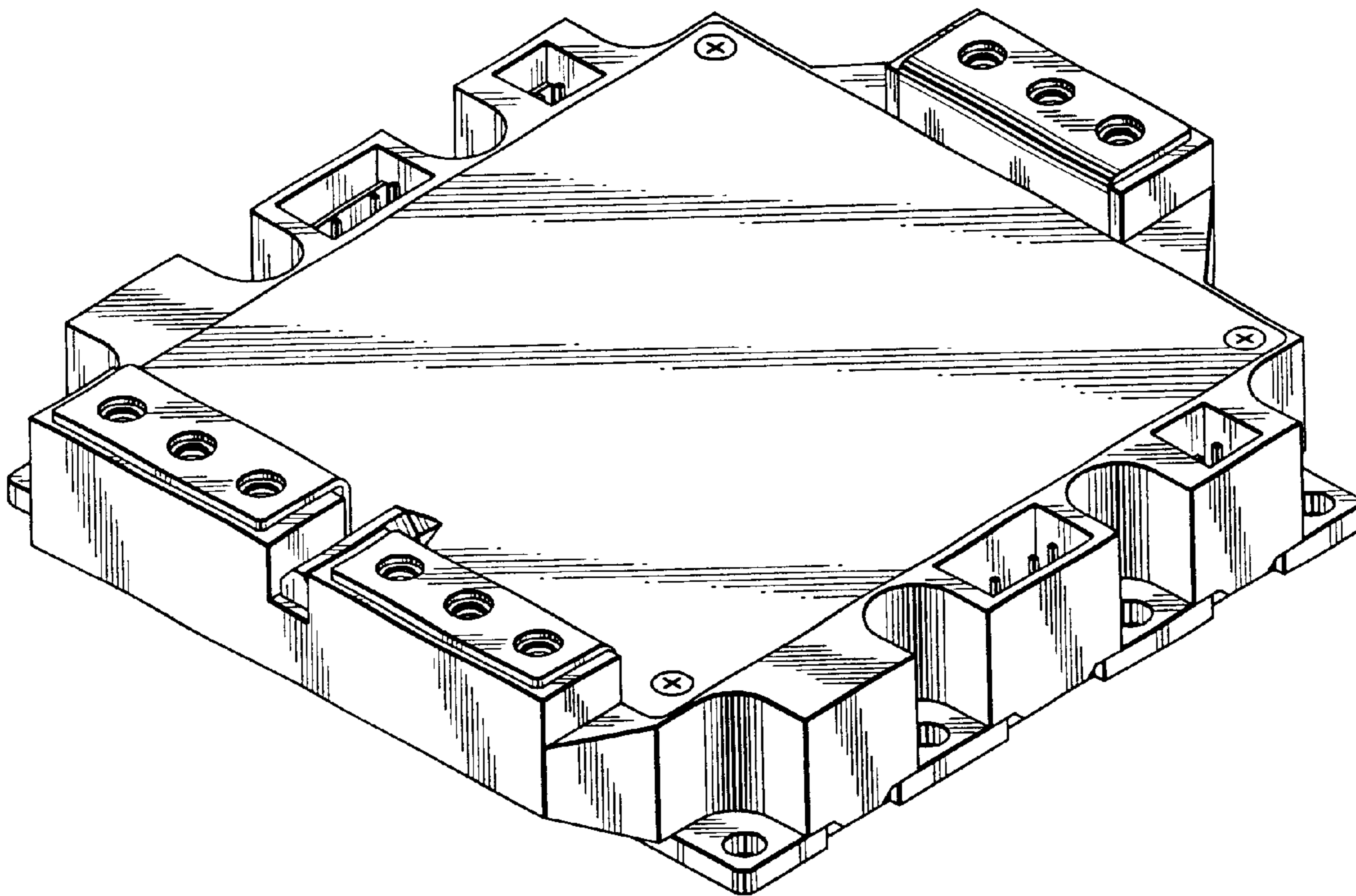


FIG. 1

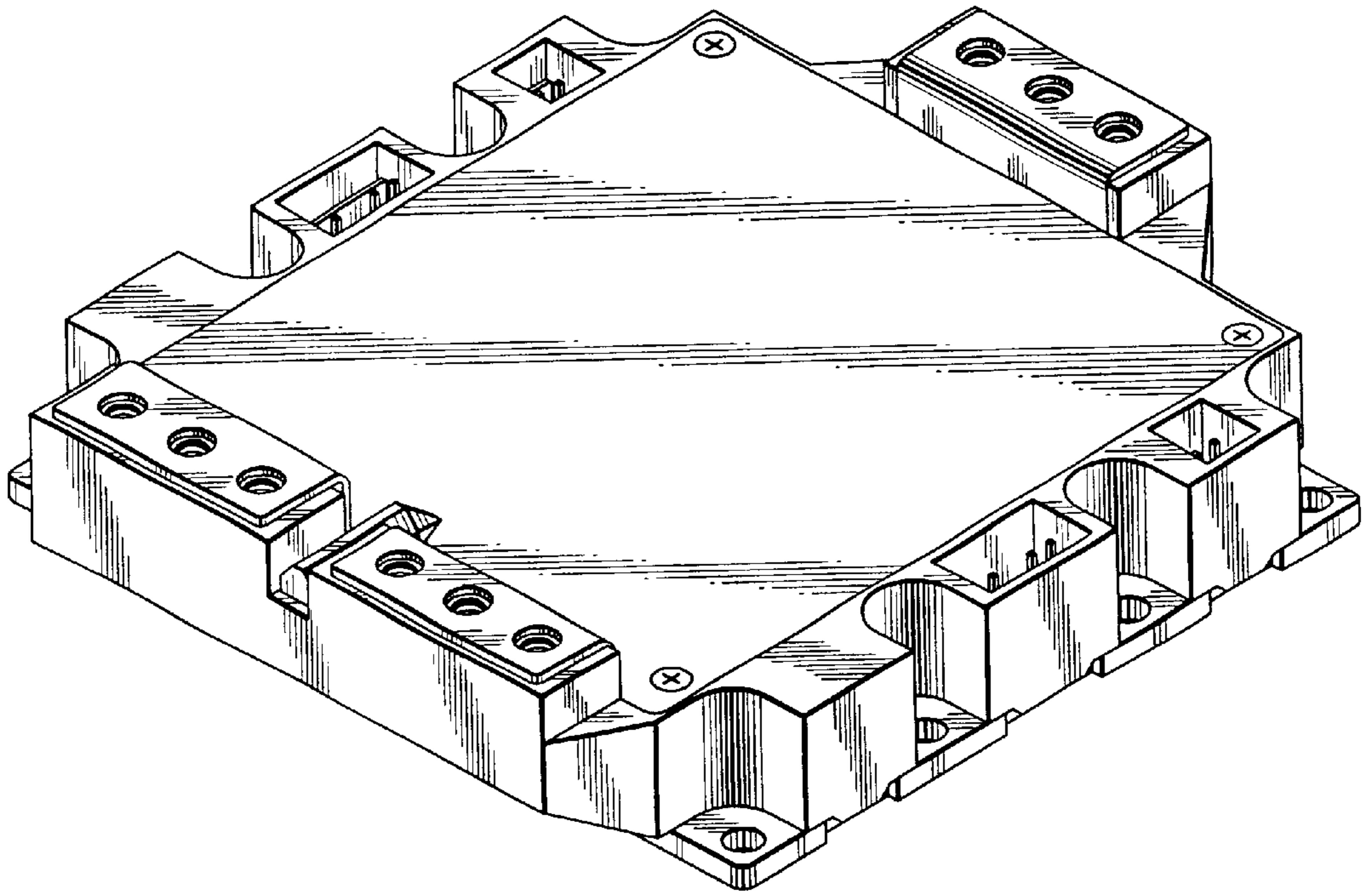


FIG. 2

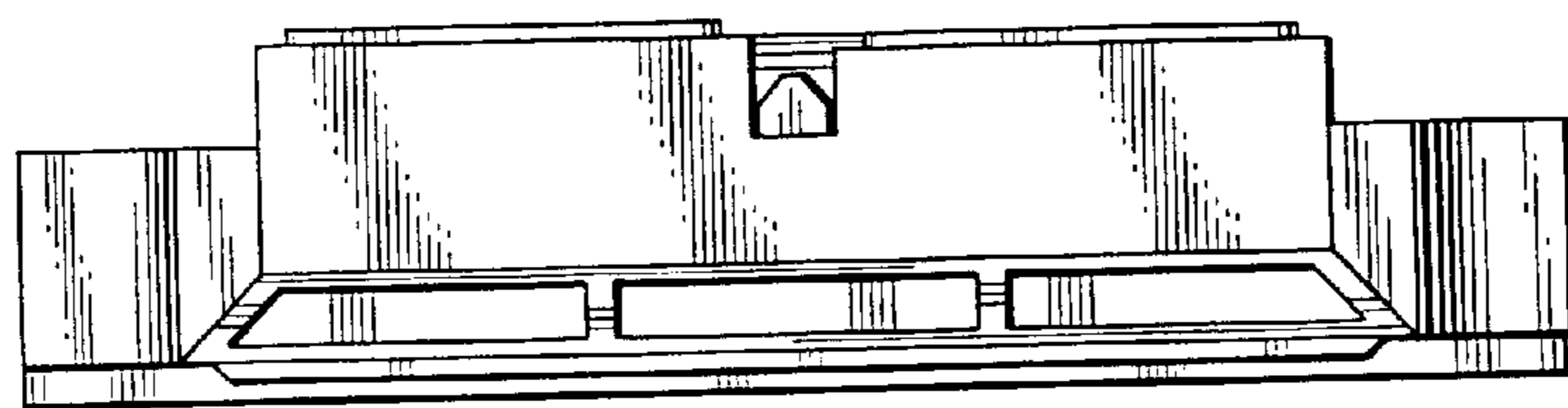


FIG. 3

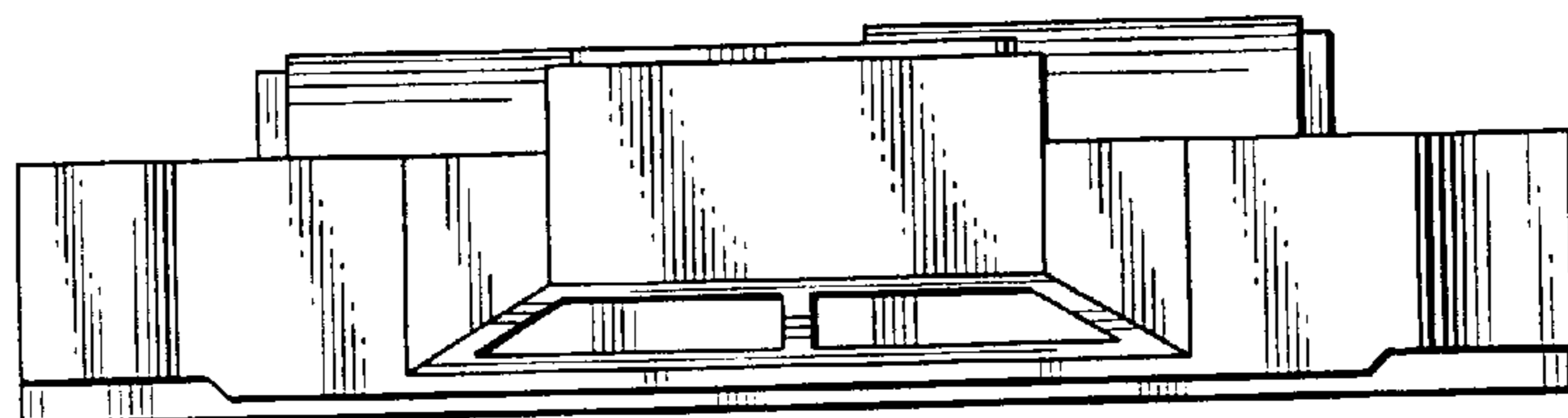


FIG. 4

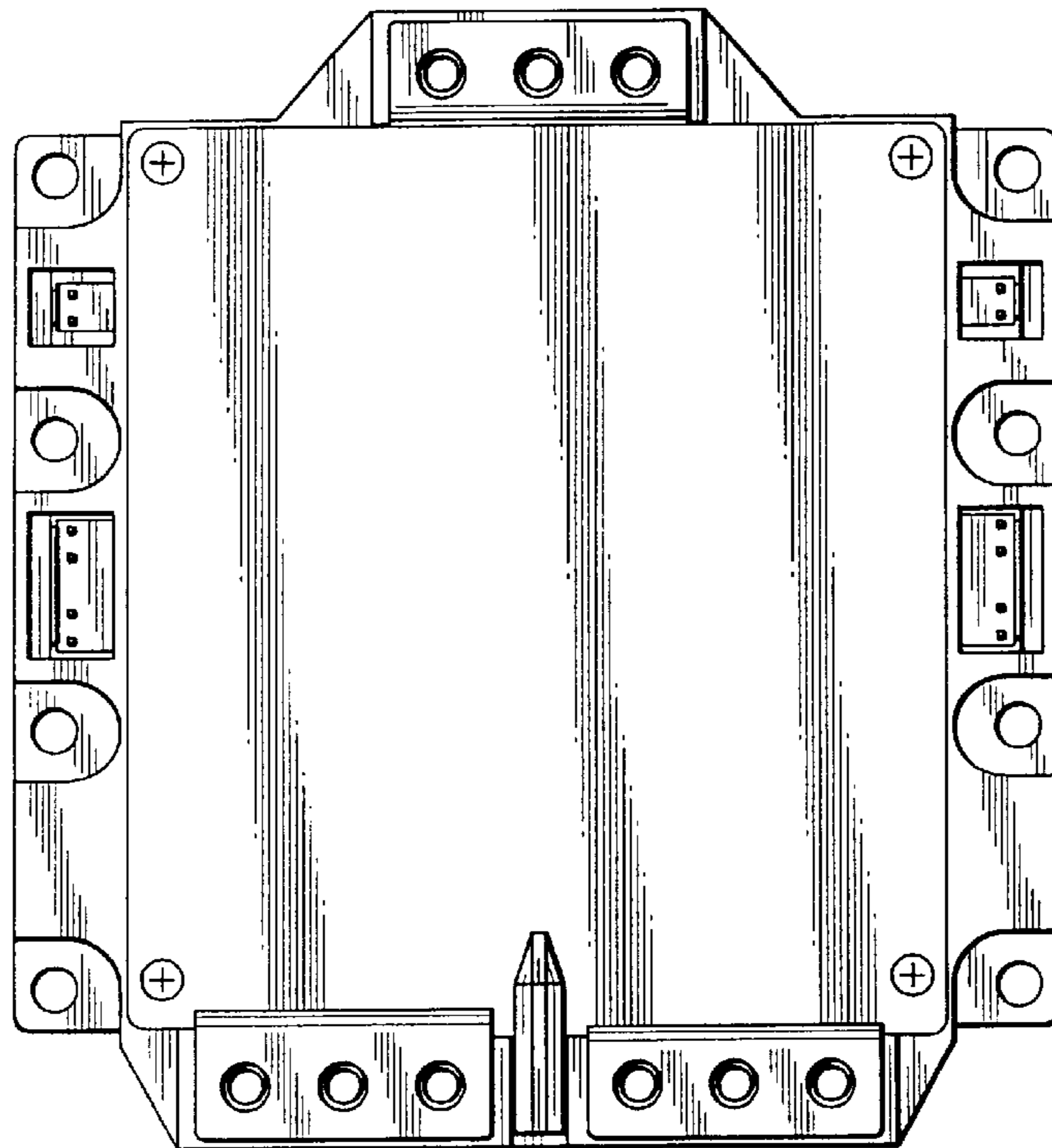


FIG. 5

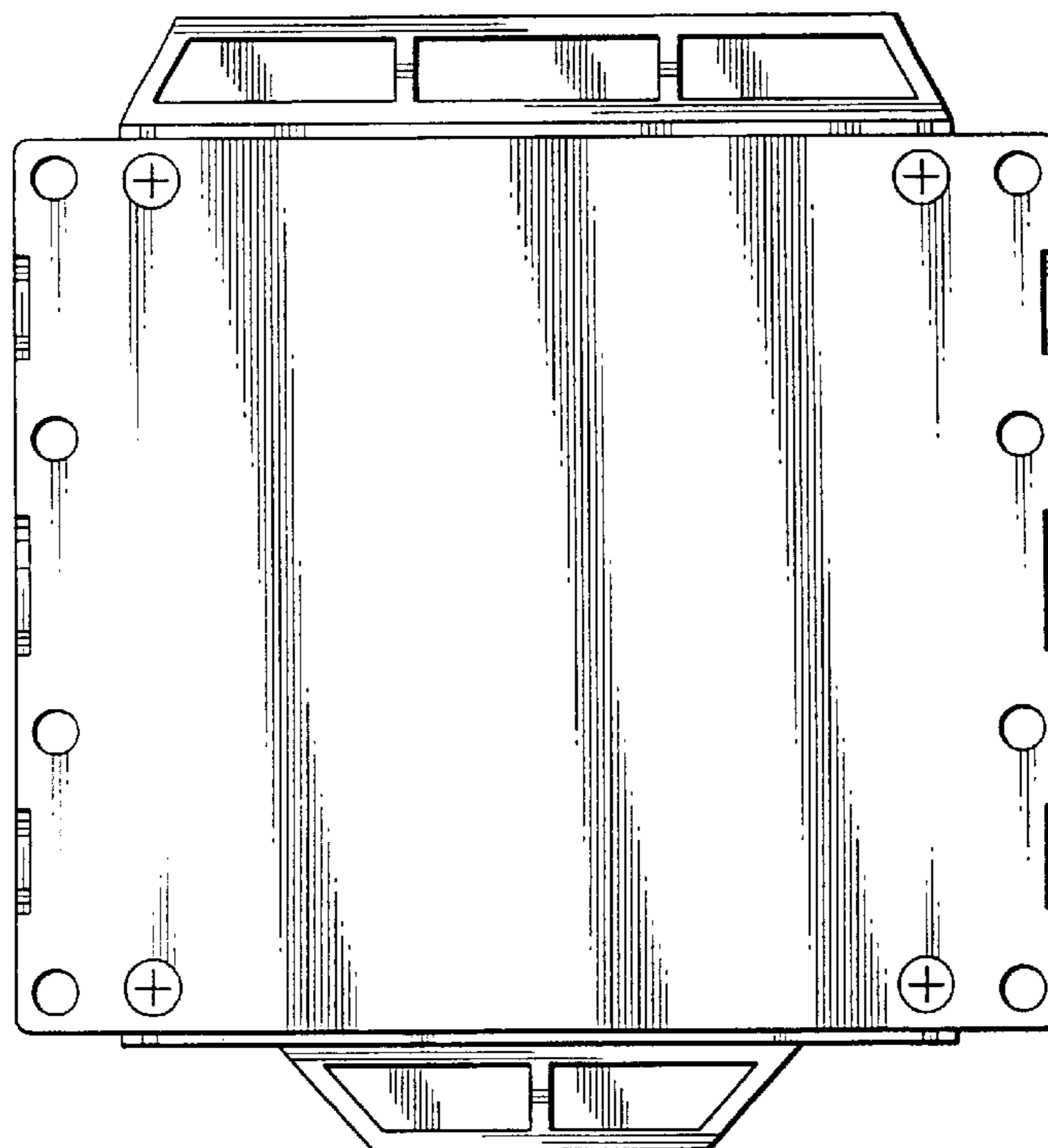


FIG. 6

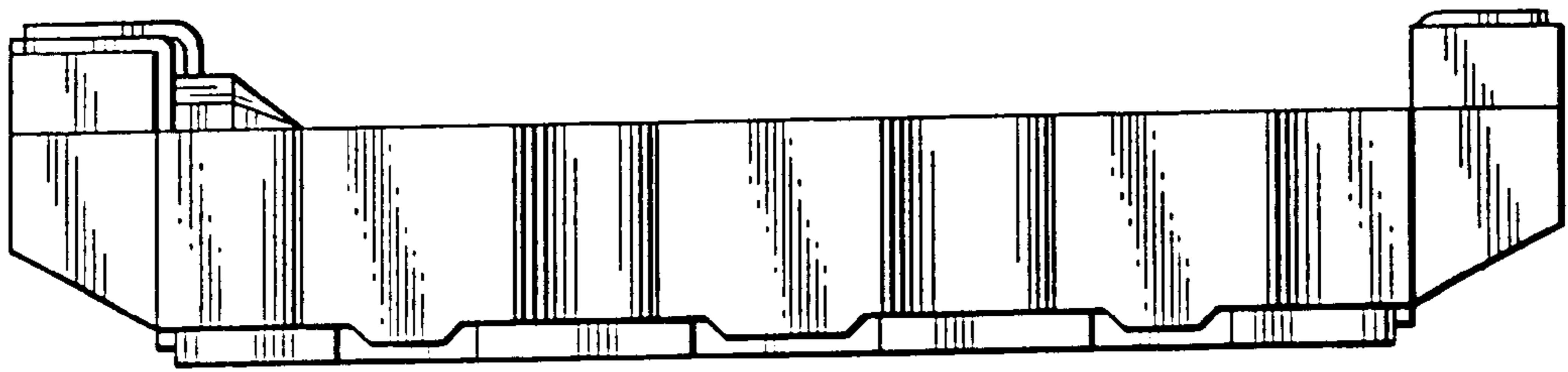


FIG. 7

