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(12) **United States Design Patent**
Matsumoto

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(45) **Date of Patent:** **** Apr. 8, 2003**

(54) **STAND FOR ELECTRONIC COMPUTERS**
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(73) Assignee: **Kabushiki Kaisha Toshiba, Tokyo (JP)**
(**) Term: **14 Years**

CN 99307754.4 12/1999
CN 99320715.4 4/2000
CN 99337232.5 7/2000
TW 087308638 5/2000
TW 087308640 5/2000

(21) Appl. No.: **29/146,494**
(22) Filed: **Aug. 13, 2001**

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(30) **Foreign Application Priority Data**

Feb. 15, 2001 (JP) 2001-003338

* cited by examiner

Primary Examiner—Kay H. Chin

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(51) **LOC (7) Cl.** **14-02**
(52) **U.S. Cl.** **D14/447; D14/451**
(58) **Field of Search** D14/432, 434,
D14/439, 447, 448, 451, 457; 361/686,
679-683; D3/201, 304, 318, 269; 206/305,
320, 701; D13/108, 107

(57) **CLAIM**

The ornamental design for a stand for electronic computers, as shown and described.

(56) **References Cited**

U.S. PATENT DOCUMENTS

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D412,162 S * 7/1999 Tal et al. D14/447
D420,989 S * 2/2000 Sandhu et al. D14/447

DESCRIPTION

FIG. 1 is a top, front and right side perspective view of a stand for electronic computers showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a right side elevational view, a left side elevational view being a mirror image thereof, FIG. 5 is a top plan view thereof; and, FIG. 6 is a bottom plan view thereof.

FOREIGN PATENT DOCUMENTS

CN 98322746.2 12/1999

1 Claim, 3 Drawing Sheets

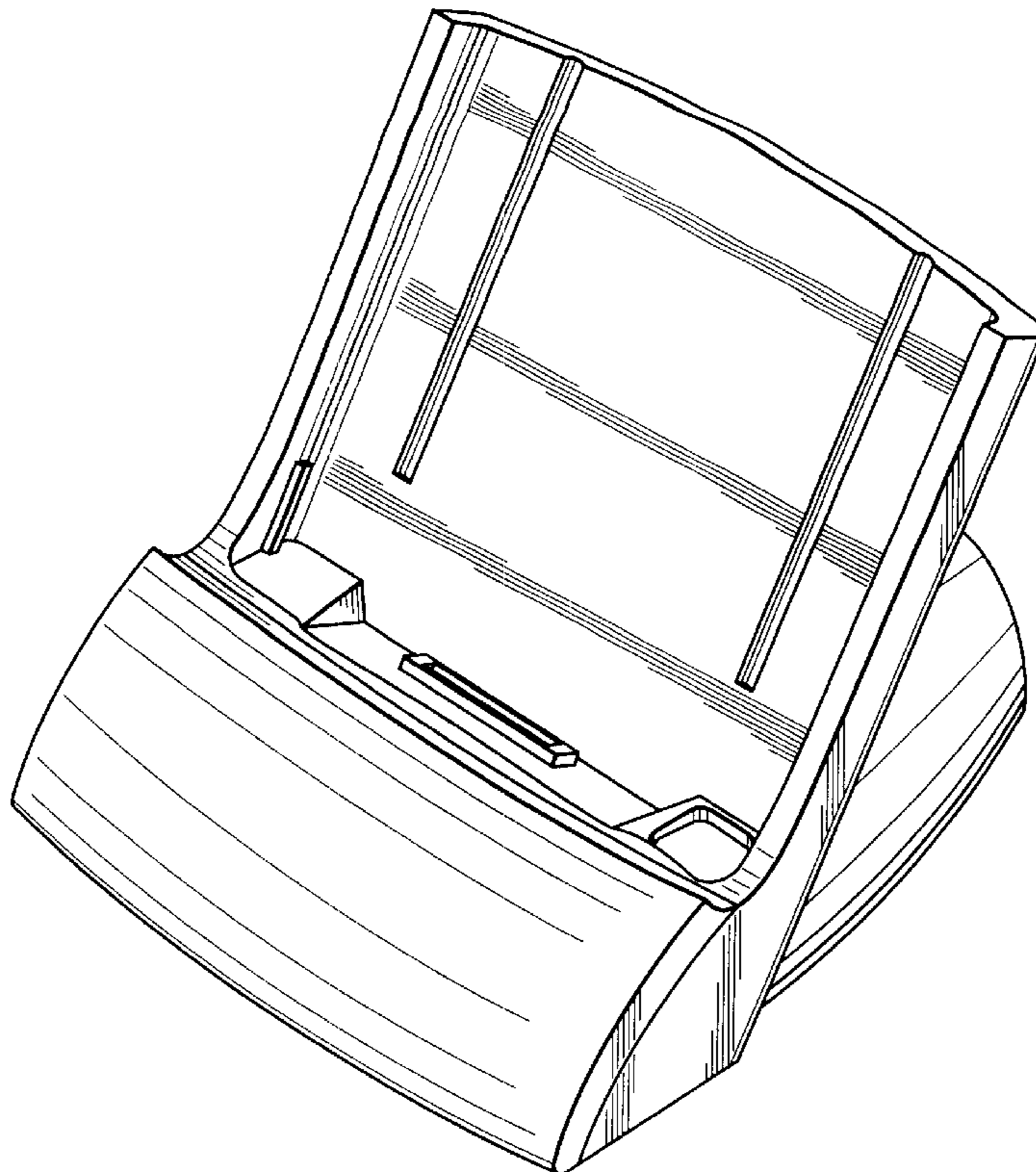


FIG. 1

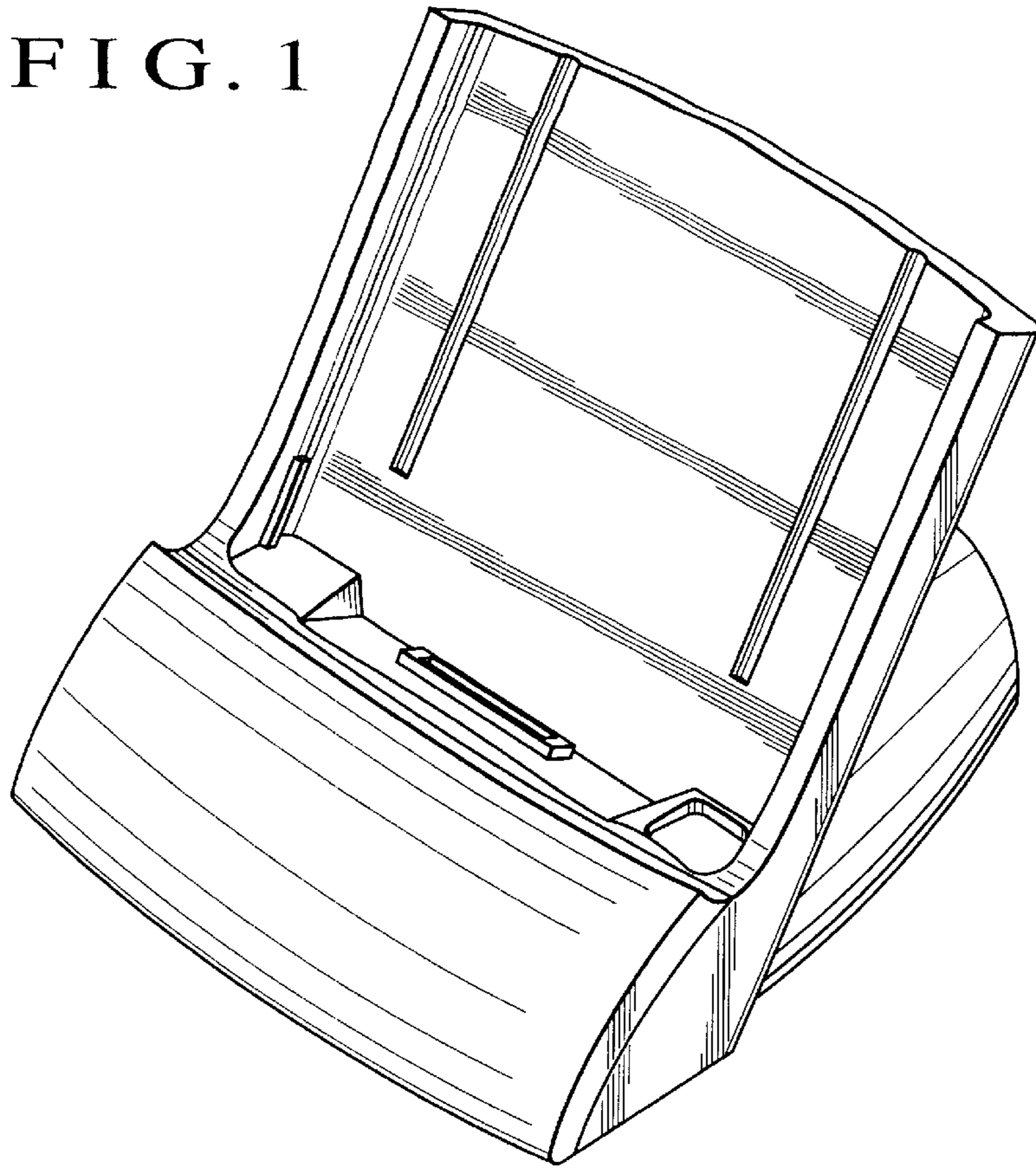


FIG. 2

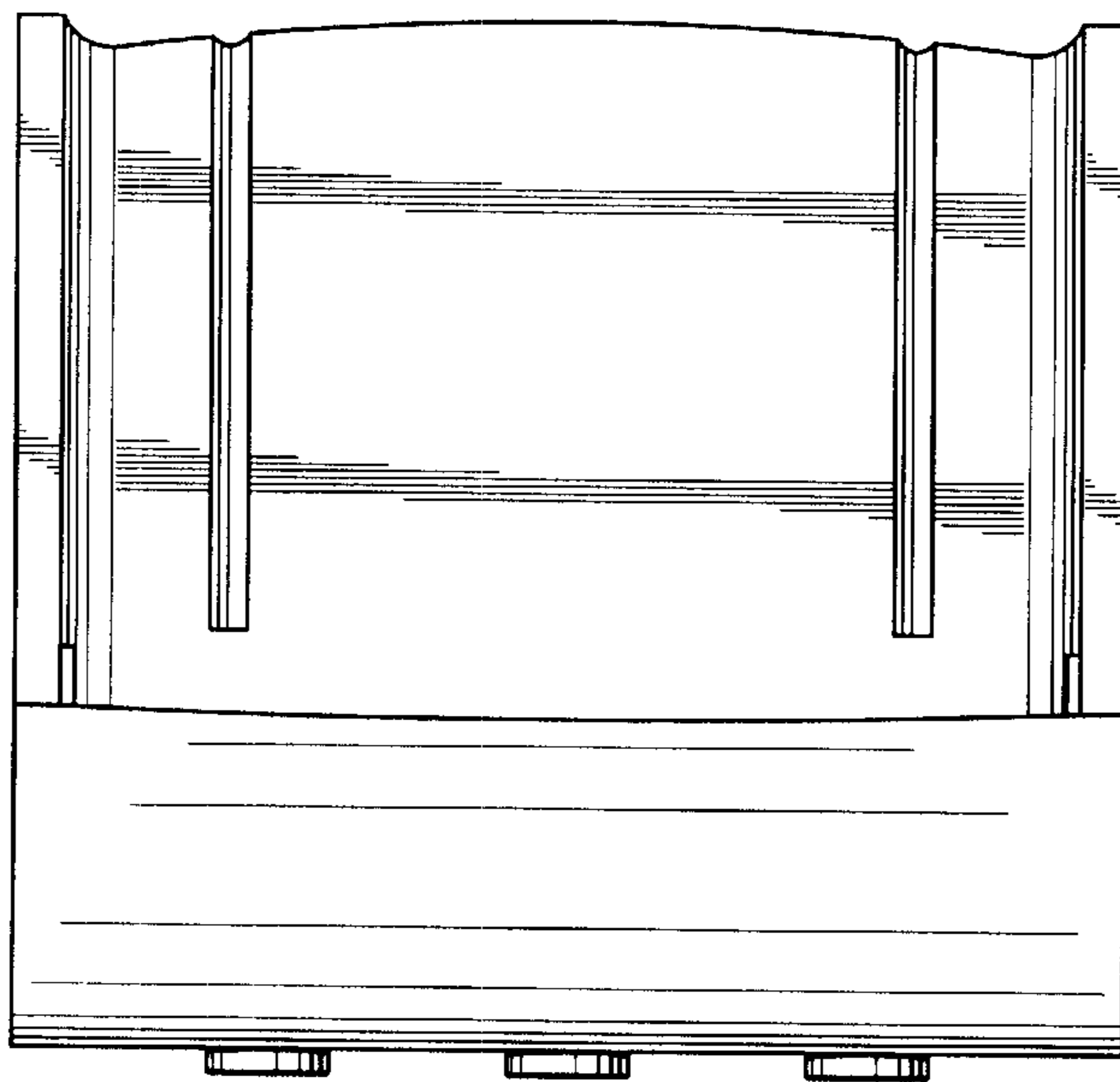


FIG. 3

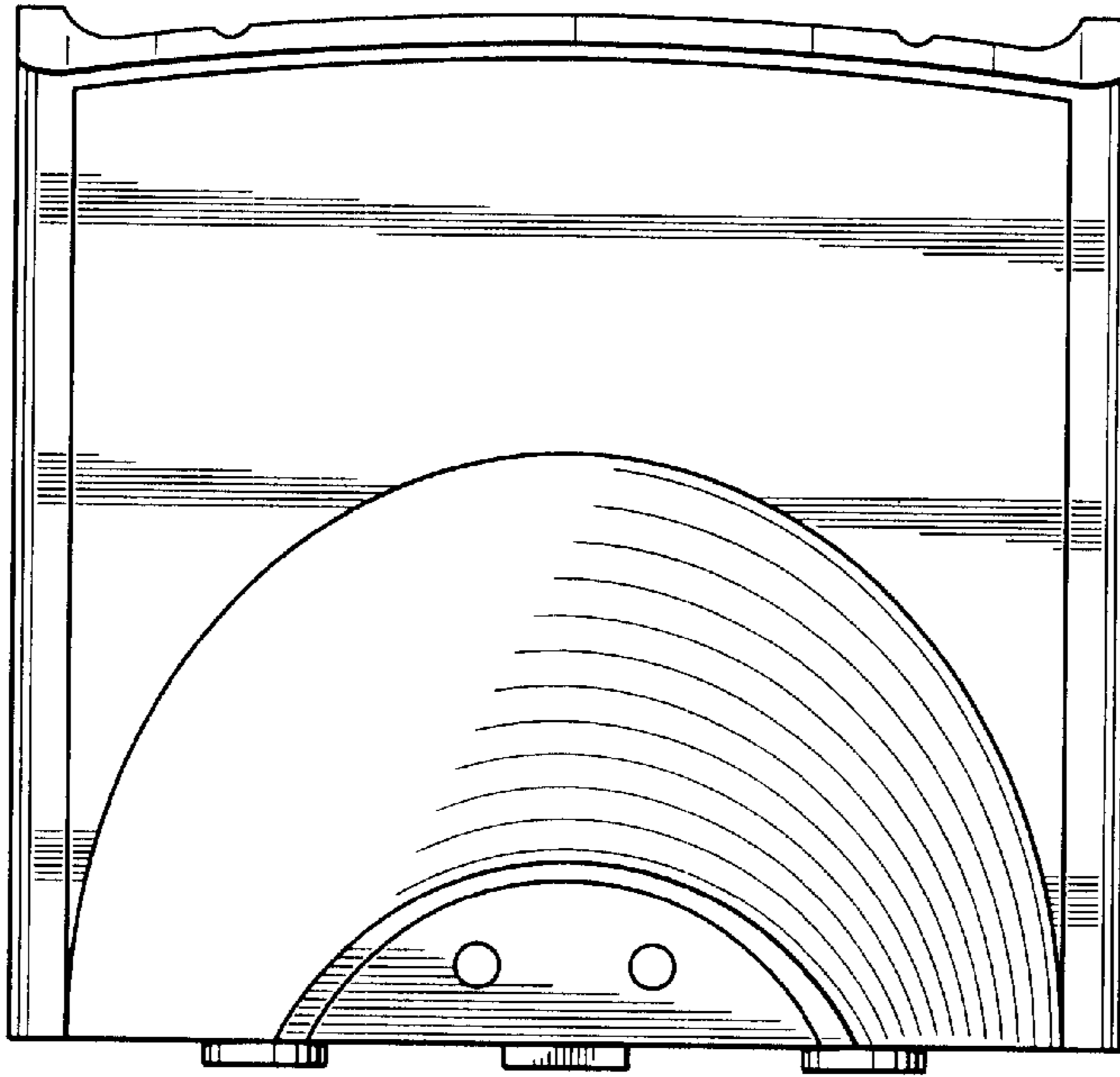


FIG. 4

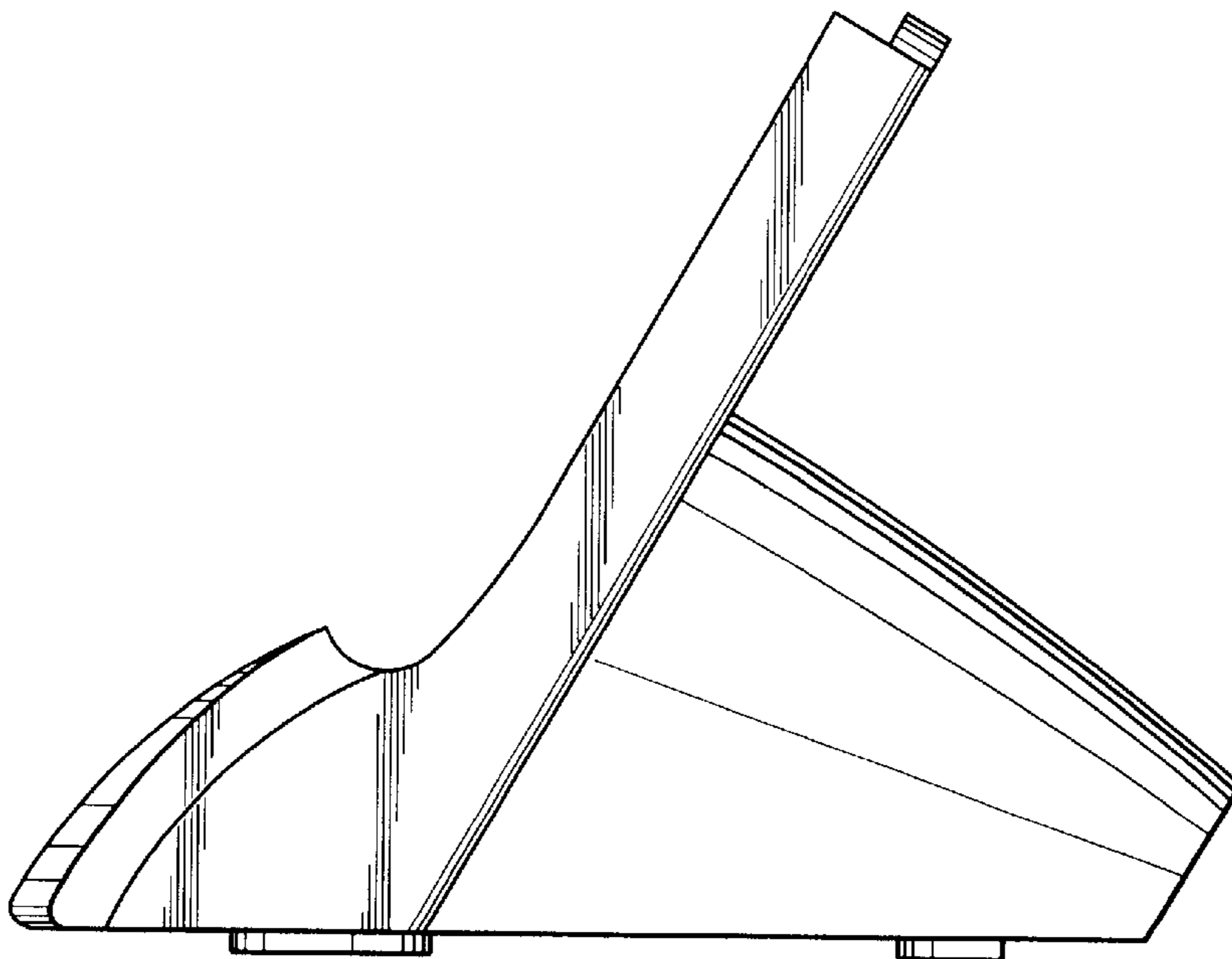


FIG. 5

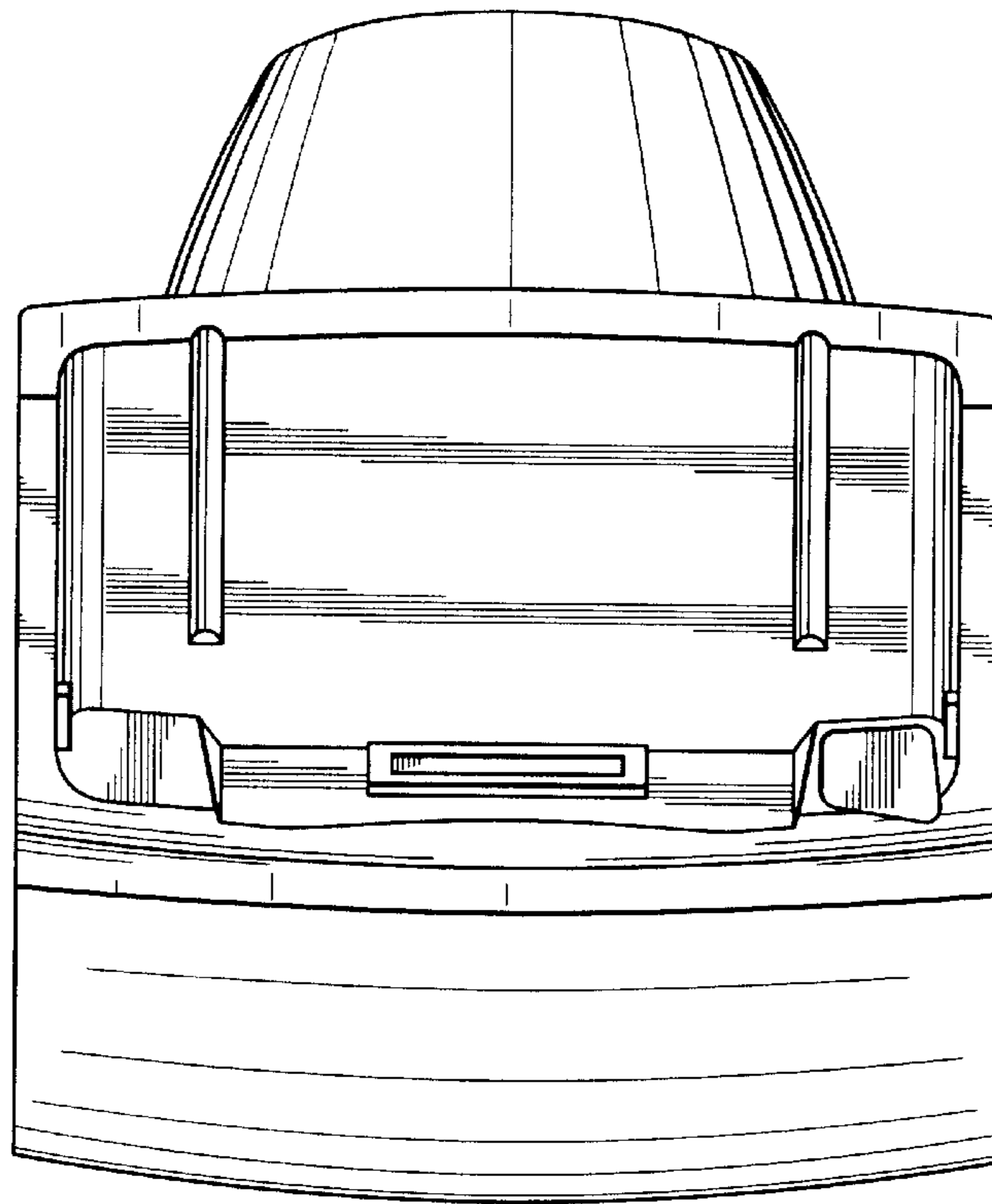


FIG. 6

