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(12) **United States Design Patent**
Tsukuda

(10) **Patent No.:** **US D471,201 S**

(45) **Date of Patent:** **** Mar. 4, 2003**

(54) **DATA INPUT AND OUTPUT DEVICE**

D433,018 S * 10/2000 Tanimura et al. D14/346
D458,931 S * 6/2002 Yu D14/398

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* cited by examiner

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(**) Term: **14 Years**

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(21) Appl. No.: **29/160,823**

(57) **CLAIM**

(22) Filed: **May 17, 2002**

The ornamental design for a data input and output device, as shown and described.

(30) **Foreign Application Priority Data**

Nov. 19, 2001 (JP) 2001-033828

DESCRIPTION

(51) **LOC (7) Cl.** **14-02**

FIG. 1 is a perspective view of the front, bottom and right side of a data input and output device showing my new design;

(52) **U.S. Cl.** **D14/455; D14/397; D14/456**

FIG. 2 is a front view thereof;

(58) **Field of Search** D14/391–399,

FIG. 3 is a rear view thereof;

D14/455, 456, 247; D18/1, 2, 4, 7; 178/17 A,

FIG. 4 is a left side view thereof;

17 C; 200/5 A, 5 R, 6 A, 6 R; 345/168;

FIG. 5 is a right side view thereof;

341/22, 23; 364/708.1; 400/484, 485, 492

FIG. 6 is a top plan view thereof; and,

(56) **References Cited**

FIG. 7 is a bottom view thereof.

U.S. PATENT DOCUMENTS

The elements shown in phantom lines do not form any part of the claimed design.

D366,256 S * 1/1996 Landry et al. D14/392

D376,356 S * 12/1996 Chung D14/392

D401,925 S * 12/1998 McSweeney et al. D14/392

1 Claim, 5 Drawing Sheets



FIG. 1



FIG. 2

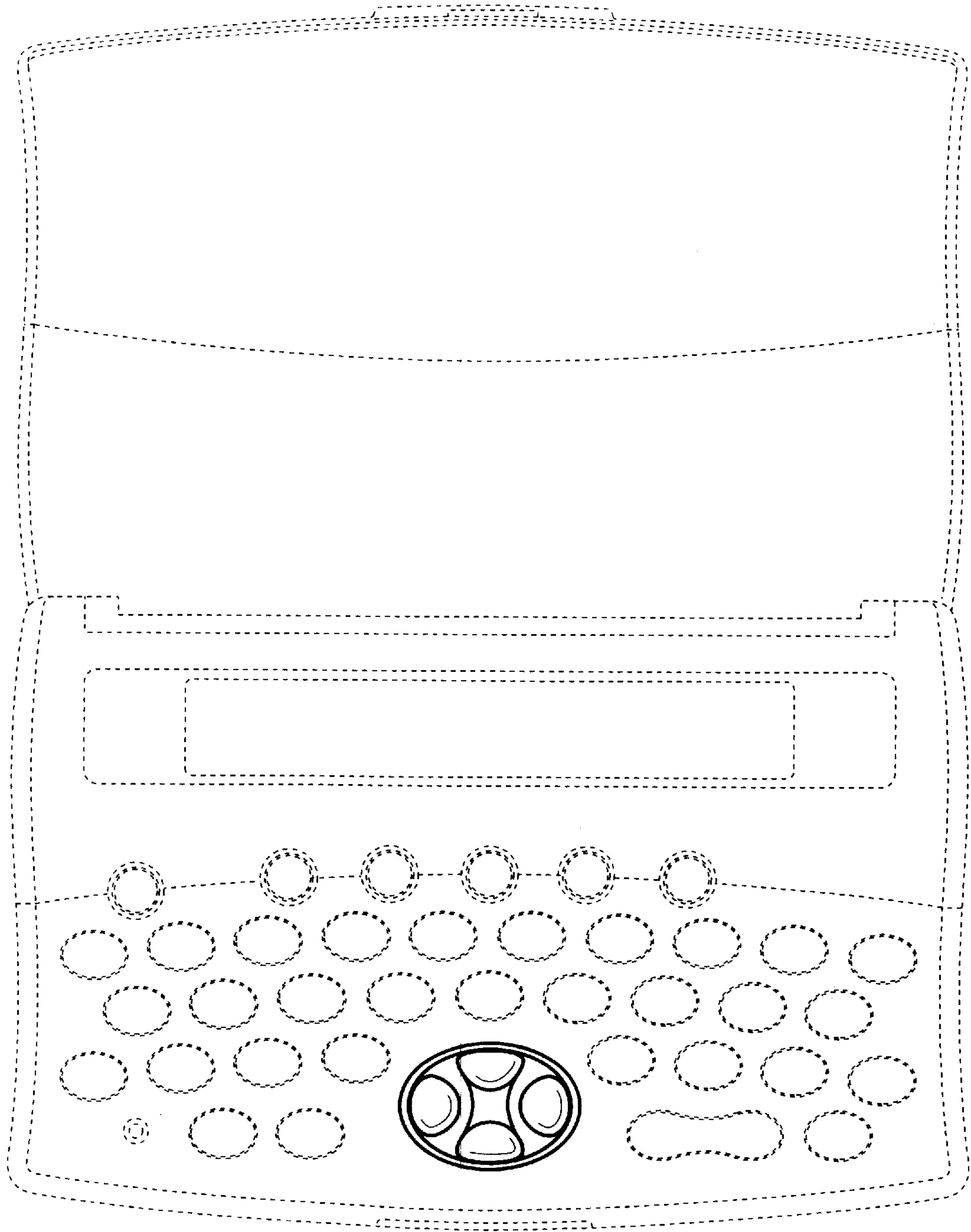


FIG. 3

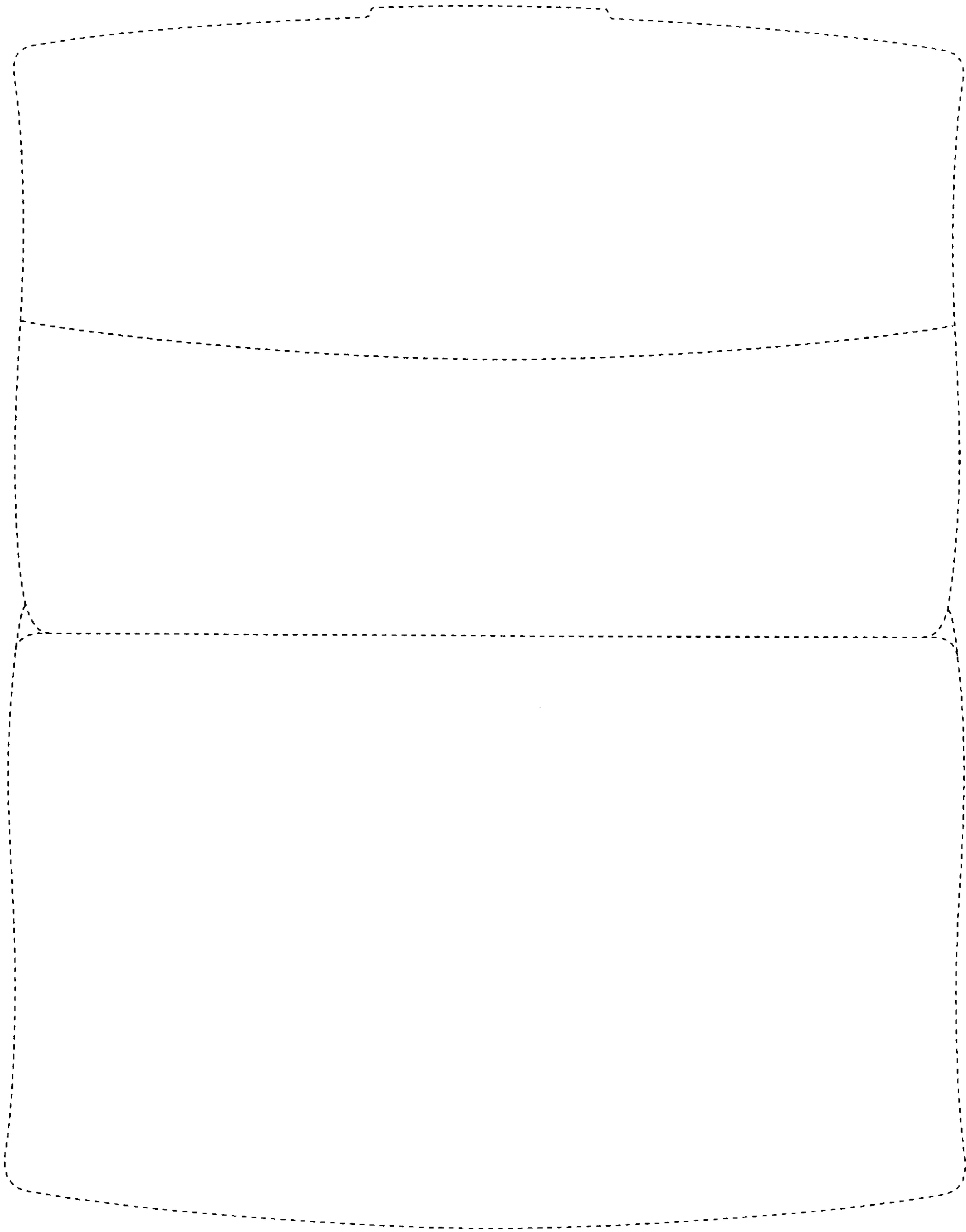


FIG. 4

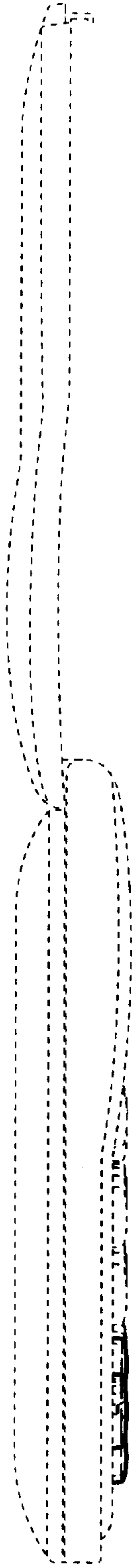


FIG. 5



FIG. 6

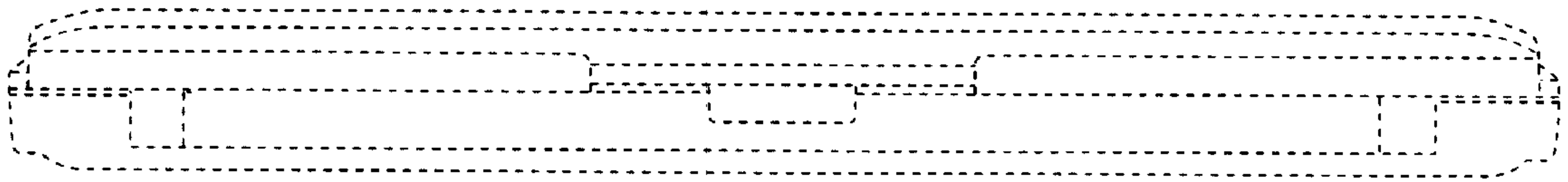


FIG. 7

