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(12) **United States Design Patent** (10) **Patent No.:** **US D471,168 S**  
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(54) **HEAT DISSIPATING RF ELECTRONICS HOUSING**

(75) Inventors: **David Ge**, Potomac, MD (US);  
**Mohammed Nusrat Jamal**,  
Germantown, MD (US)

(73) Assignee: **Teletronics International**, Rockville,  
MD (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/152,997**

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(51) **LOC (7) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/184**

(58) **Field of Search** ..... D13/179, 184;  
D14/124, 155, 168, 188, 189; 361/271,  
274.3, 697, 702, 709, 710, 711, 514; 165/180,  
185; 257/706, 707, 718-722; 455/344,  
347, 350, 351

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*Primary Examiner*—James Gandy

*Assistant Examiner*—Selina Sikder

(74) *Attorney, Agent, or Firm*—Cahn & Samuels, LLP

(57) **CLAIM**

The ornamental design for a heat dissipating RF electronics housing, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective photographic view from a first side of the electronics housing according to the invention.

FIG. 2 is a perspective photographic view from the opposite side of the housing according to FIG. 1.

FIG. 3 is a top photographic view of the housing according to FIG. 1.

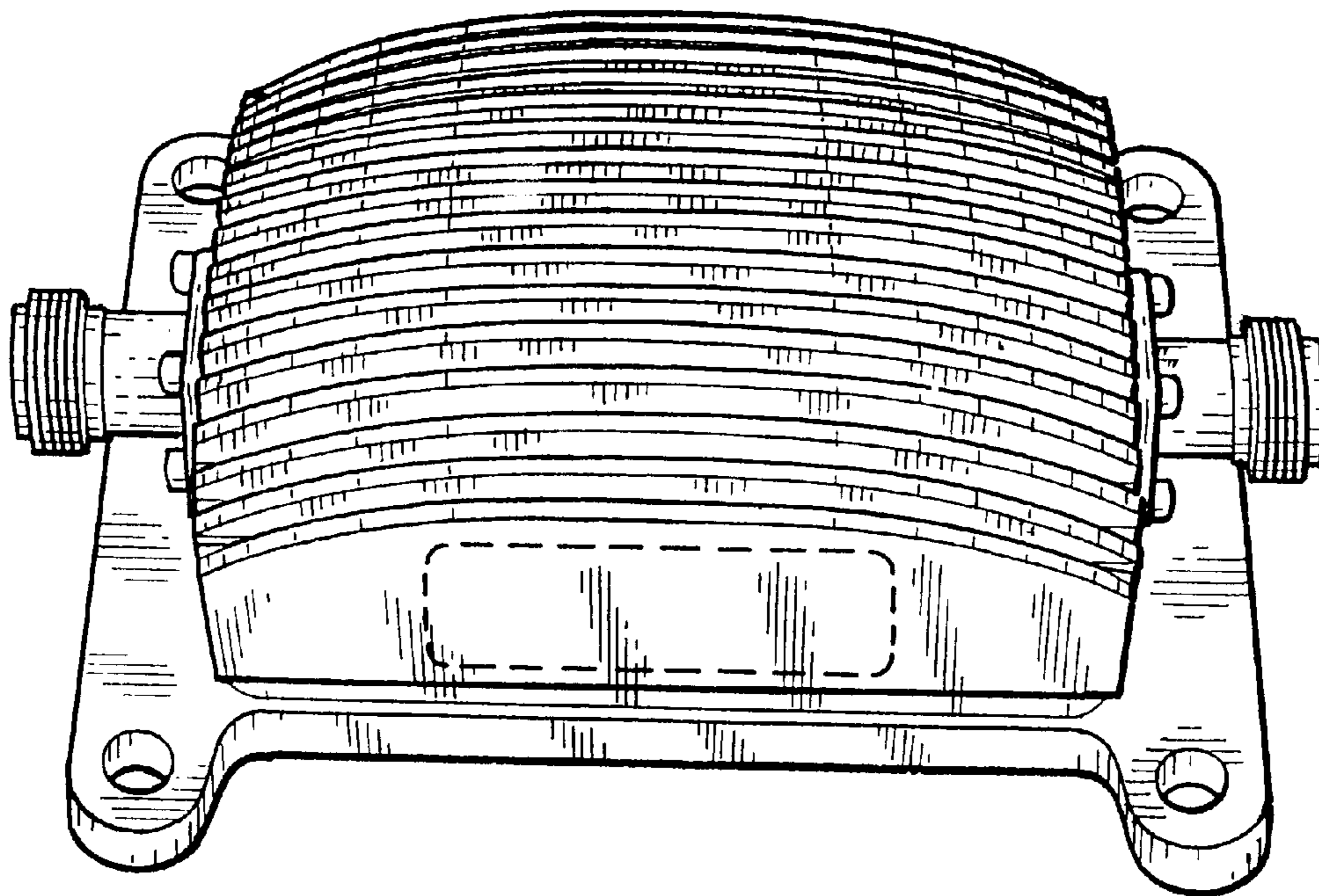
FIG. 4 is a side photographic view of the housing according to FIG. 1.

FIG. 5 is a perspective photographic view from an end of the housing according to FIG. 1.

FIG. 6 is an end view of the housing according to FIG. 5; and,

FIG. 7 is a photographic view of the bottom of the housing according to FIG. 1.

**1 Claim, 3 Drawing Sheets**



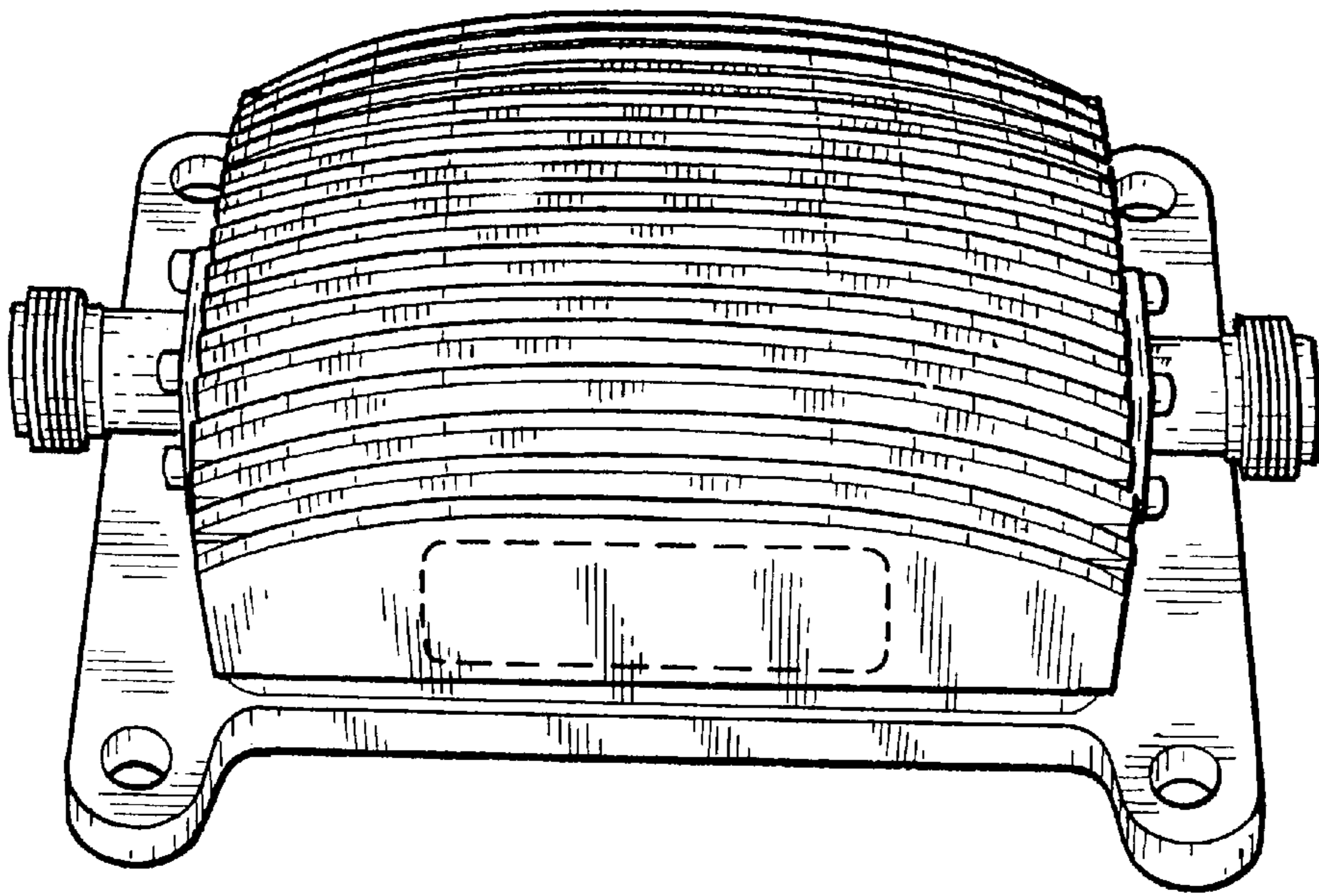


FIG. 1

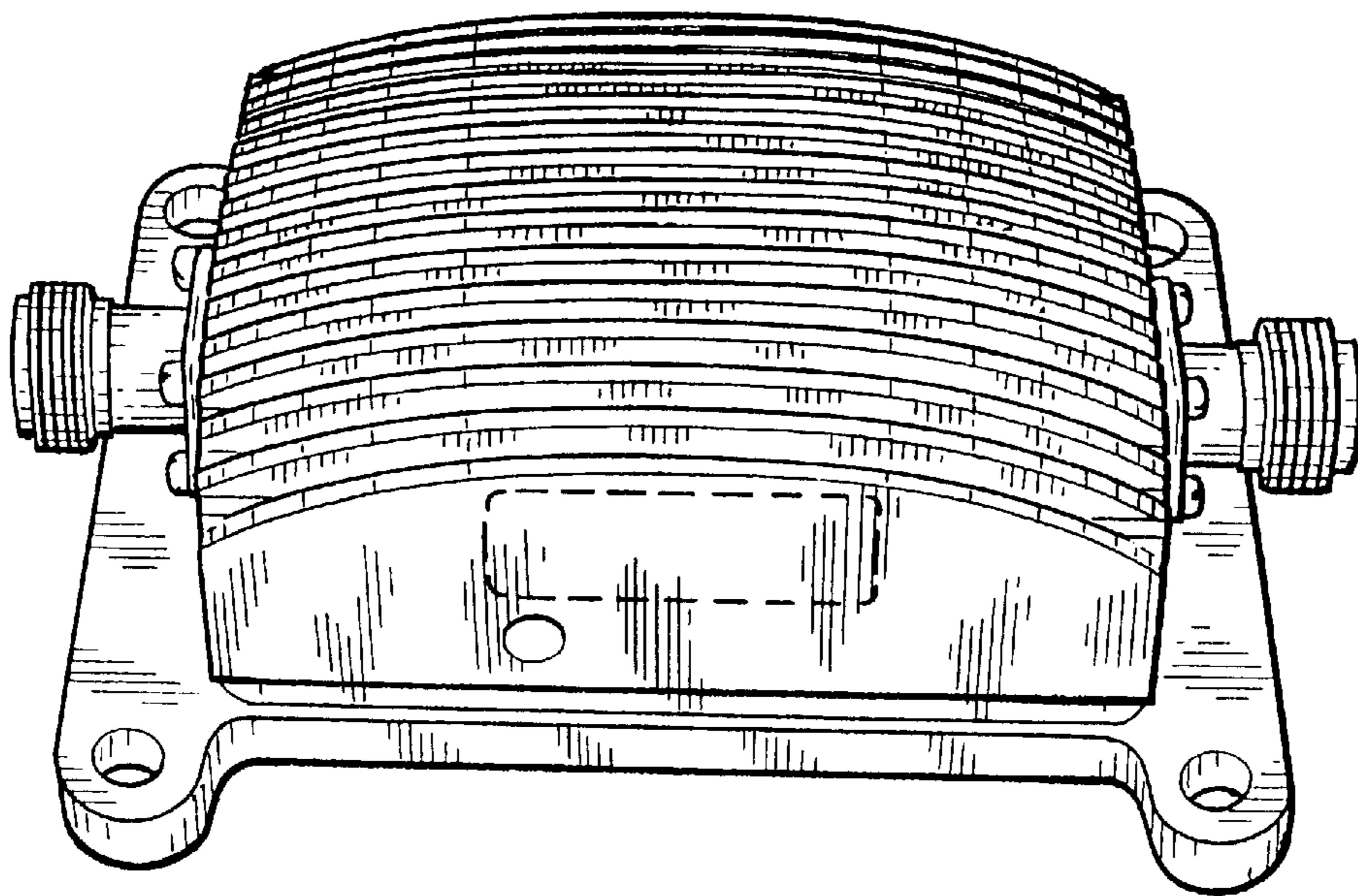


FIG. 2

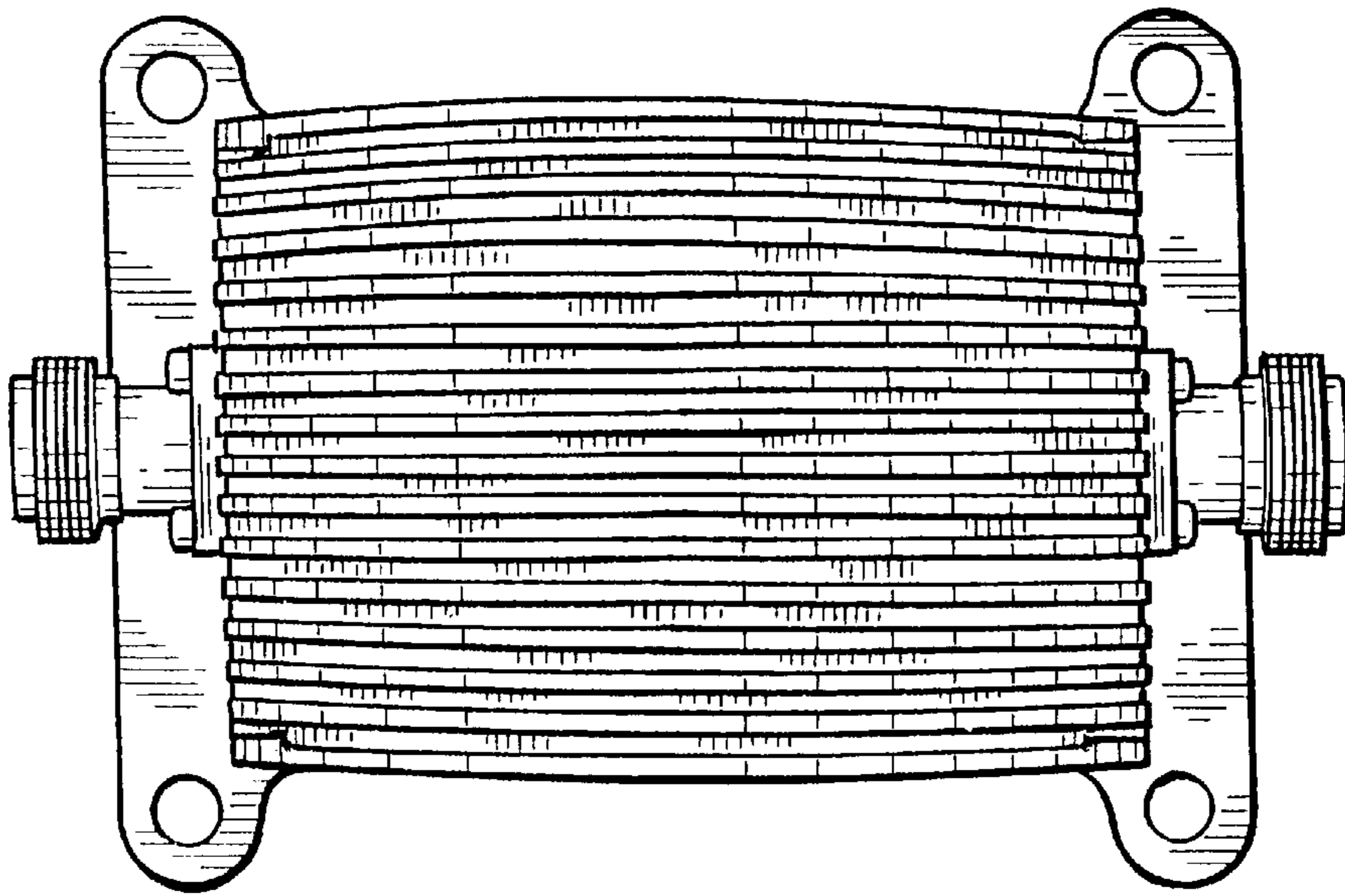


FIG. 3

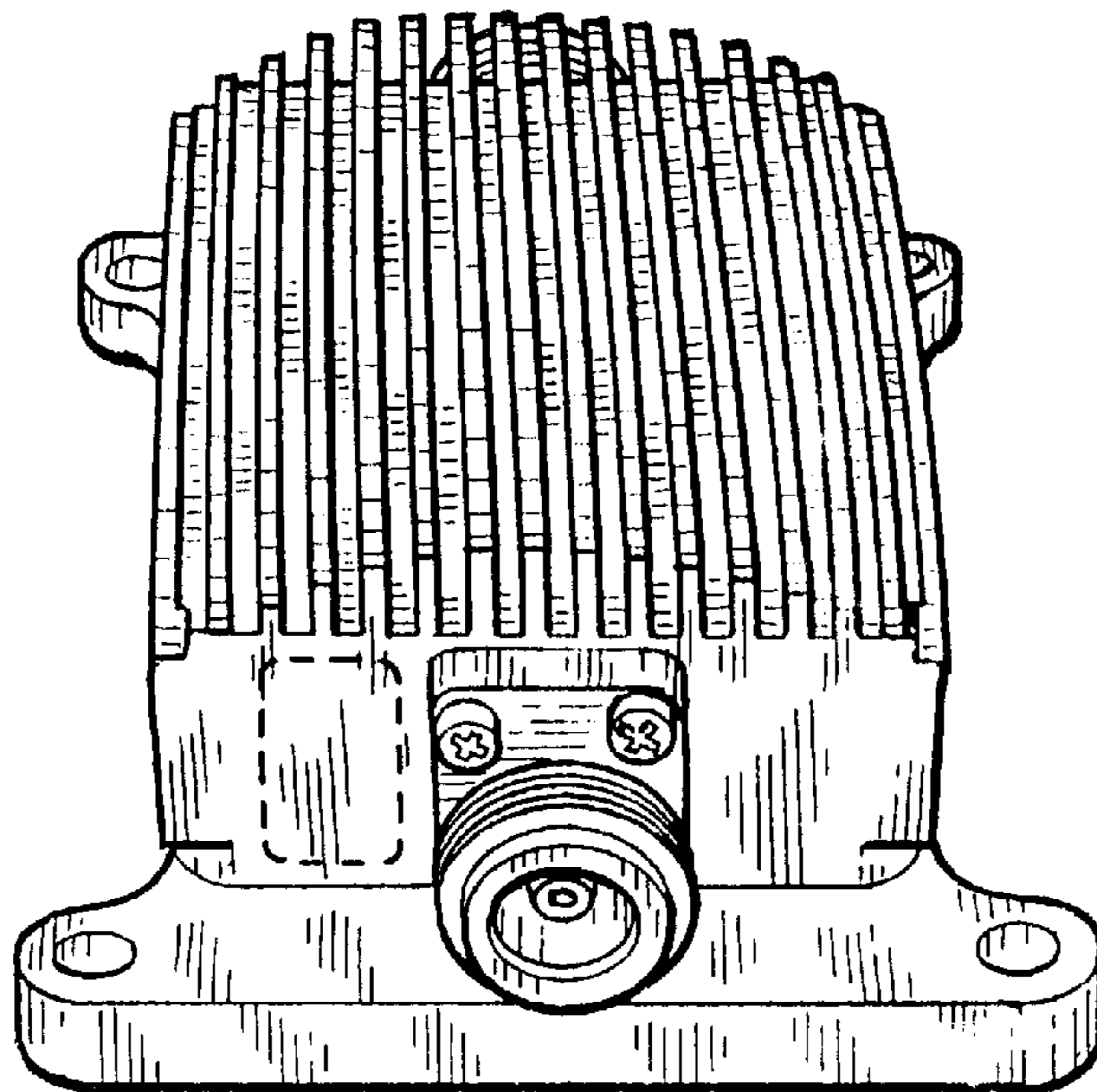


FIG. 5

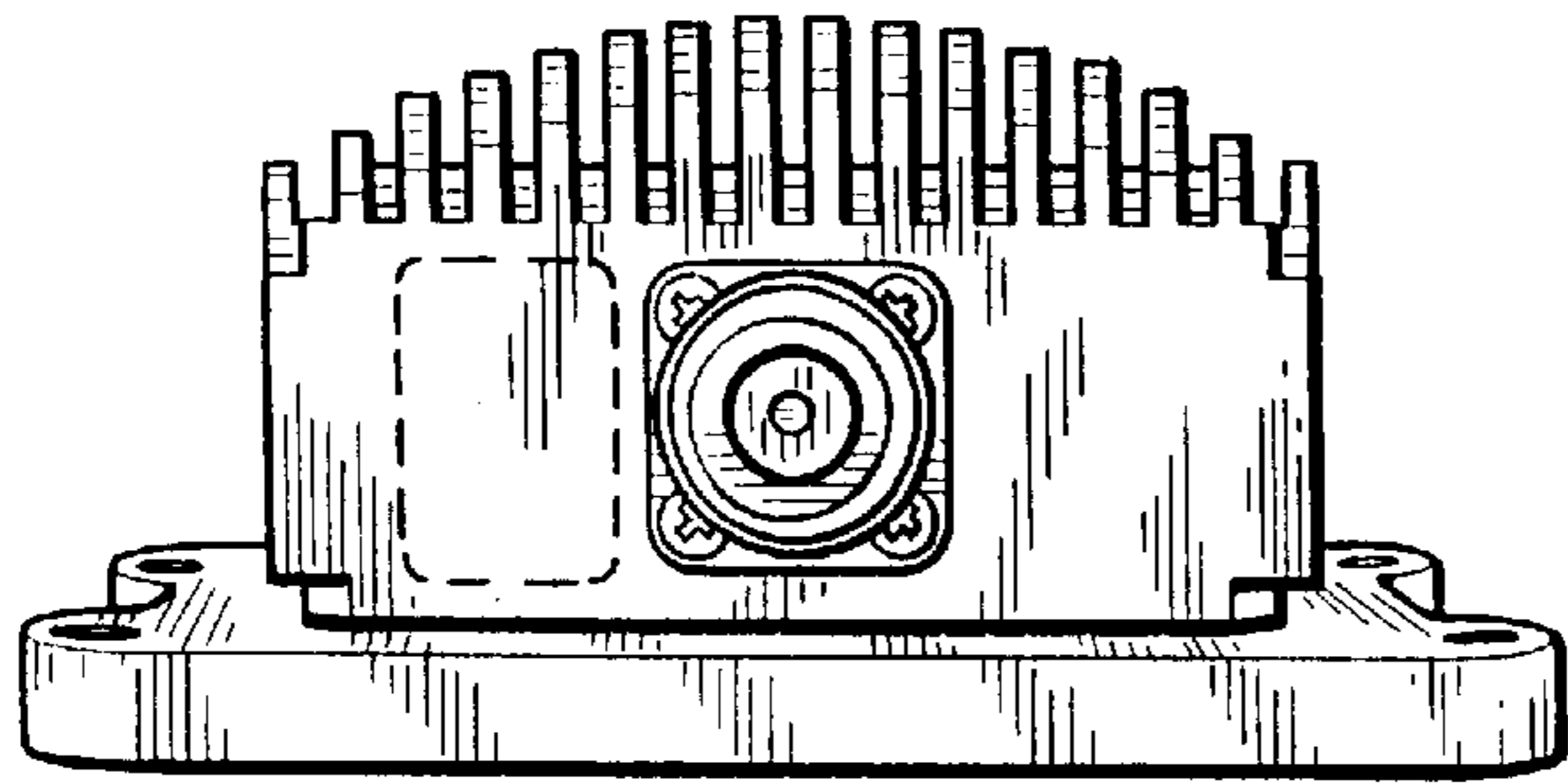


FIG. 6

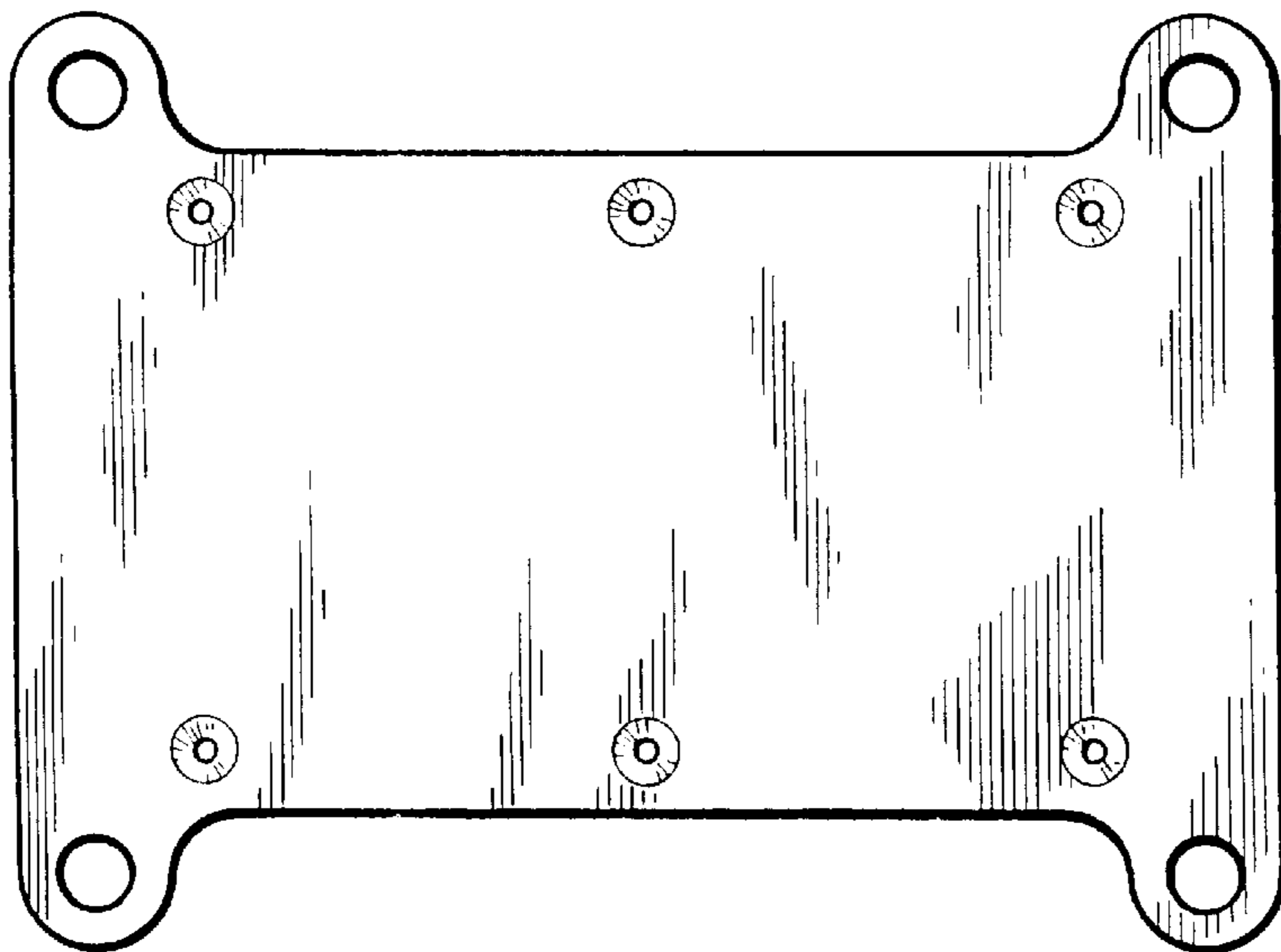


FIG. 7

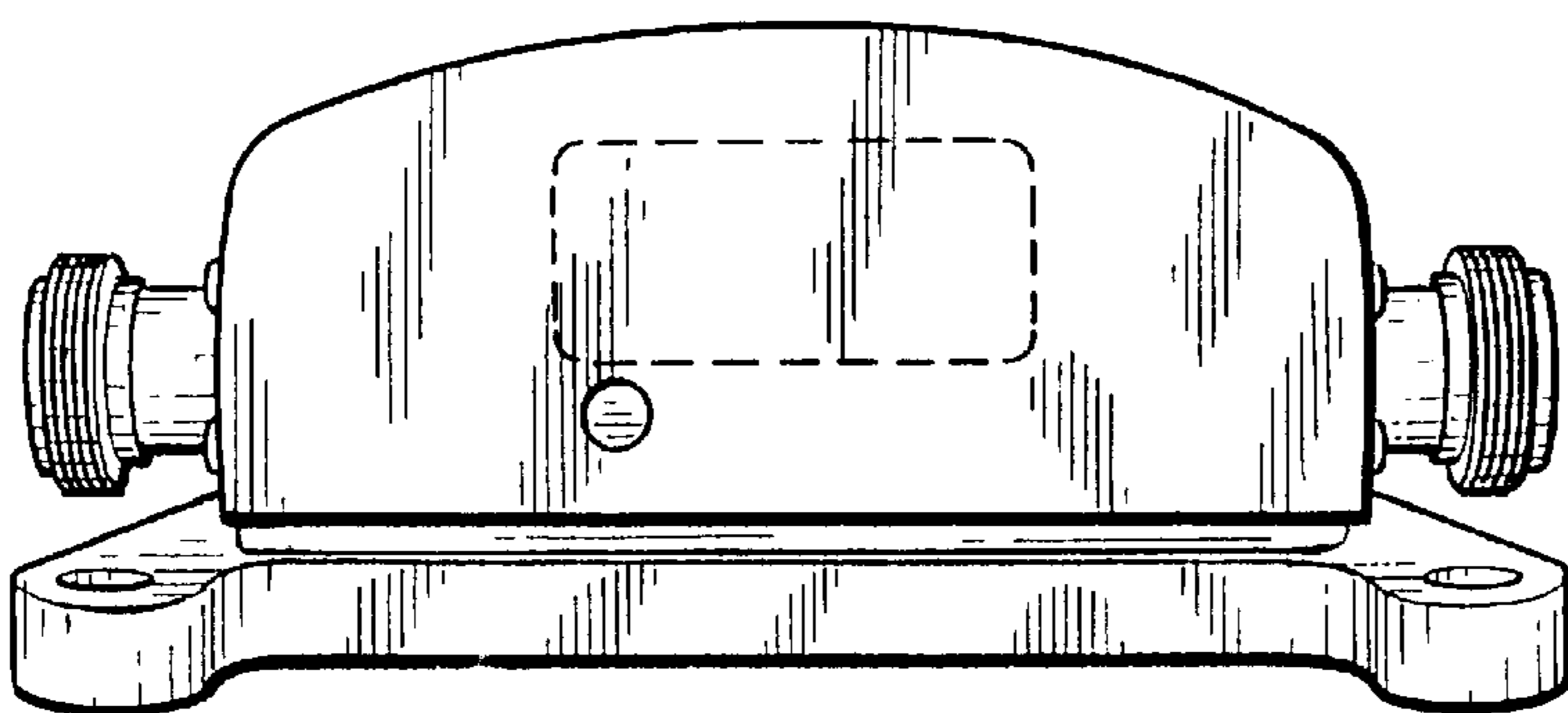


FIG. 4