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(12) **United States Design Patent**  
**Hsu**

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(54) **ELECTRONIC CLOCK**

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(\*\*) **Term:** **14 Years**

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(51) **LOC (7) Cl.** ..... **10-01**

(52) **U.S. Cl.** ..... **D10/18; D10/15**

(58) **Field of Search** ..... D10/1-40, 122-132;  
368/41-44, 82-84, 28-30, 239-242, 276-277,  
280-282, 285

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

4,893,291	A	*	1/1990	Bick et al.	.....	368/10
D337,950	S	*	8/1993	Davis	.....	D10/15
D434,070	S	*	11/2000	Lin et al.	.....	D19/21
D436,621	S	*	1/2001	Chu et al.	.....	D19/21

D437,351	S	*	2/2001	Chang et al.	.....	D19/21
D438,900	S	*	3/2001	Chu et al.	.....	D19/21
D444,072	S	*	6/2001	Chan	.....	D10/3
D449,004	S	*	10/2001	Li	.....	D10/2
D457,070	S	*	5/2002	Fong	.....	D10/10

\* cited by examiner

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Services

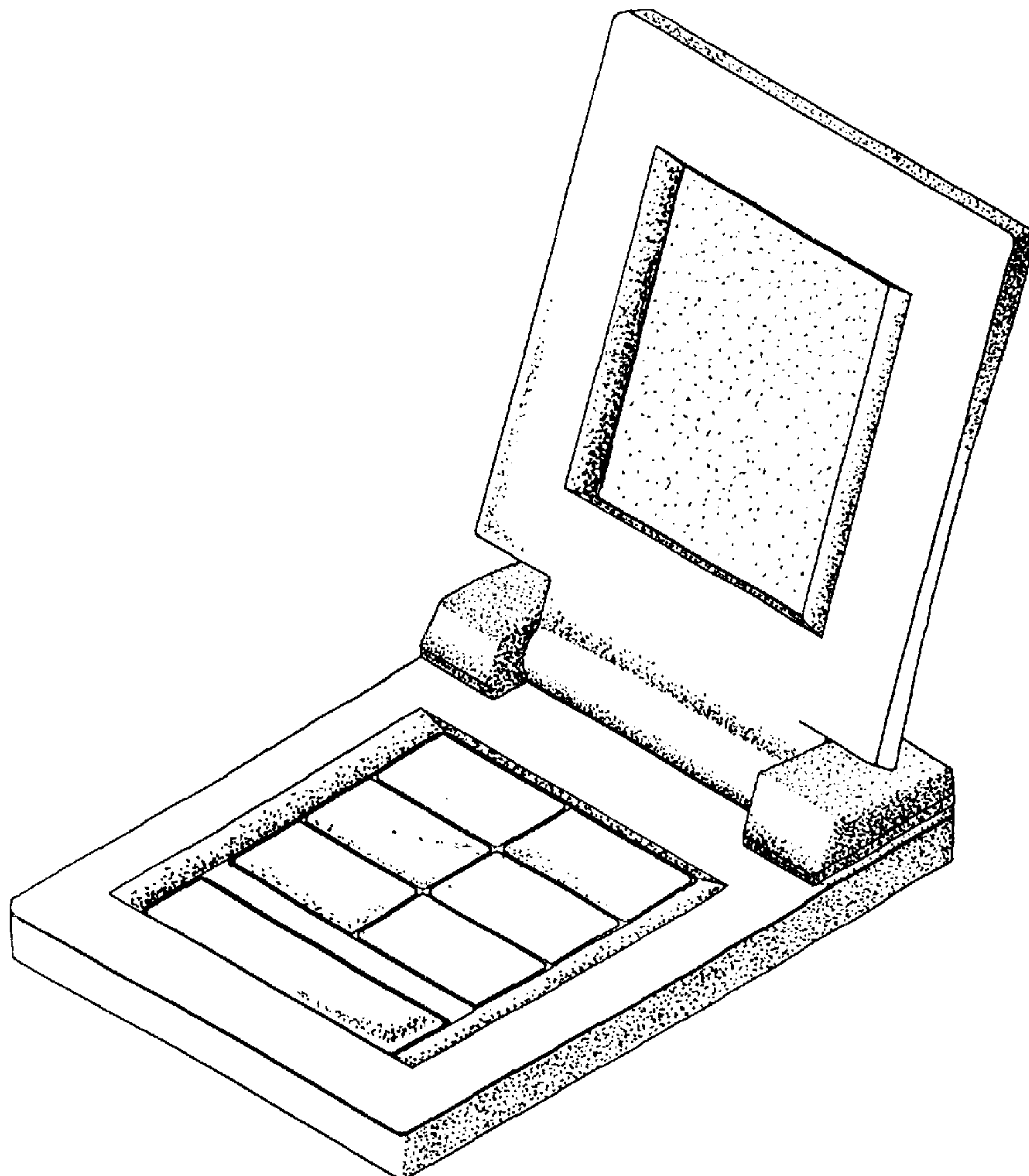
(57) **CLAIM**

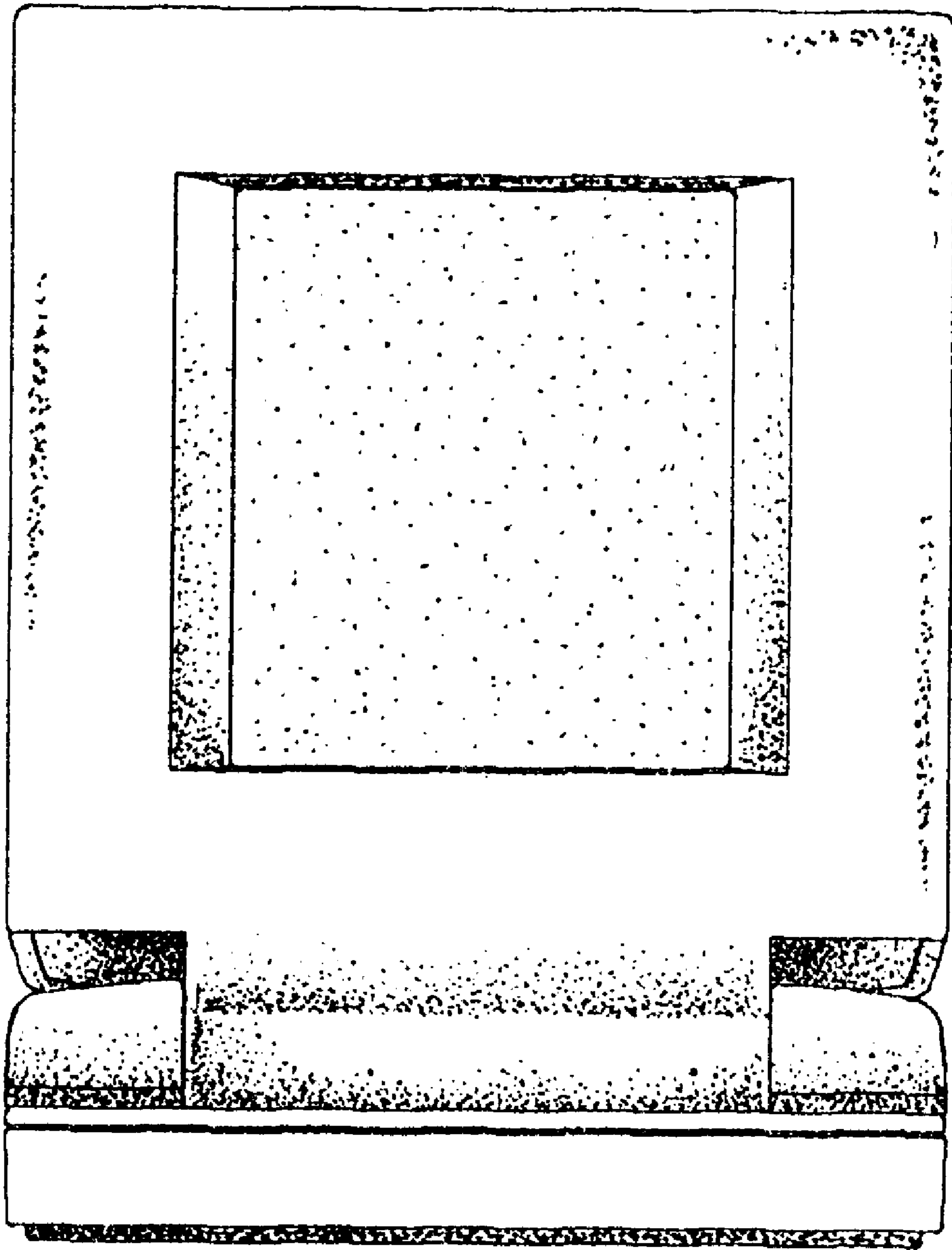
The ornamental design for an electronic clock for, as shown.

**DESCRIPTION**

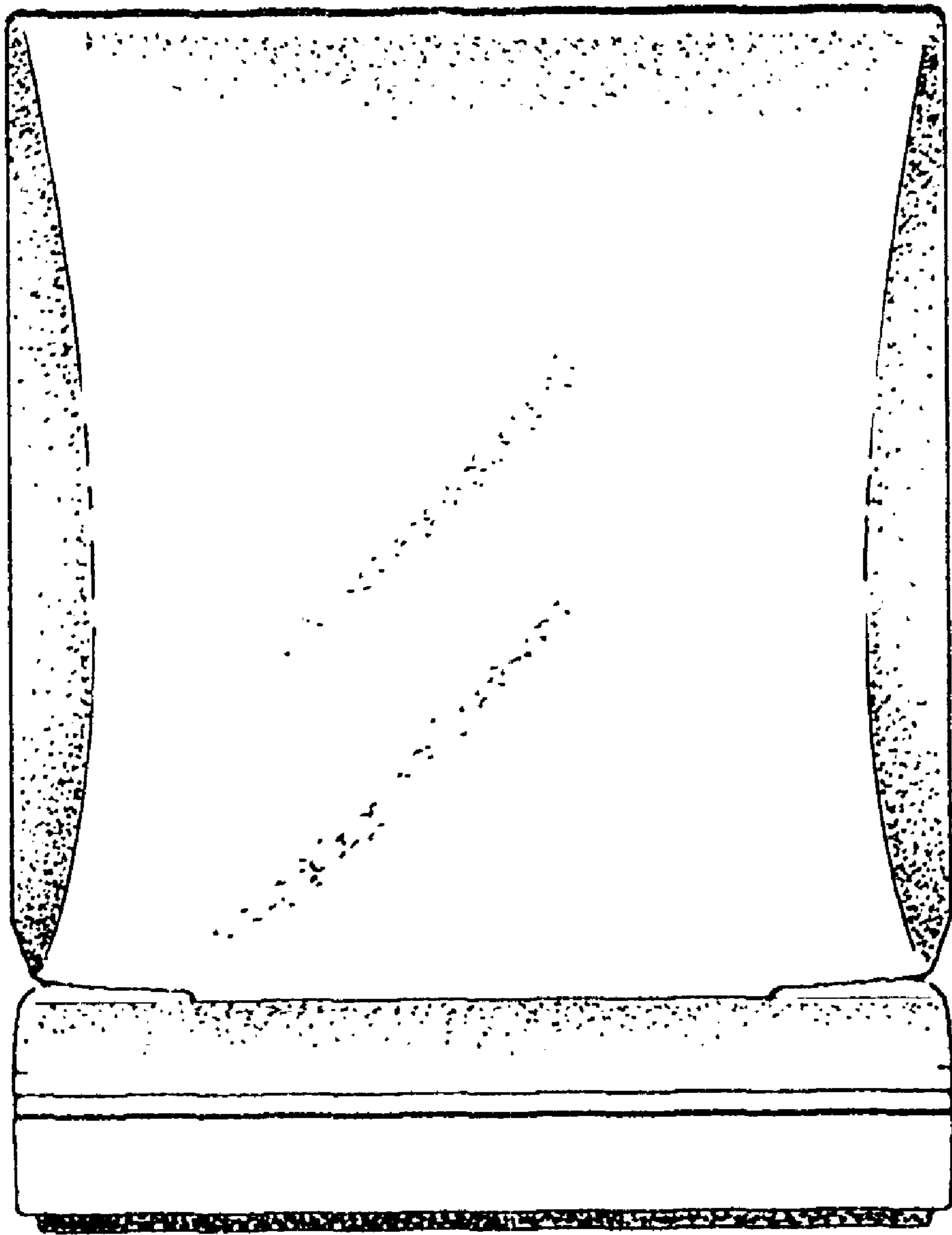
FIG. 1 is a front view of the present design;  
FIG. 2 is a back view of the present design;  
FIG. 3 is a left side view of the present design;  
FIG. 4 is a right side view of the present design;  
FIG. 5 is a top view of the present design;  
FIG. 6 is a bottom view of the present design; and,  
FIG. 7 is a stereographic view of the present design.

**1 Claim, 7 Drawing Sheets**

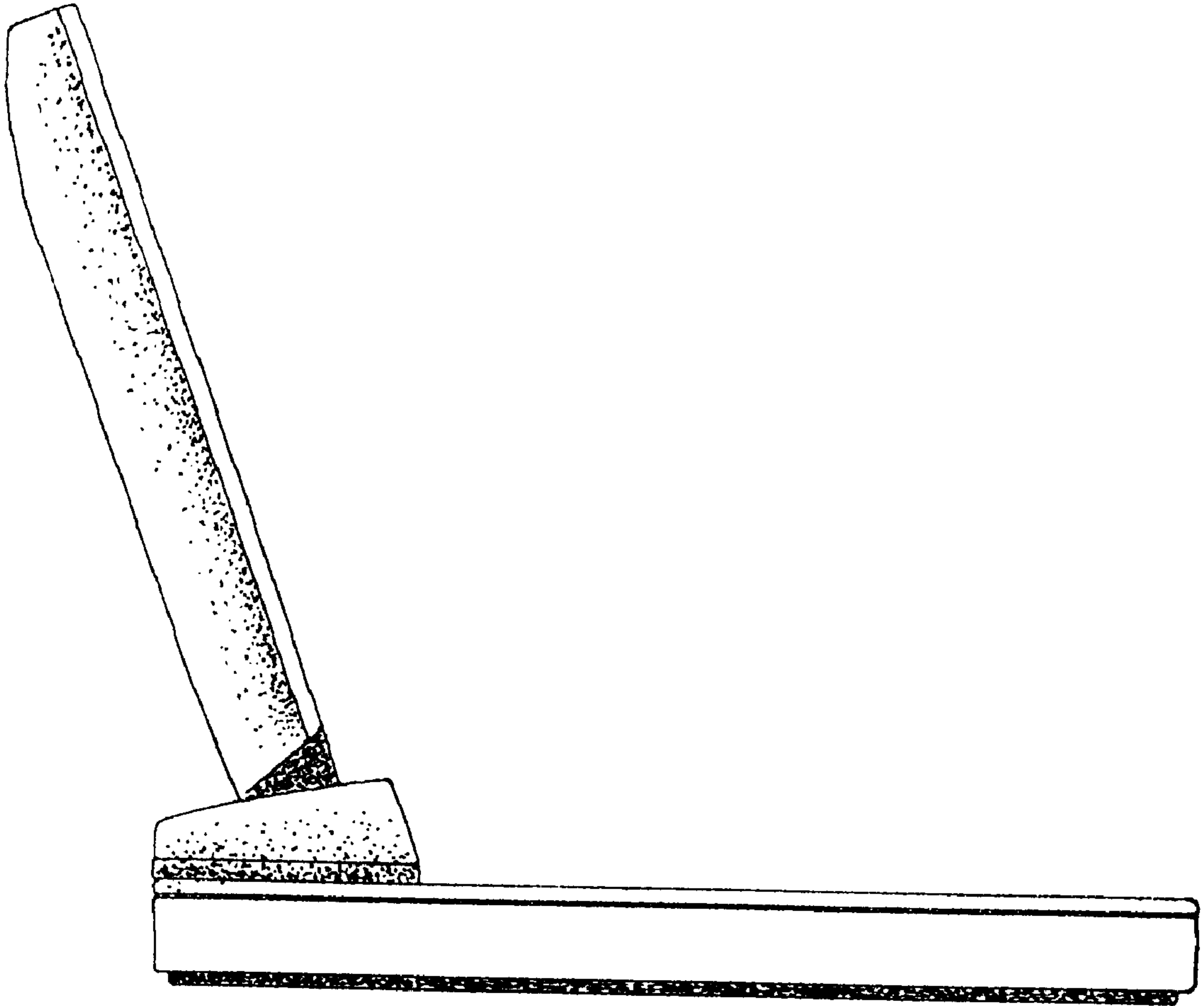




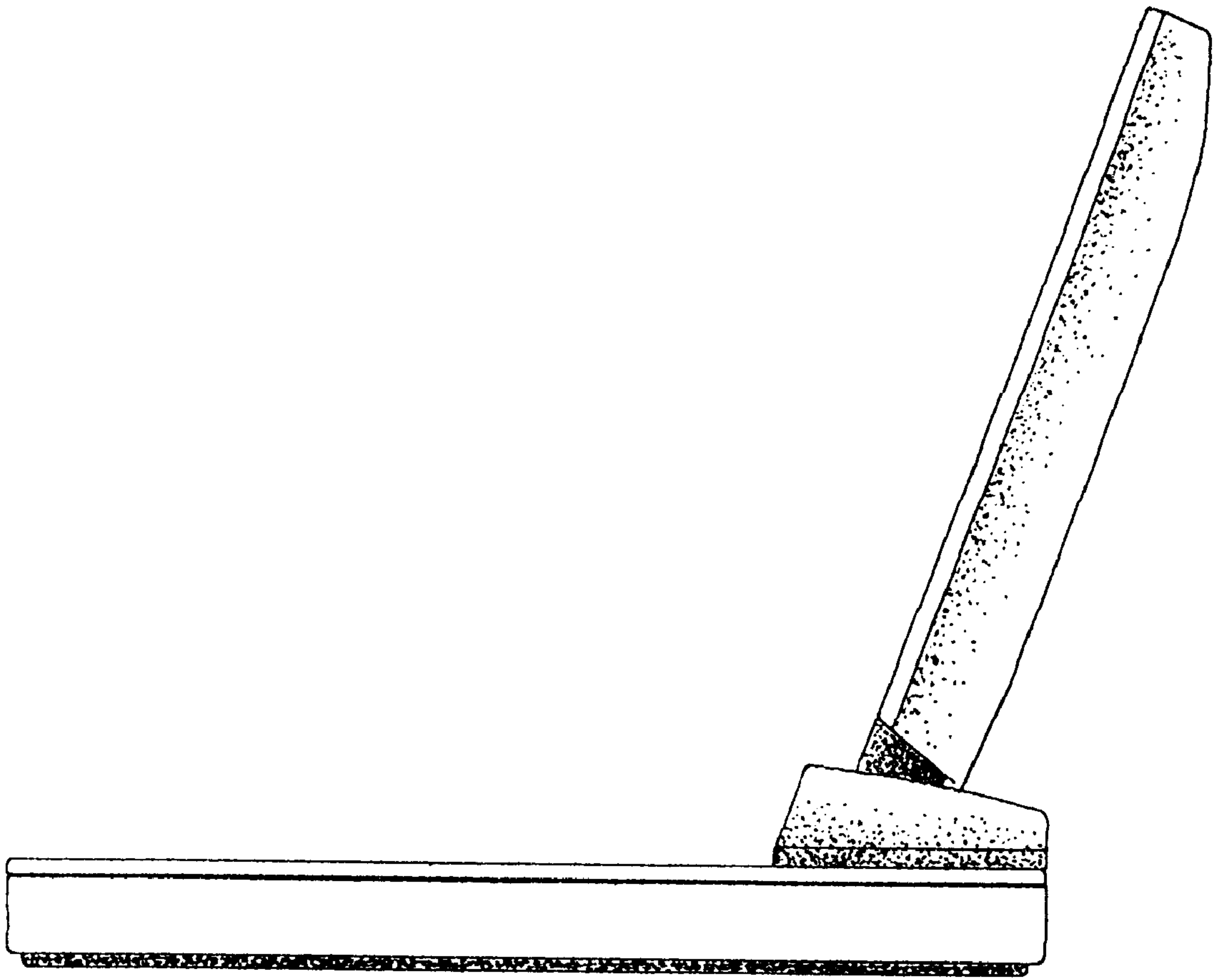
**FIG. 1**



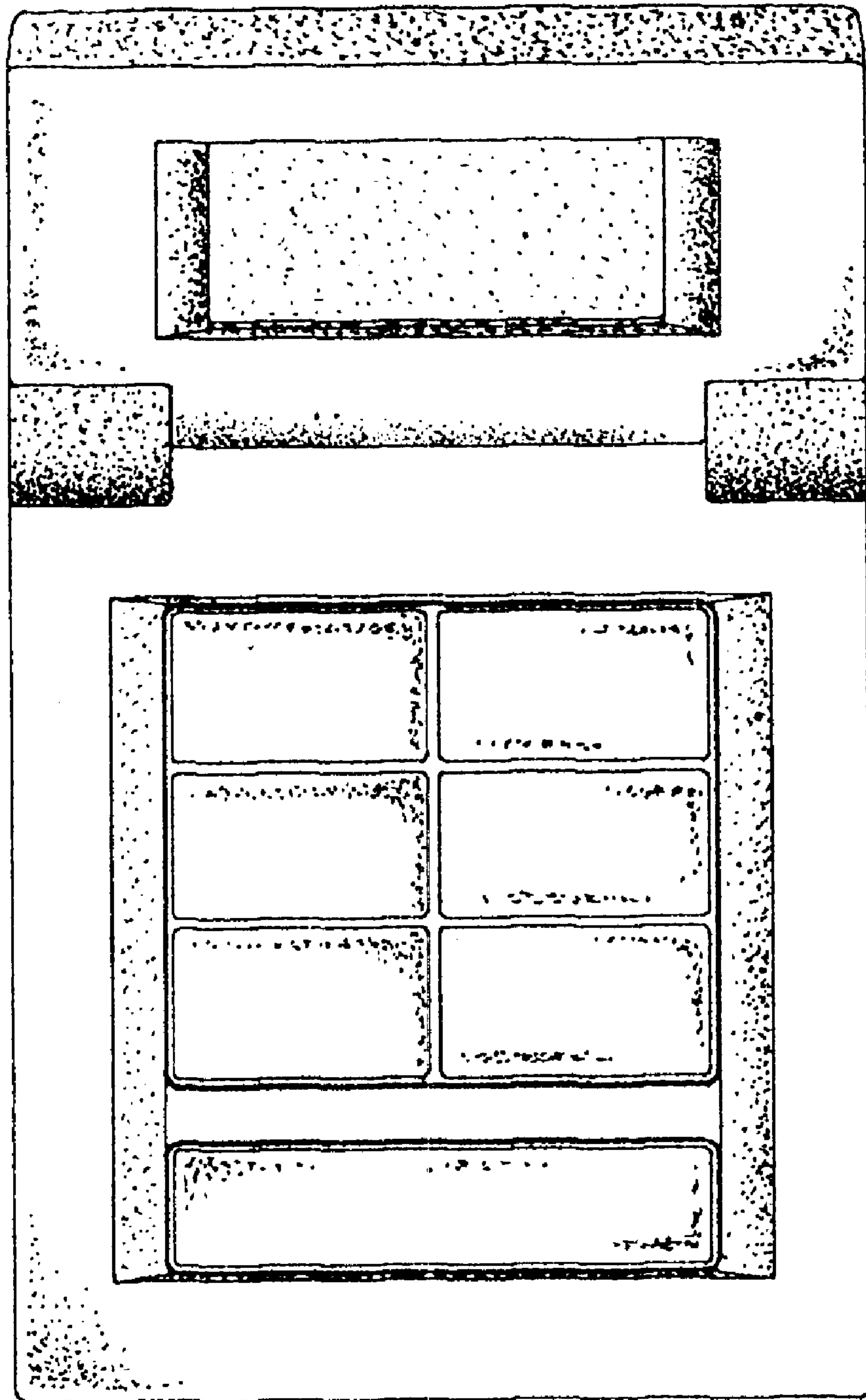
**FIG. 2**



**FIG. 3**

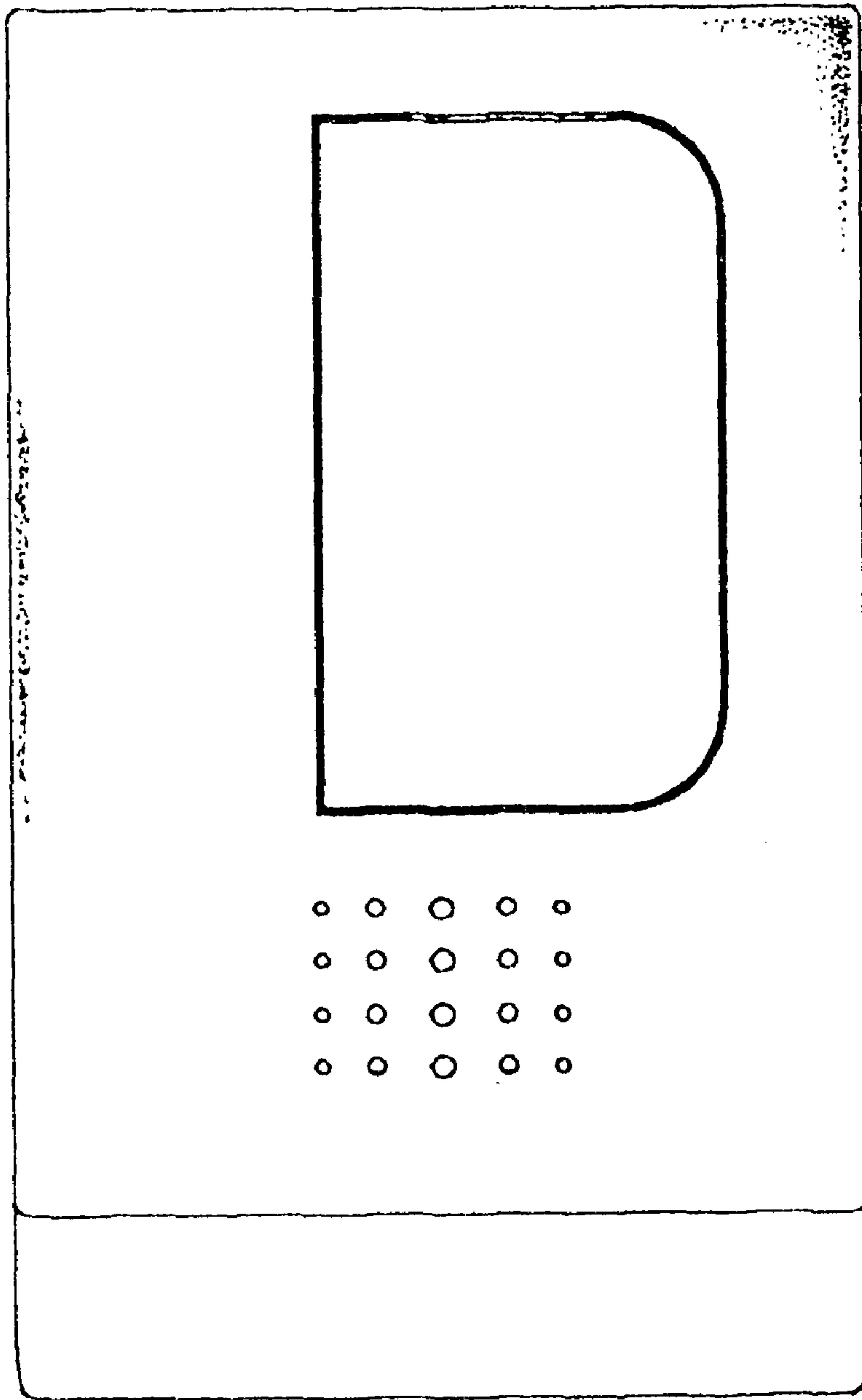


**FIG. 4**

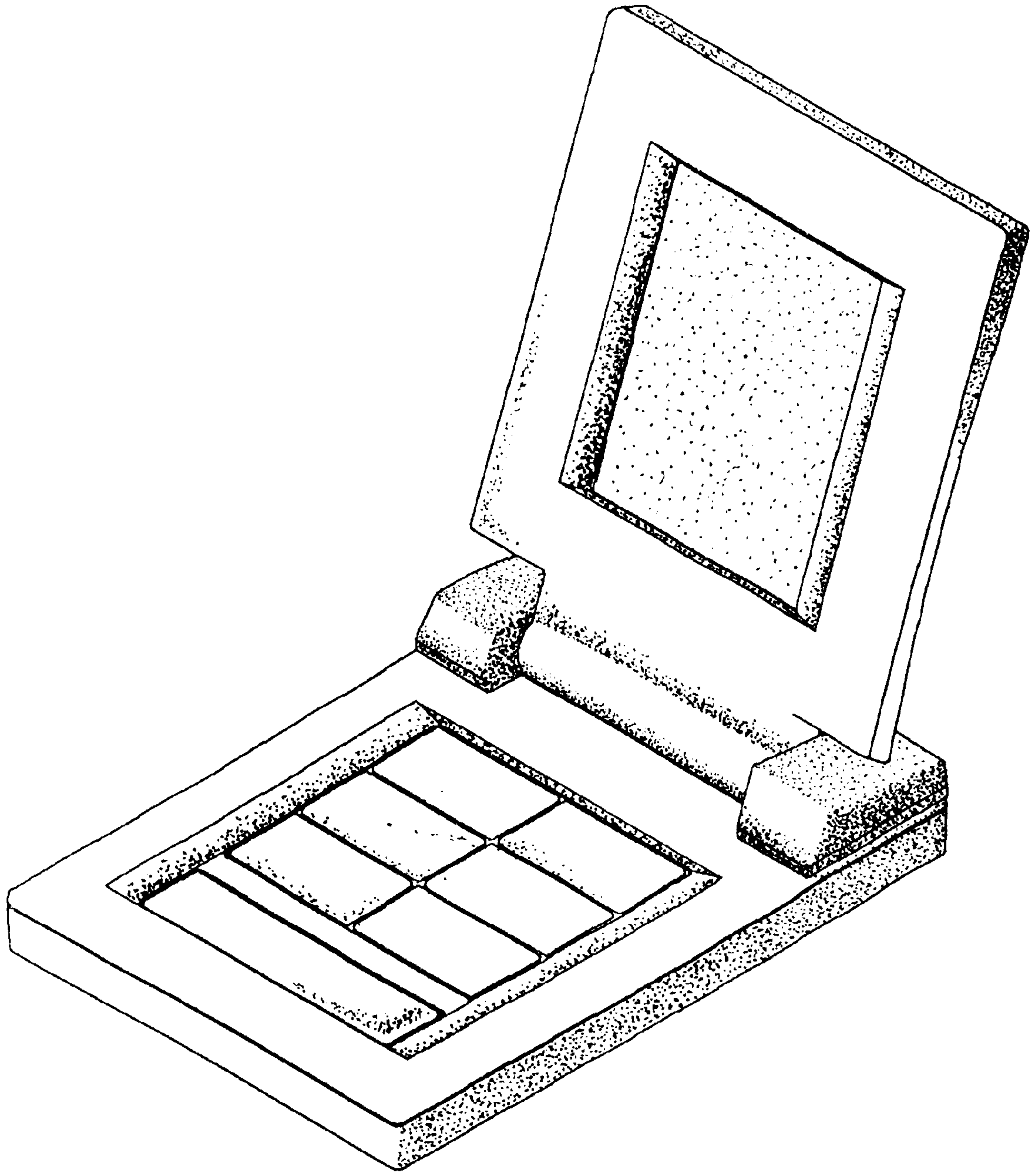


**FIG. 5**





**FIG. 6**



**FIG. 7**