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(12) **United States Design Patent** (10) **Patent No.:** **US D465,465 S**  
**Chapman et al.** (45) **Date of Patent:** **\*\* Nov. 12, 2002**

(54) **ELECTRONICS ENCLOSURE**

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(\*\*) Term: **14 Years**

(21) Appl. No.: **29/155,749**

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(51) **LOC (7) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/184**

(58) **Field of Search** ..... D10/75, 78, 80,  
D10/103; D14/240, 353, 354, 355; D13/184;  
73/431; 174/50, 51, 52.1; 206/702, 703,  
706, 707, 710, 713, 718, 719, 722; 361/600-678,  
679, 683, 685, 692, 709, 724, 730, 735,  
736, 752, 763, 728; 439/76.1

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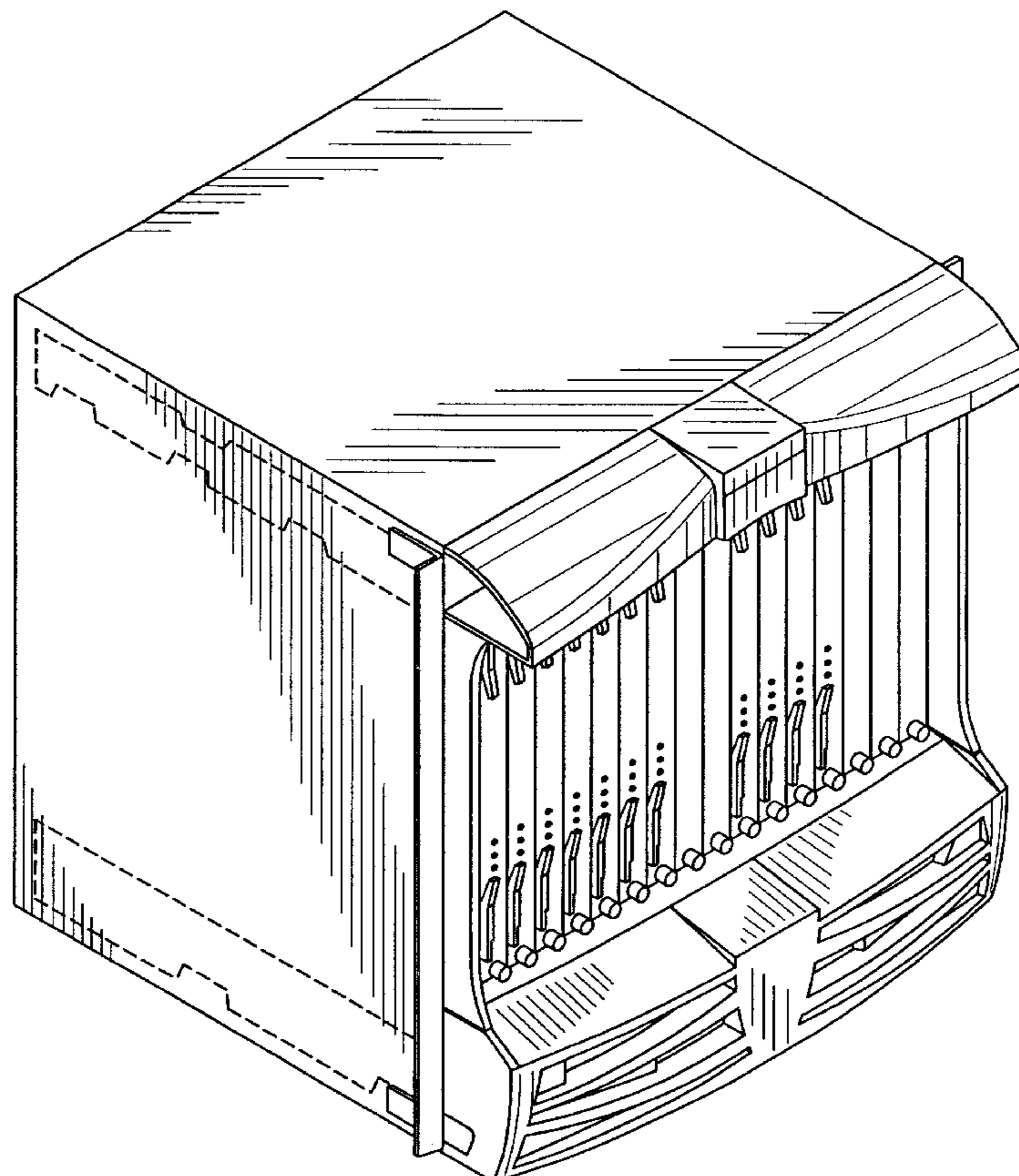
(57) **CLAIM**

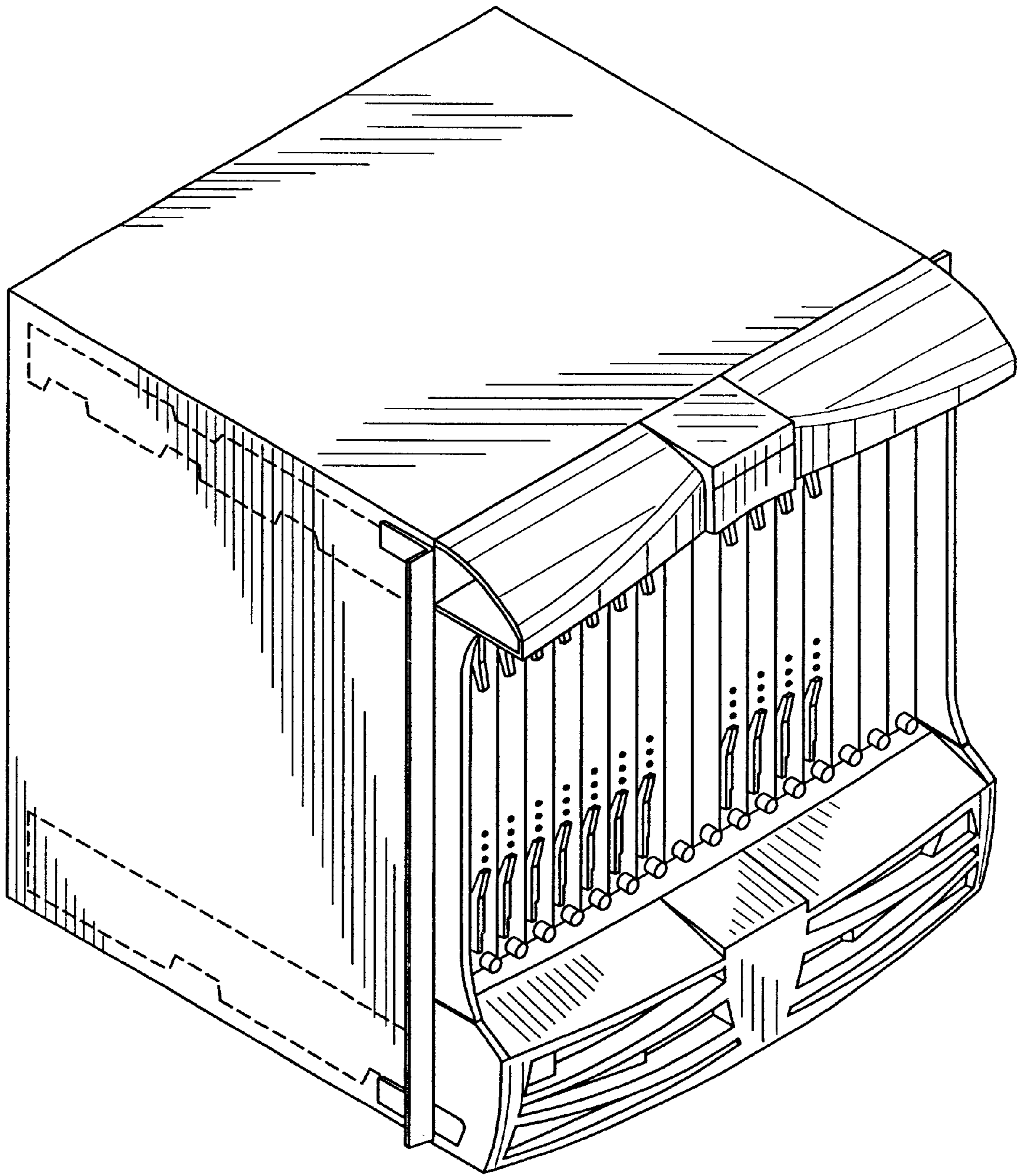
The ornamental design for an electronics enclosure, as shown and described.

**DESCRIPTION**

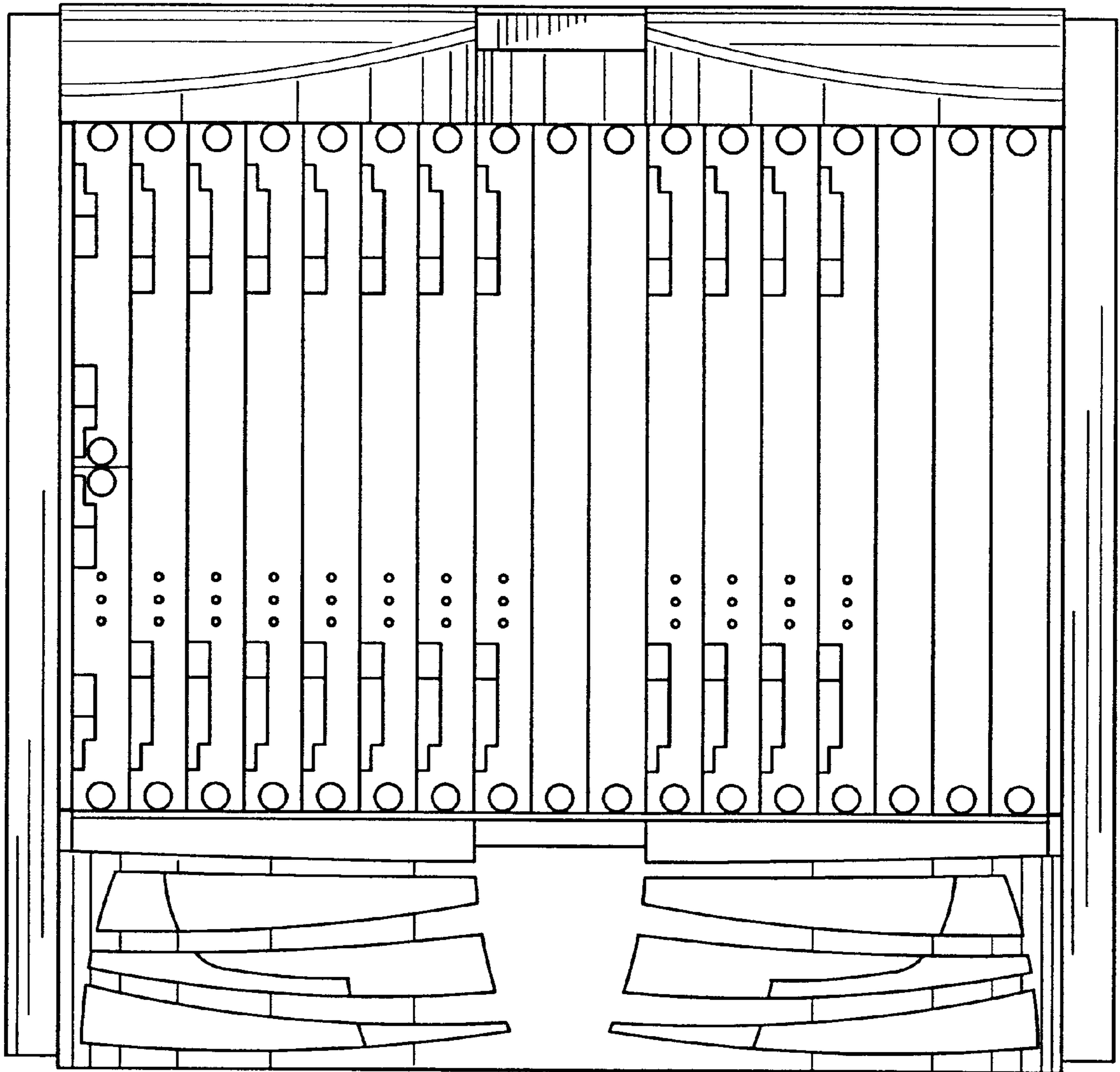
FIG. 1 is a perspective view of the electronics enclosure;  
FIG. 2 is a front view of the electronics enclosure of FIG. 1;  
FIG. 3 is a right side view of the electronics enclosure of  
FIG. 1;  
FIG. 4 is a left side view of the electronics enclosure of FIG.  
1; and,  
FIG. 5 is a top view of the electronics enclosure of FIG. 1.  
The broken line showing of the environment is for illustrative  
purpose only and forms not part of the claimed design.

**1 Claim, 5 Drawing Sheets**

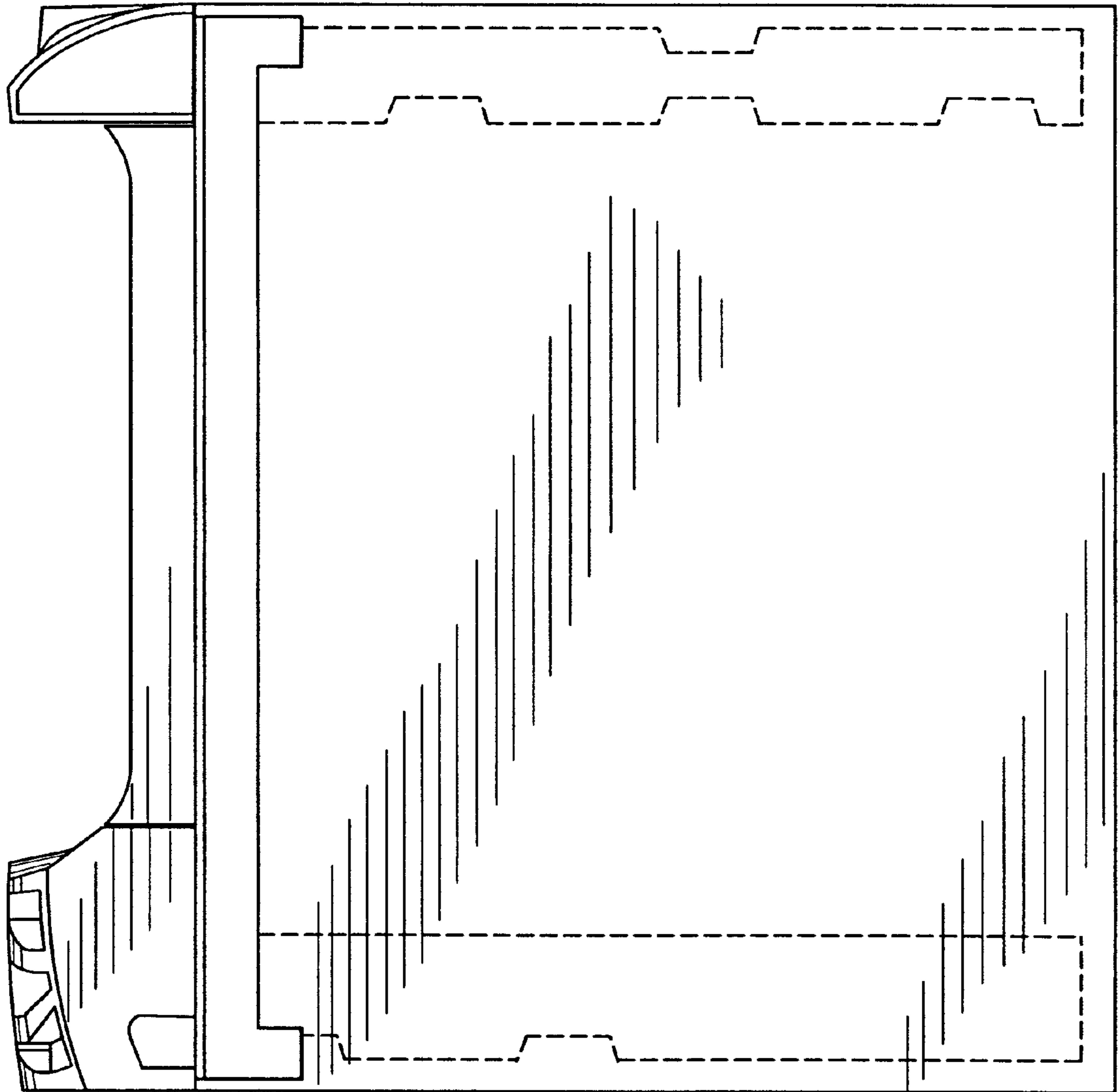




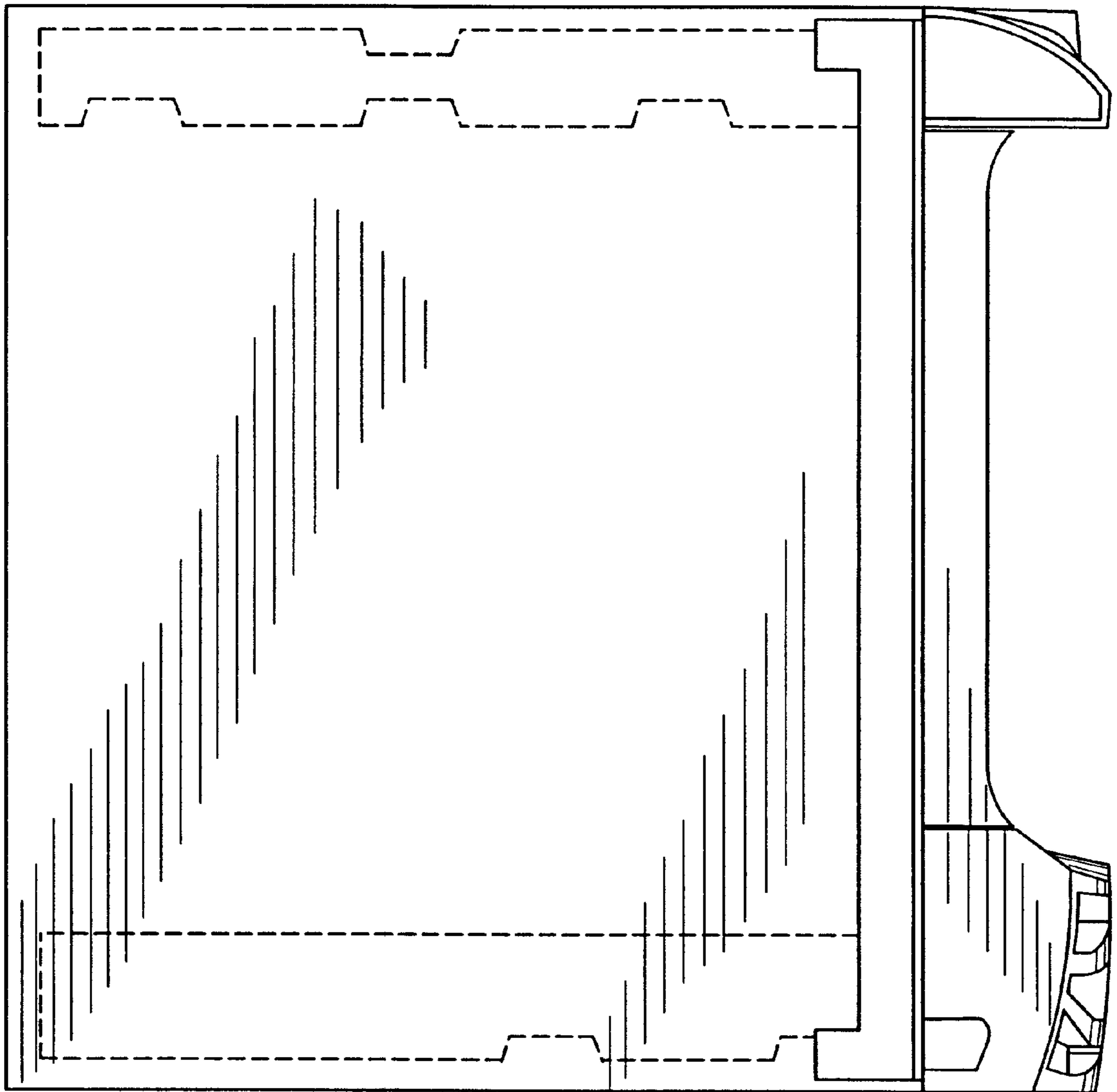
**FIG. 1**



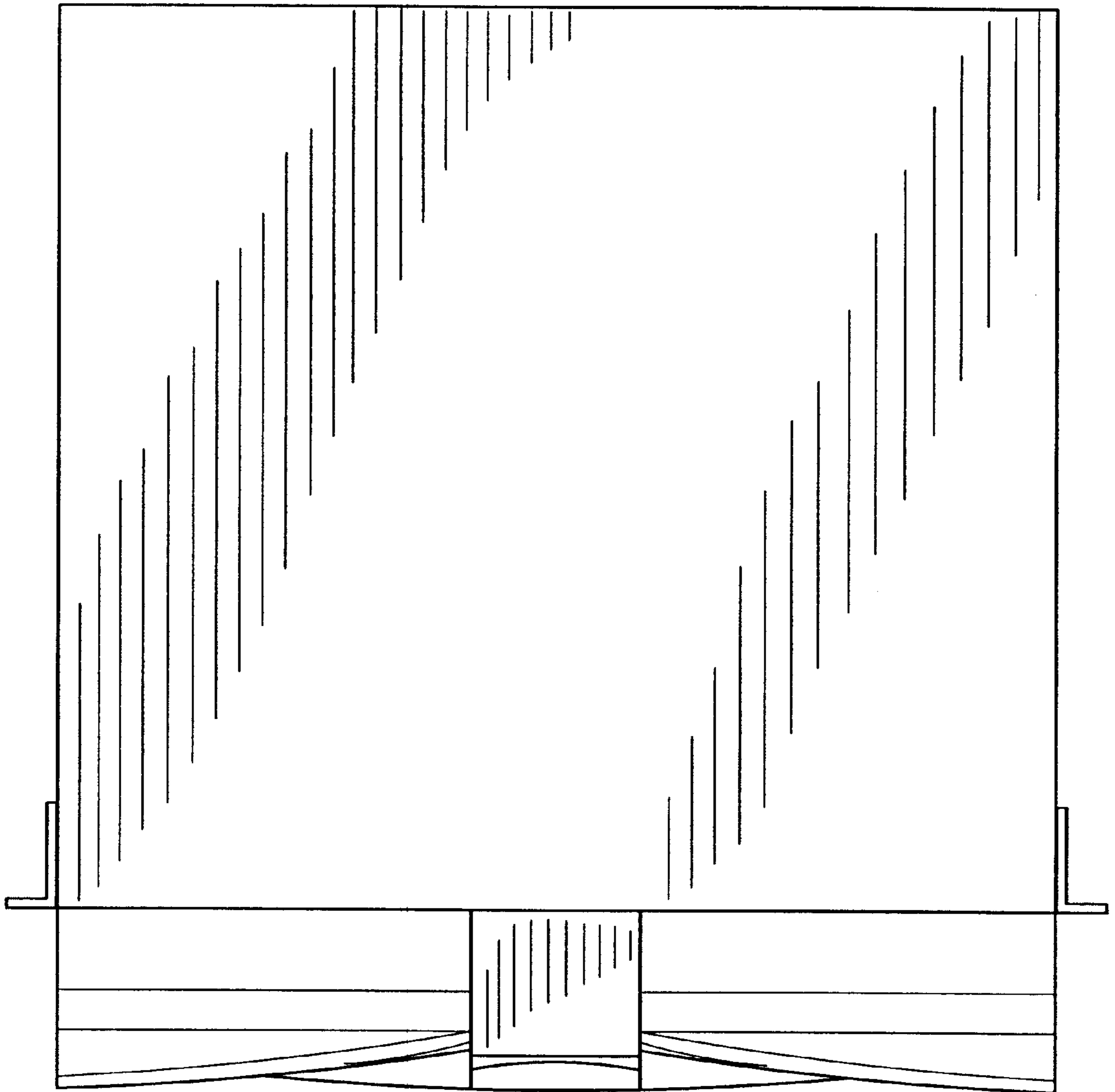
**FIG. 2**



**FIG. 3**



**FIG. 4**



***FIG. 5***