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(12) **United States Design Patent**
Duryea

(10) **Patent No.:** **US D465,200 S**

(45) **Date of Patent:** **** Nov. 5, 2002**

(54) **PROGRAMMABLE POWER STRIP**

D446,187 S * 8/2001 Lam et al. D13/137.2
D450,036 S * 11/2001 Okamoto D13/137.2

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* cited by examiner

(**) Term: **14 Years**

Primary Examiner—Robert M. Spear

(21) Appl. No.: **29/154,635**

(57) **CLAIM**

(22) Filed: **Jan. 25, 2002**

The ornamental design for a programmable power strip, as shown and described.

(51) **LOC (7) Cl.** **13-03**

DESCRIPTION

(52) **U.S. Cl.** **D13/137.3**

(58) **Field of Search** D13/123, 133,
D13/137.1–137.4, 139.1–139.8; 174/48,
49, 53–57; 191/12 R, 12.4; 361/614; 439/373,
528, 535, 536, 538, 650; 307/140–143

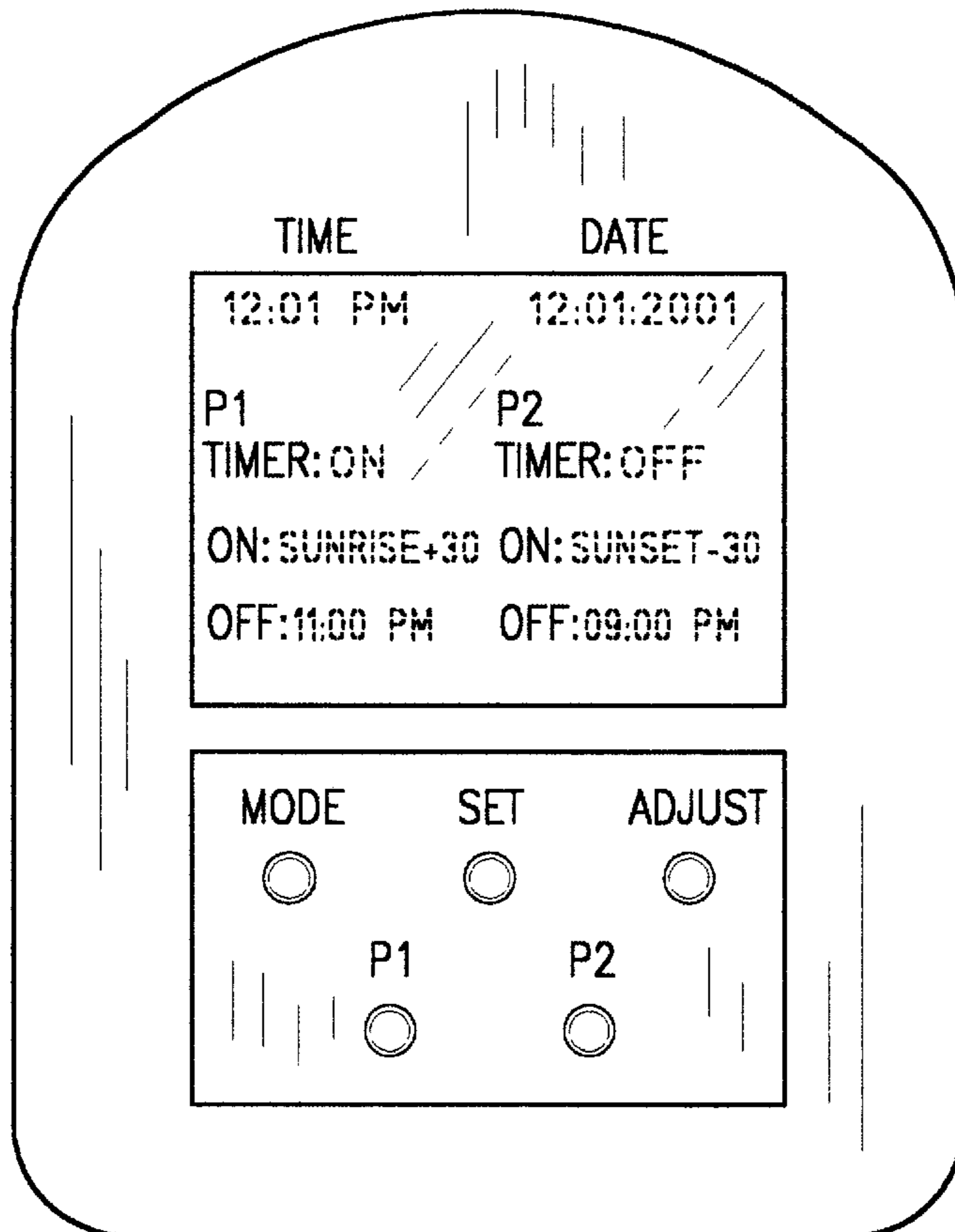
FIG. 1 is a front elevational view of a programmable power strip of the present invention;
FIG. 2 is a rear elevational view of the programmable power strip of FIG. 1;
FIG. 3 is a left side elevational view of the programmable power strip of FIG. 1;
FIG. 4 is a right side elevational view of the programmable power strip of FIG. 1;
FIG. 5 is a top plan view of the programmable power strip of FIG. 1; and,
FIG. 6 is a bottom plan view of the programmable power strip of FIG. 1.
The broken lines showing numbers and letters in FIG. 1 are for illustrative purposes only and form no part of the claimed design.

(56) **References Cited**

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1 Claim, 3 Drawing Sheets



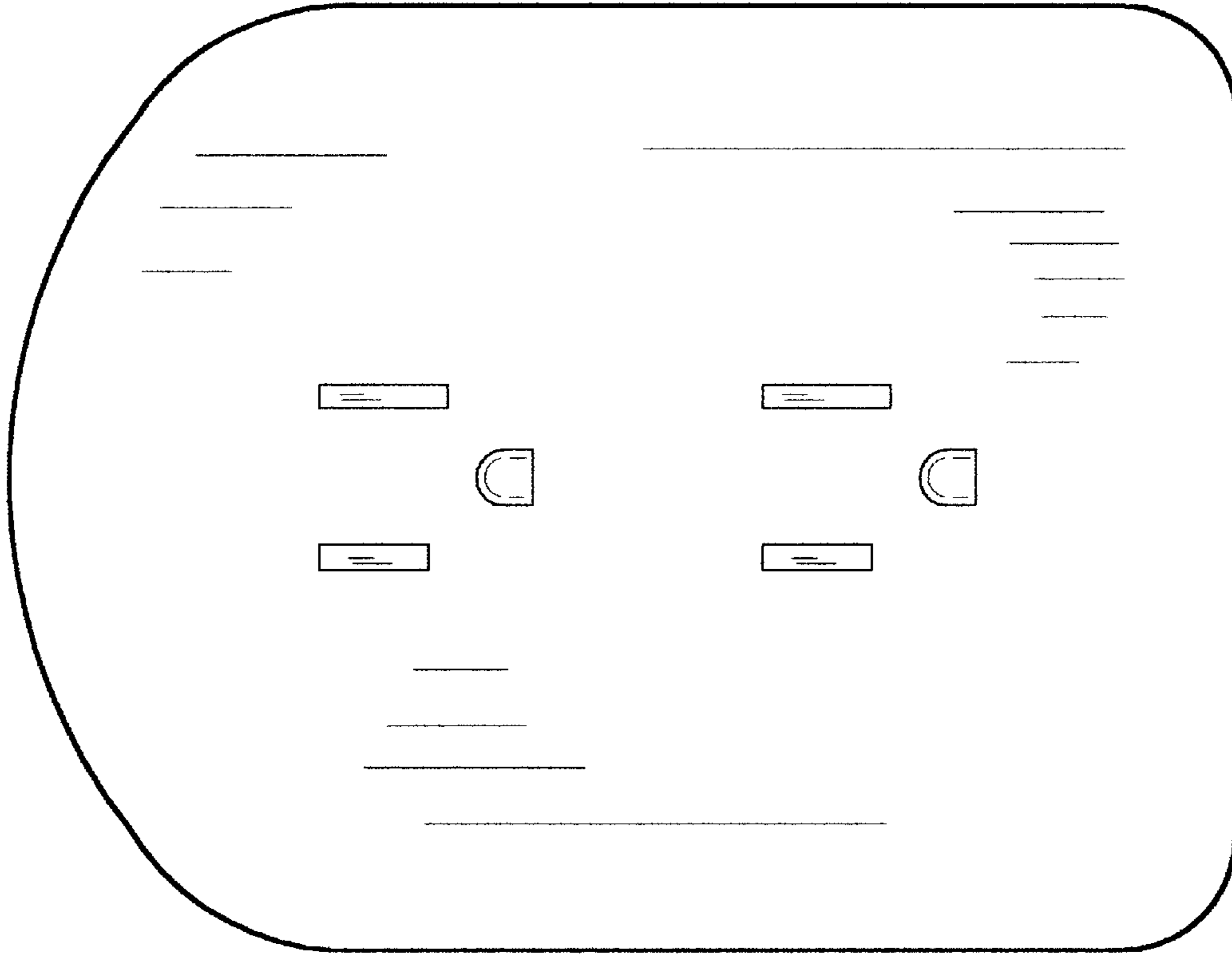


FIG. 2

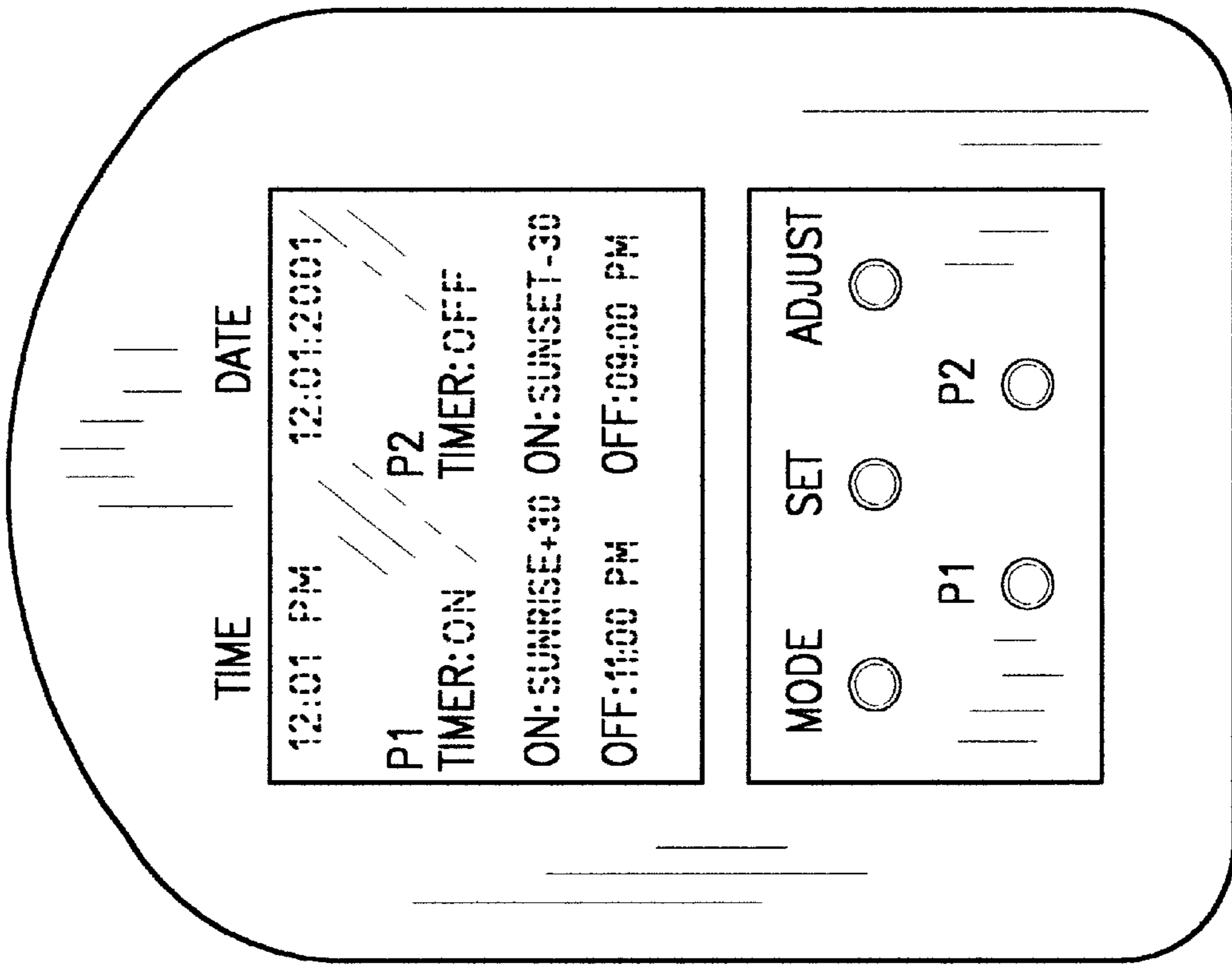


FIG. 1

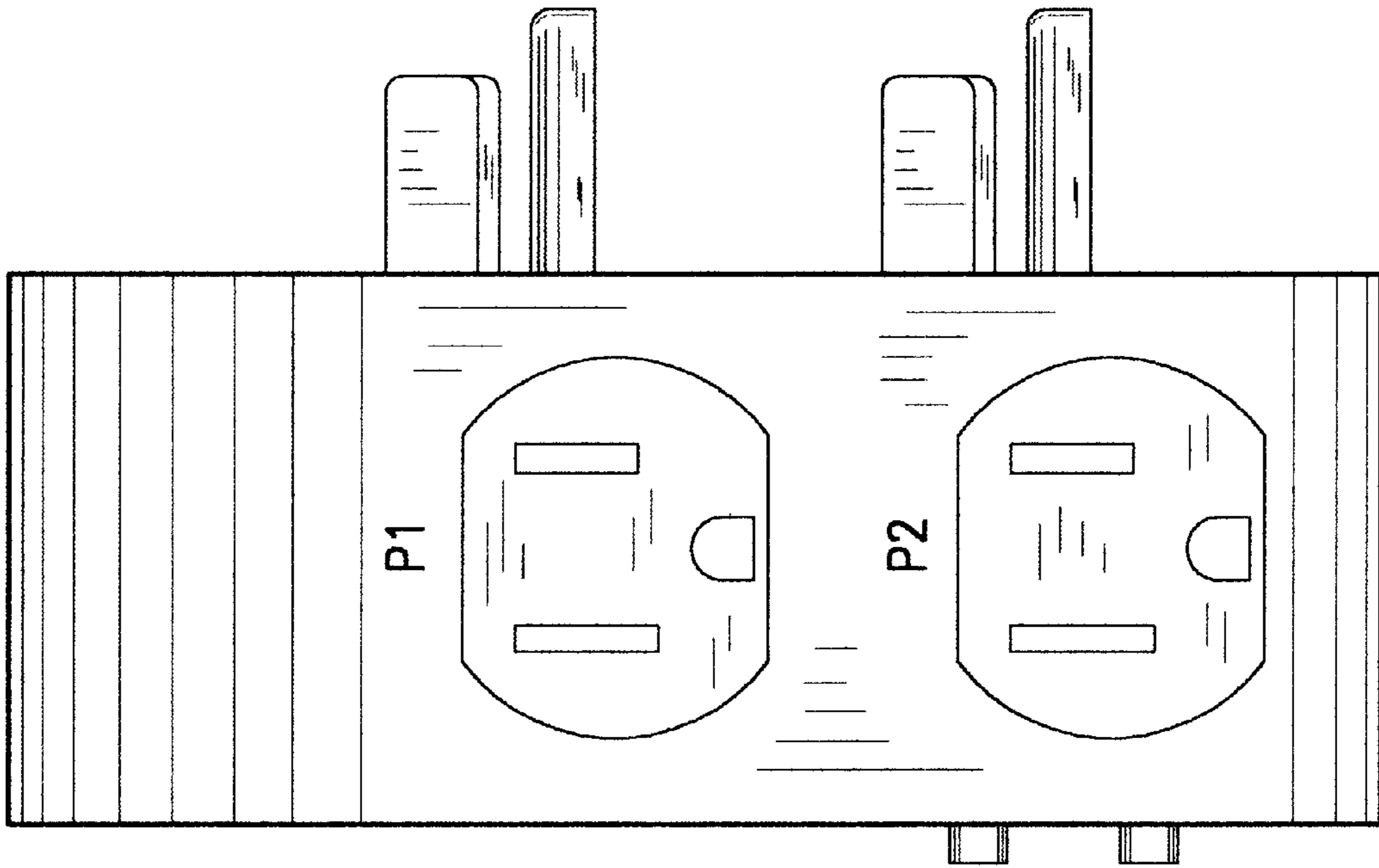


FIG. 3

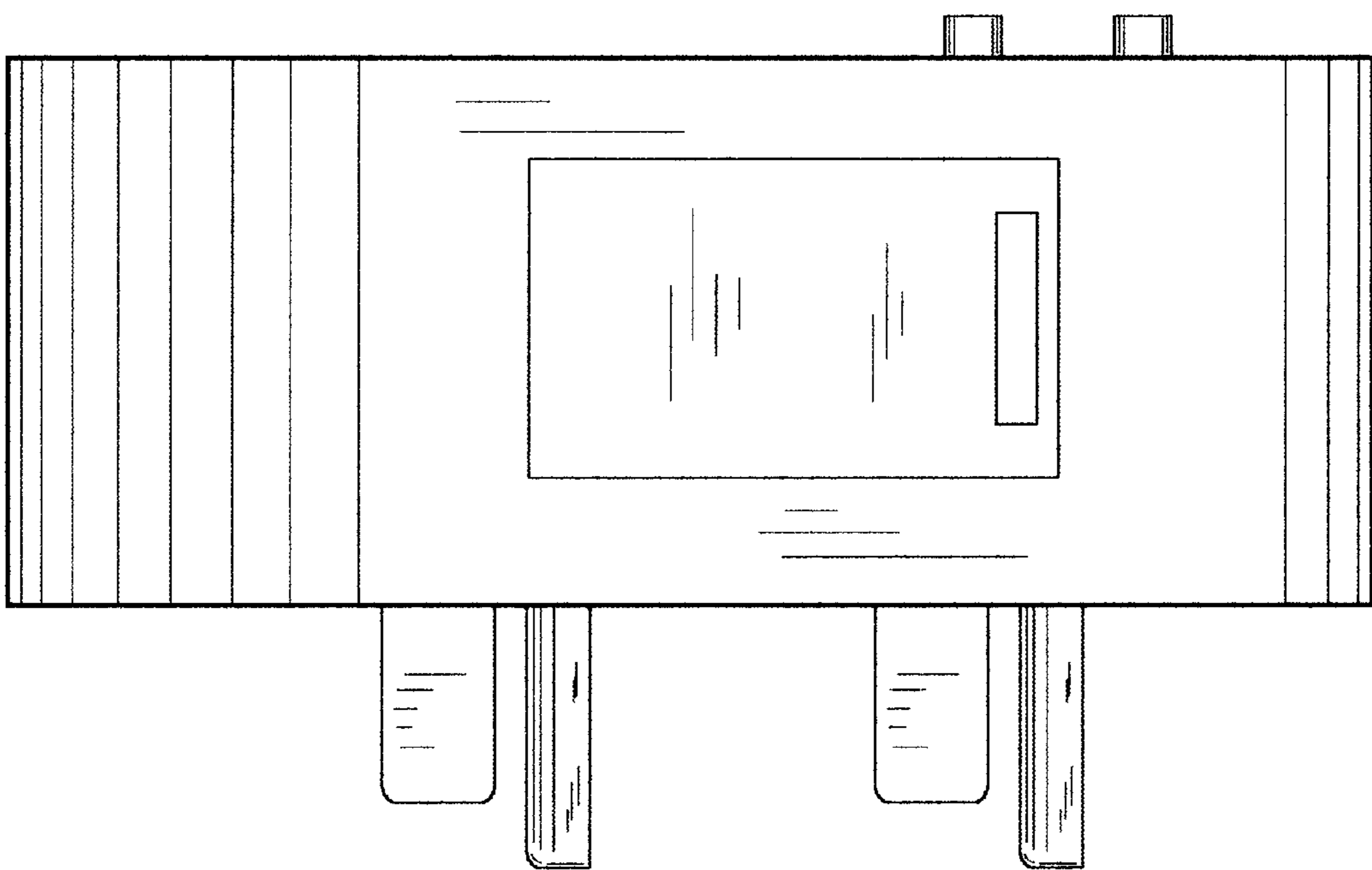


FIG. 4

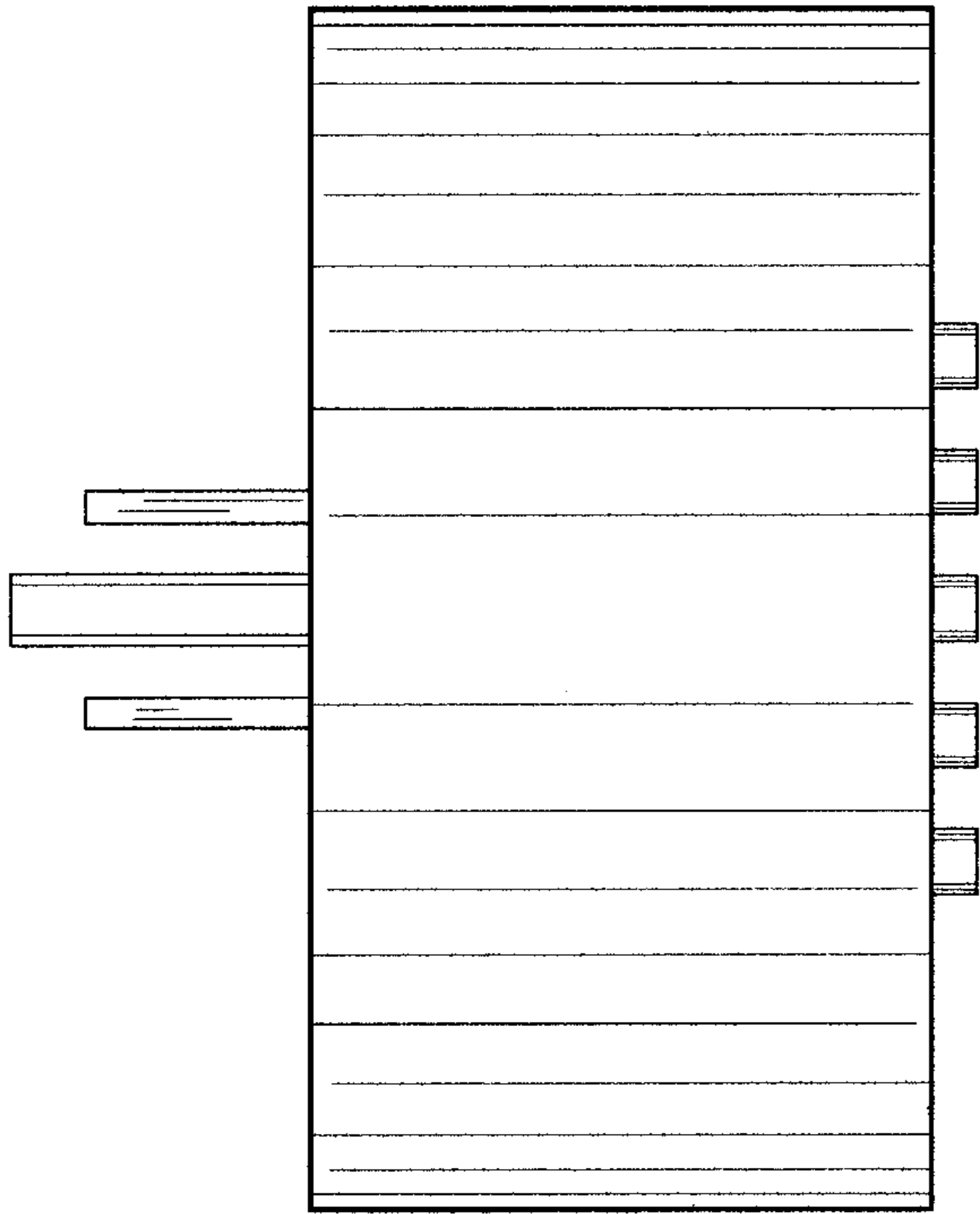


FIG. 5

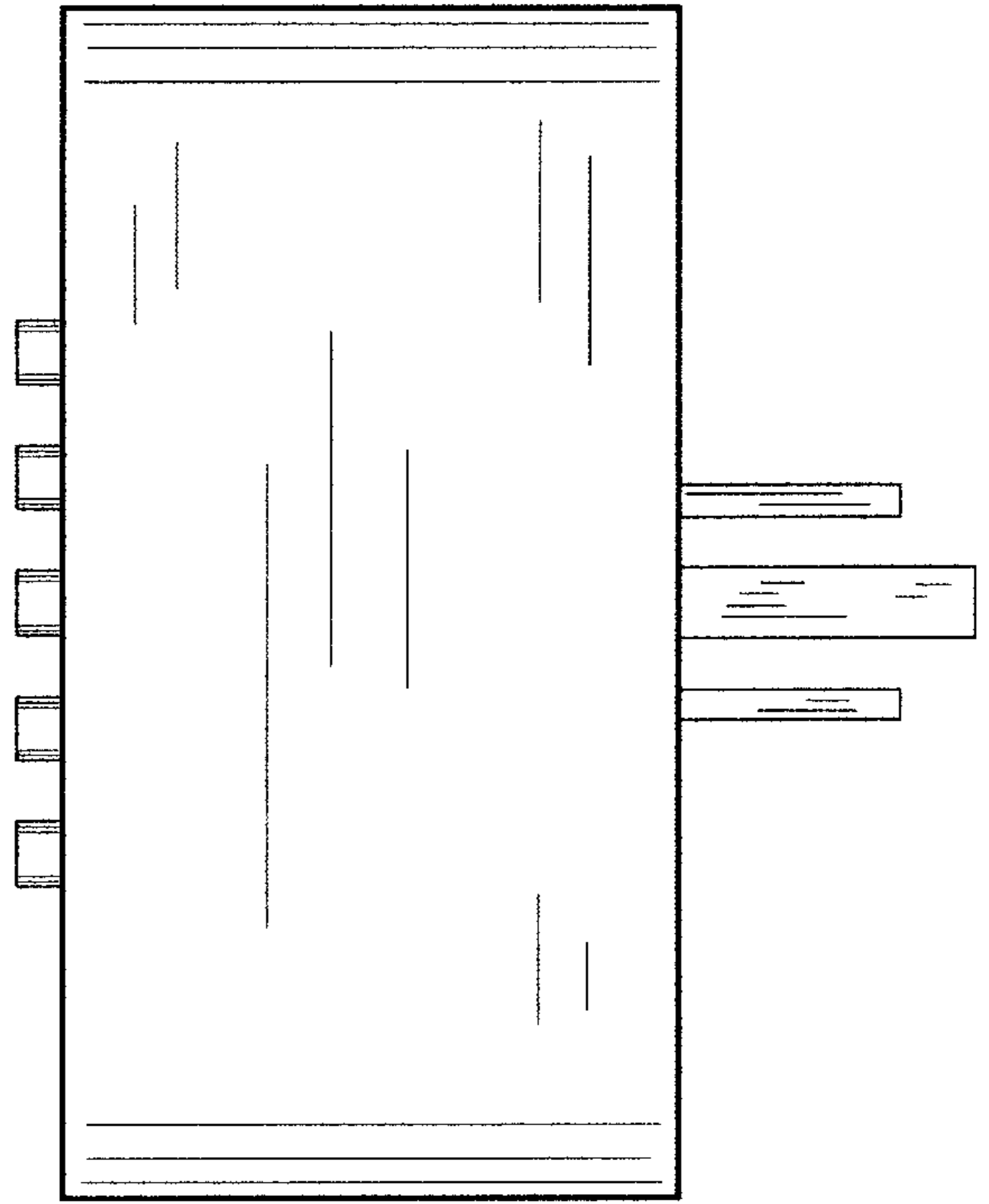


FIG. 6