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(12) **United States Design Patent** (10) **Patent No.:** **US D462,664 S**
Roesch et al. (45) **Date of Patent:** **** Sep. 10, 2002**

(54) **LOW VOLTAGE ELECTRICAL ASSEMBLY**

(75) Inventors: **Mark A. Roesch**, Brecksville, OH
(US); **Dennis P. Revlock, Sr.**, Medina,
OH (US)

(73) Assignee: **The Lamson & Sessions Co.**,
Cleveland, OH (US)

(**) Term: **14 Years**

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Related U.S. Application Data

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2001, which is a continuation of application No. 29/147,782,
filed on Sep. 5, 2001.

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/152**

(58) **Field of Search** D13/152; 174/48,
174/49, 50, 53, 58, 105 R; 220/3.2, 3.3,
3.9, 3.92, 3.94

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Primary Examiner—Jennifer Rivard

(74) *Attorney, Agent, or Firm*—Jones, Day, Reavis &
Pogue

(57) **CLAIM**

The ornamental design for a low voltage electrical assembly,
as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of one embodiment of the low
voltage electrical assembly of our new design showing a
double gang assembly;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a top view thereof;

FIG. 5 is a bottom view thereof;

FIG. 6 is a right side view thereof;

FIG. 7 is a left side view thereof;

FIG. 8 is a perspective view of another embodiment of the
low voltage electrical assembly of our new design showing
the double gang assembly of FIG. 1, but with portions of the
assembly shown in phantom;

FIG. 9 is a front view thereof;

FIG. 10 is a rear view thereof;

FIG. 11 is a top view thereof;

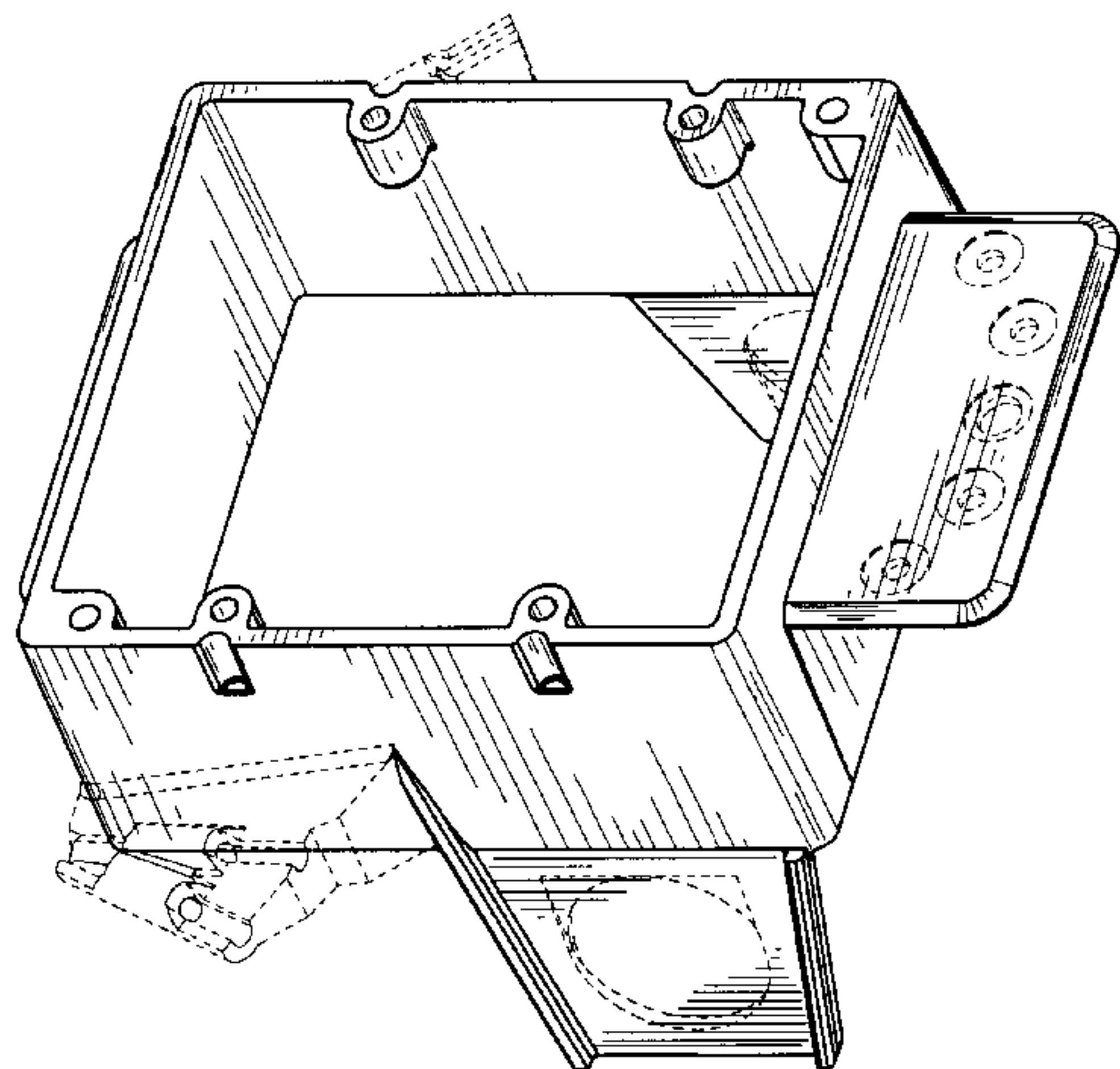
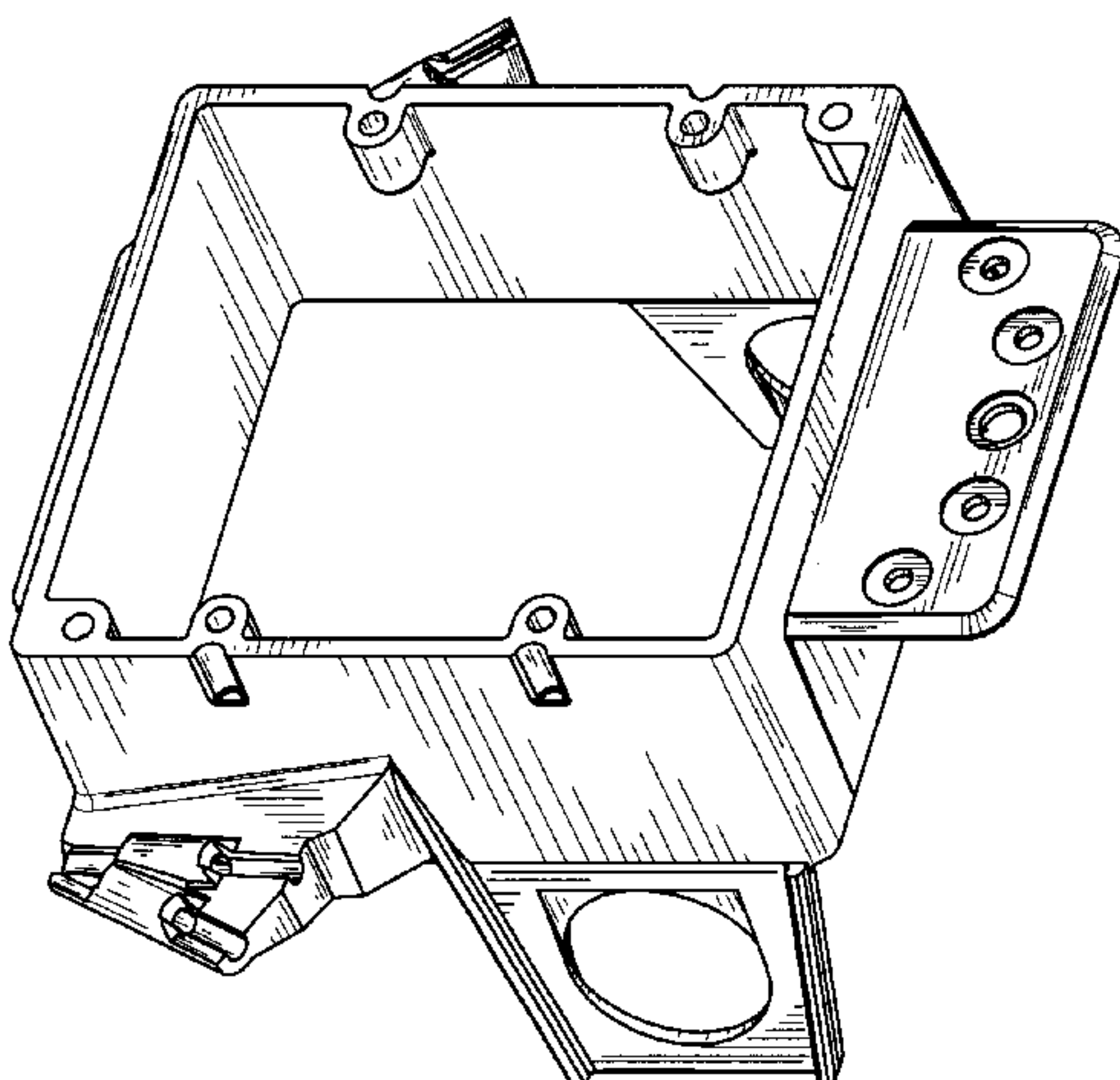
FIG. 12 is a bottom view thereof;

FIG. 13 is a right side view thereof; and,

FIG. 14 is a left side view thereof.

The broken line showings of FIGS. 8–14 are for illustrative
purposes only and form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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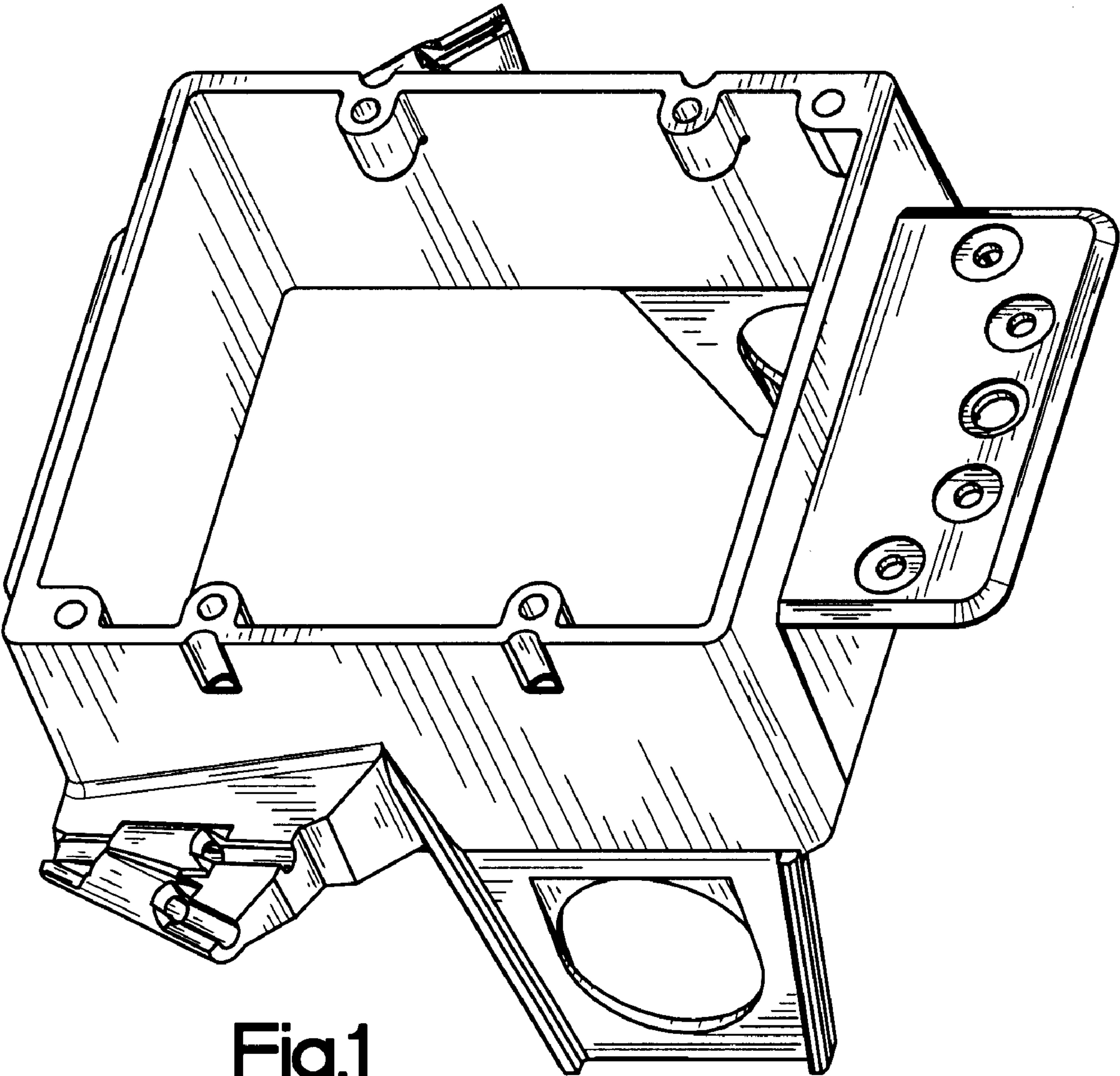


Fig.1

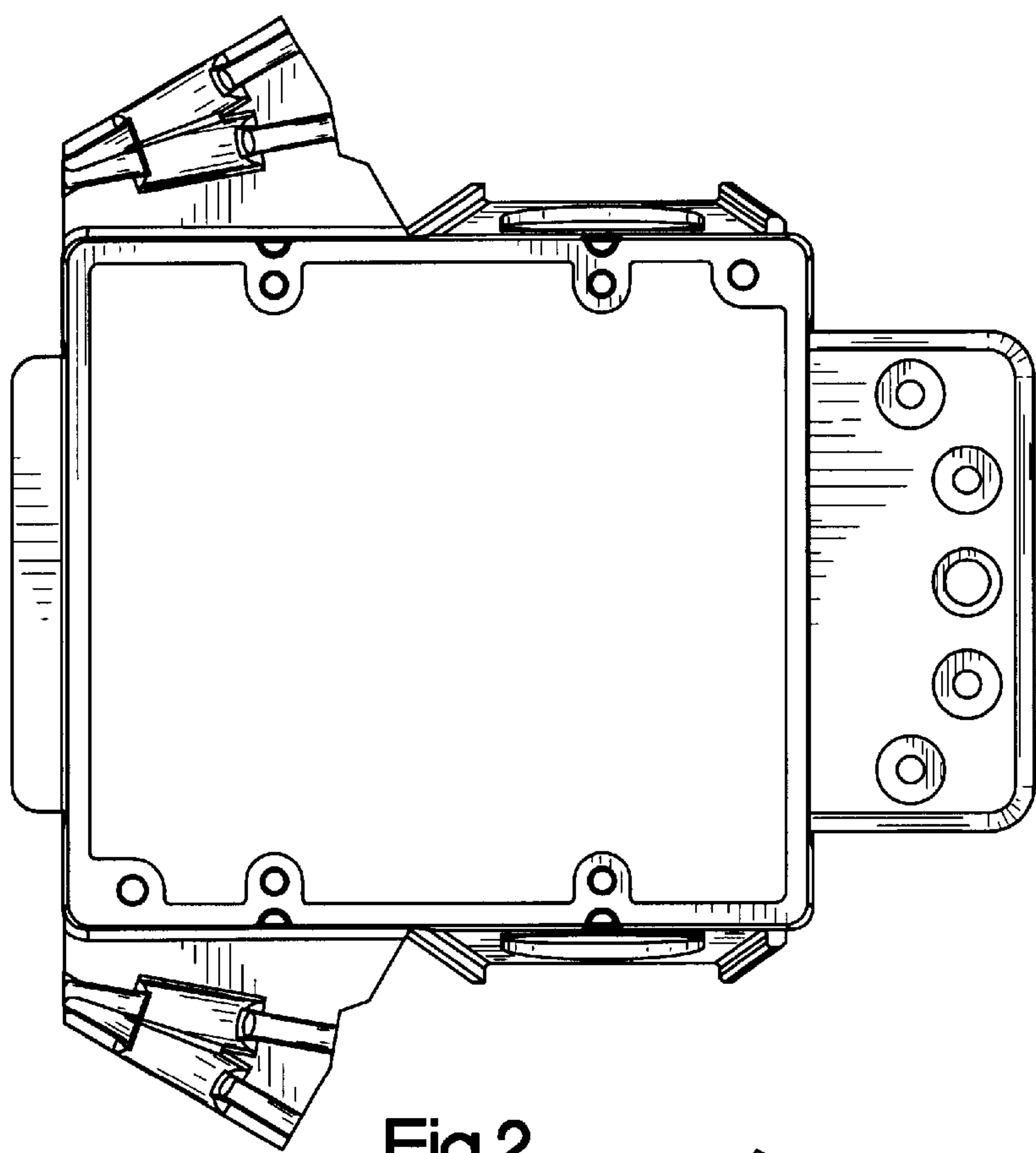


Fig.2

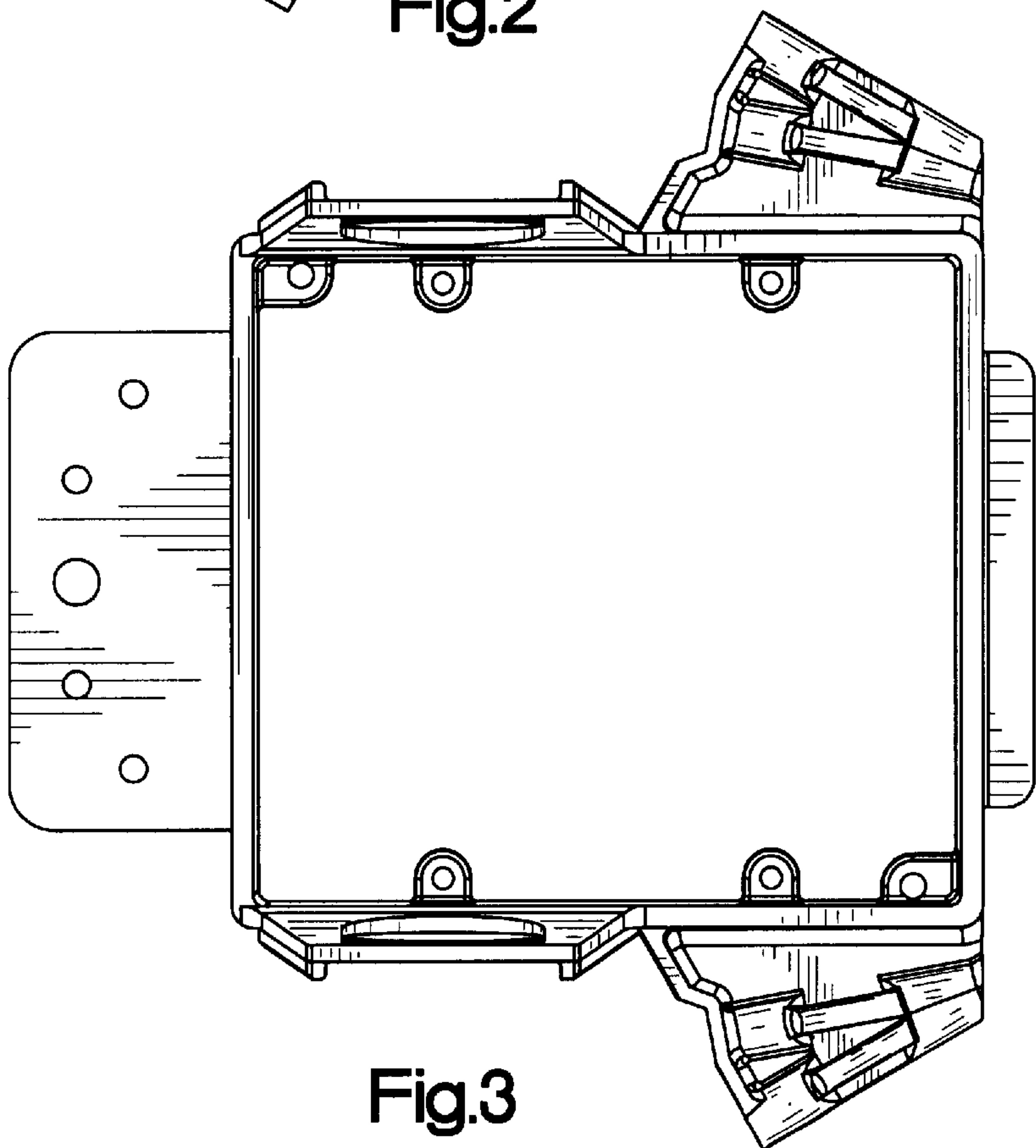


Fig.3

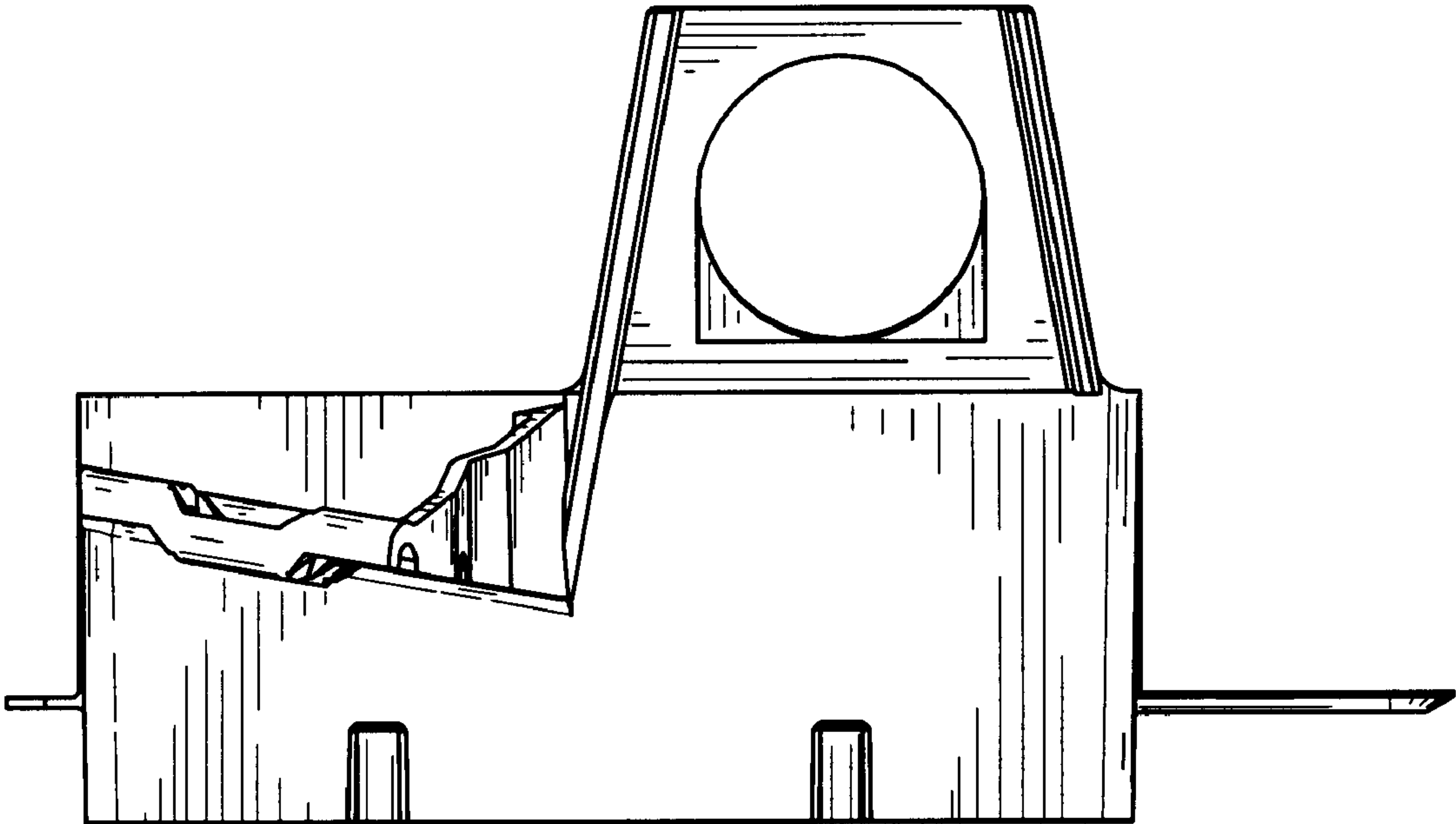


Fig.4

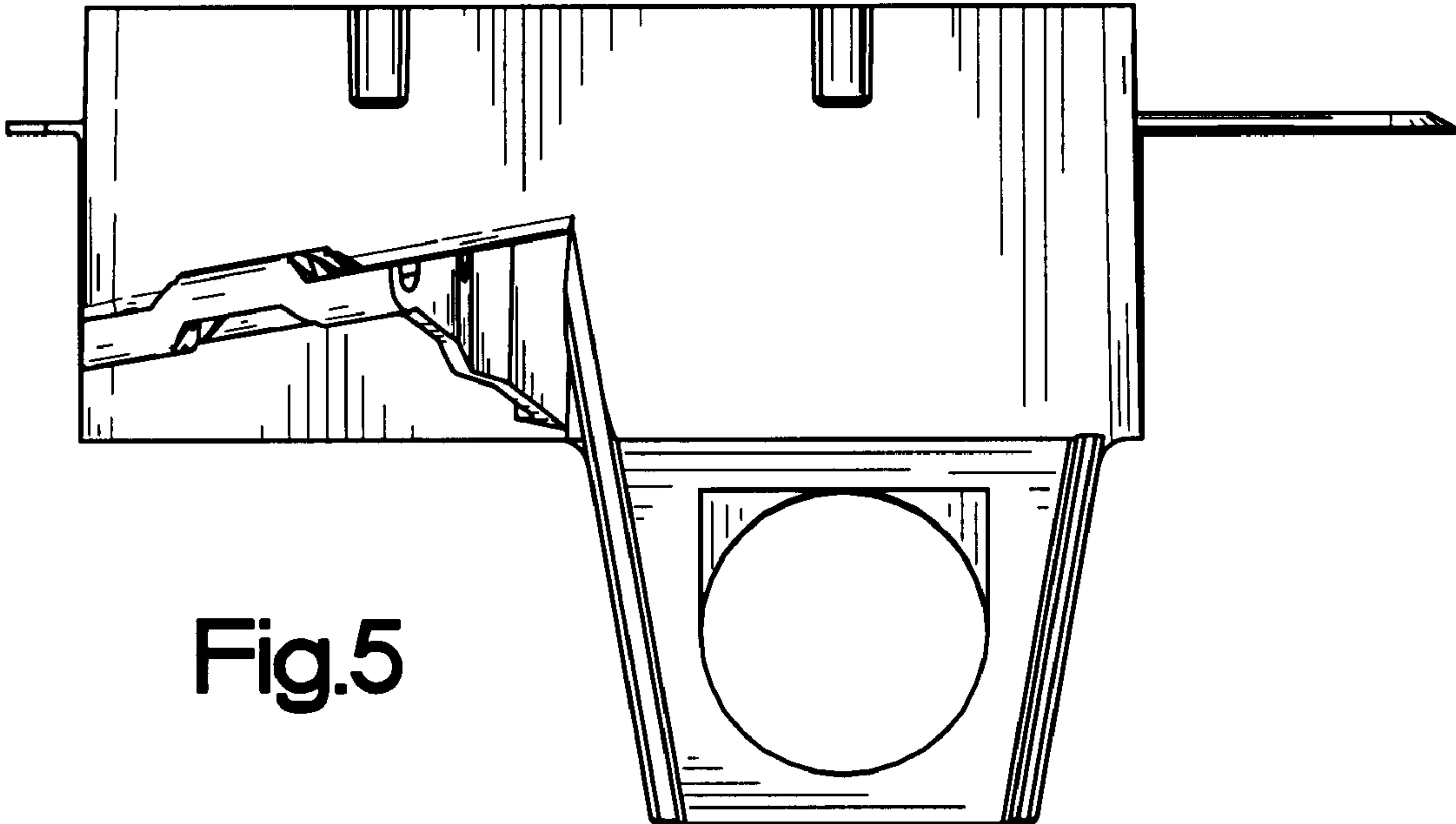
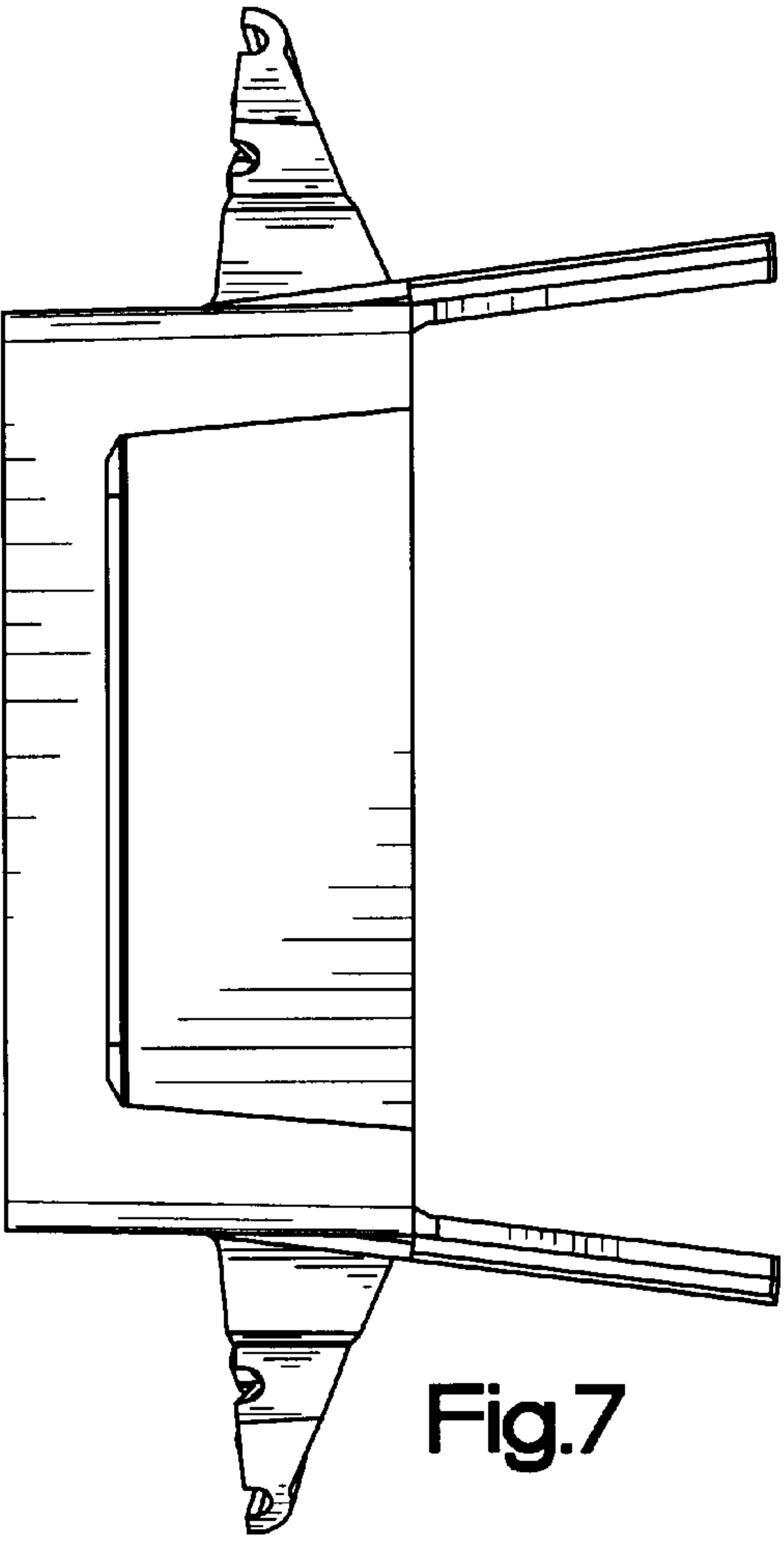
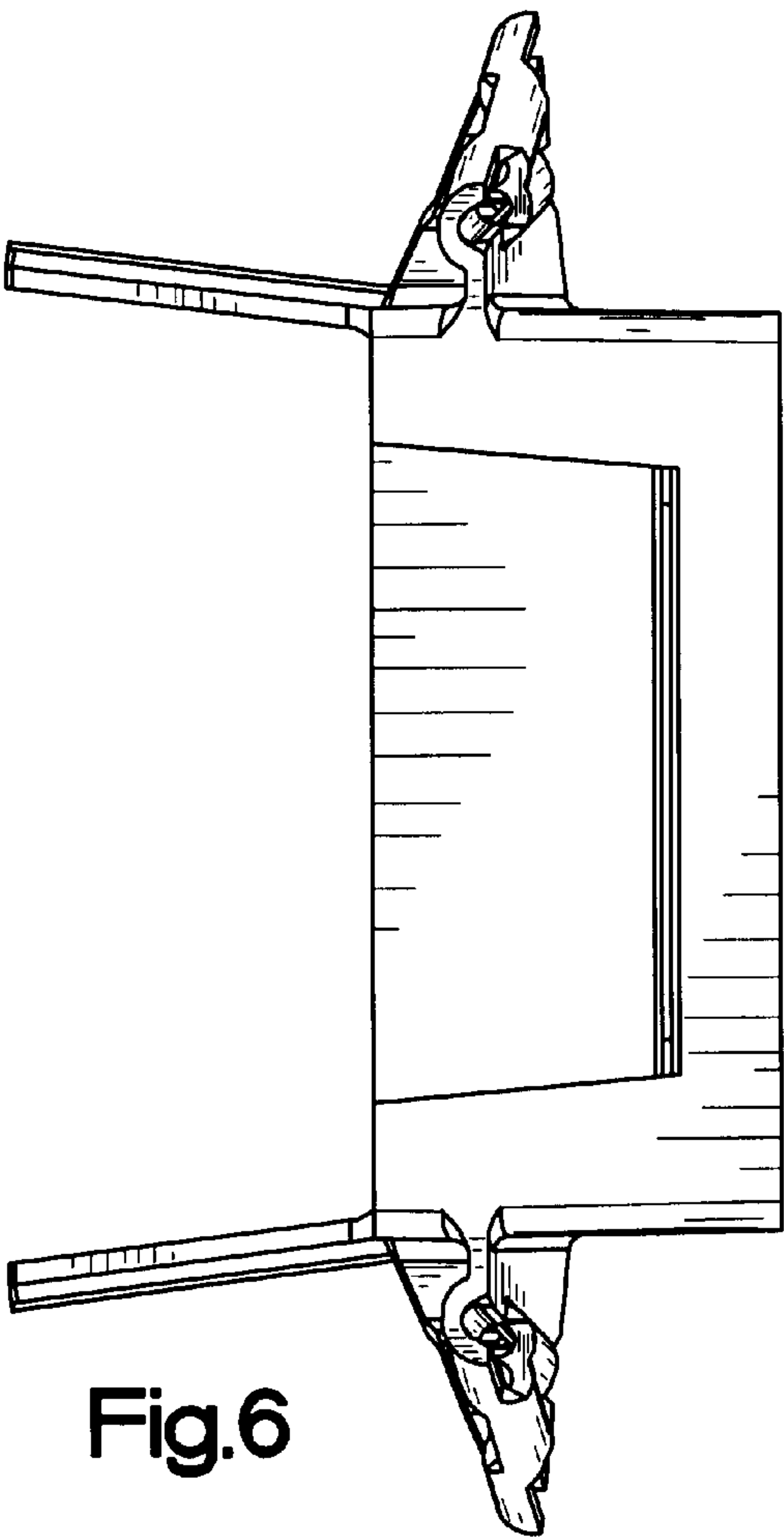


Fig.5



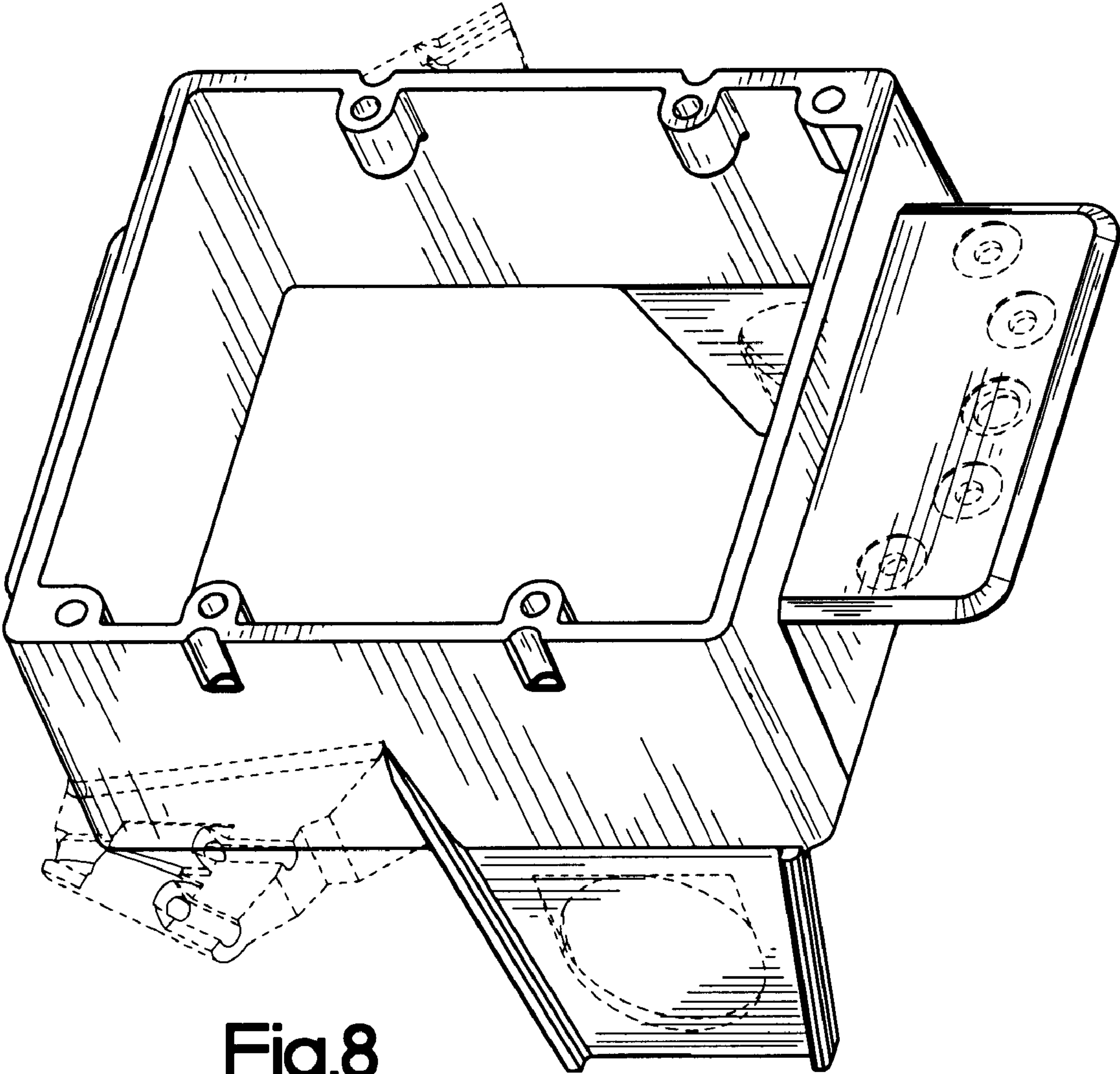


Fig.8

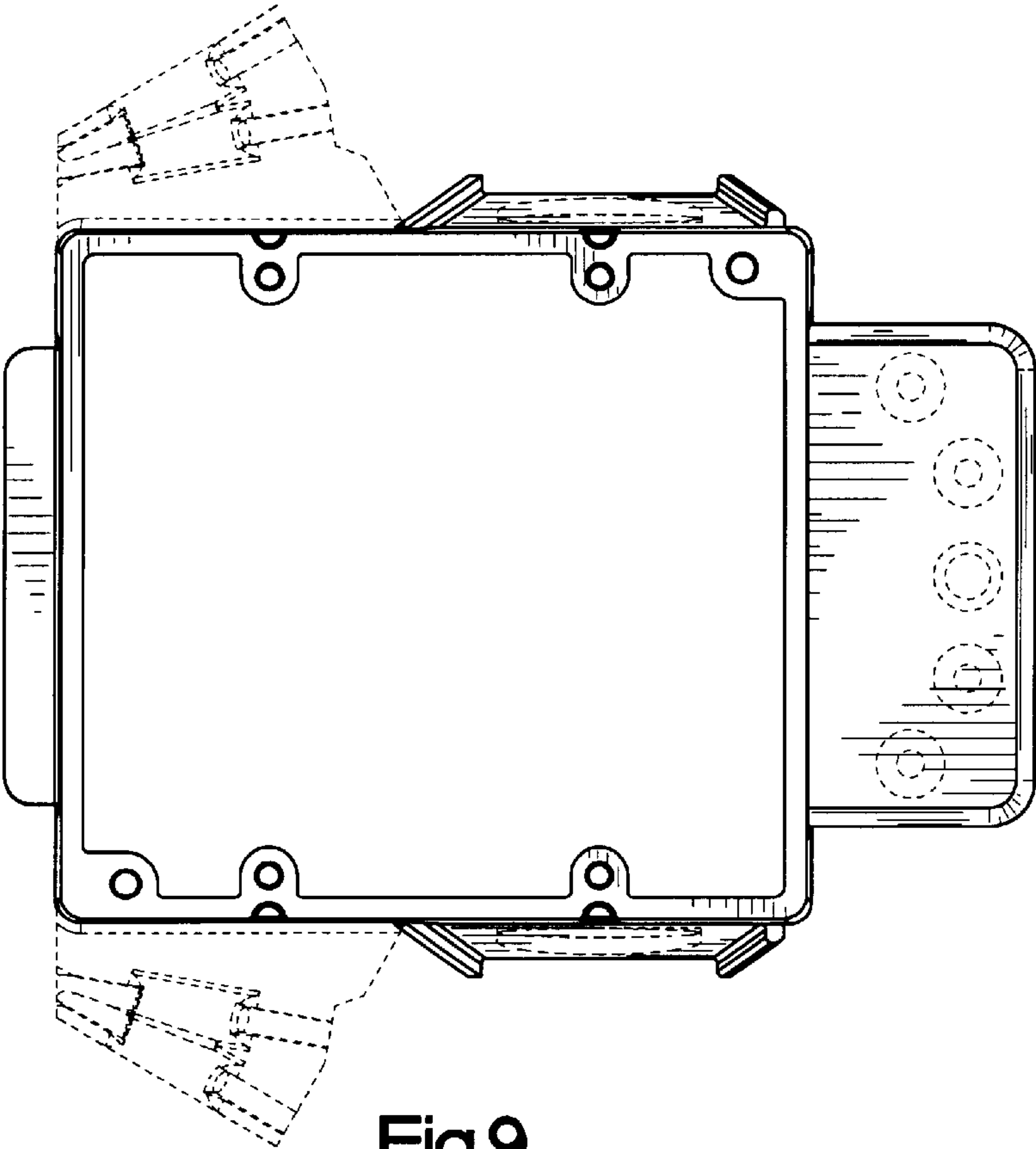


Fig.9

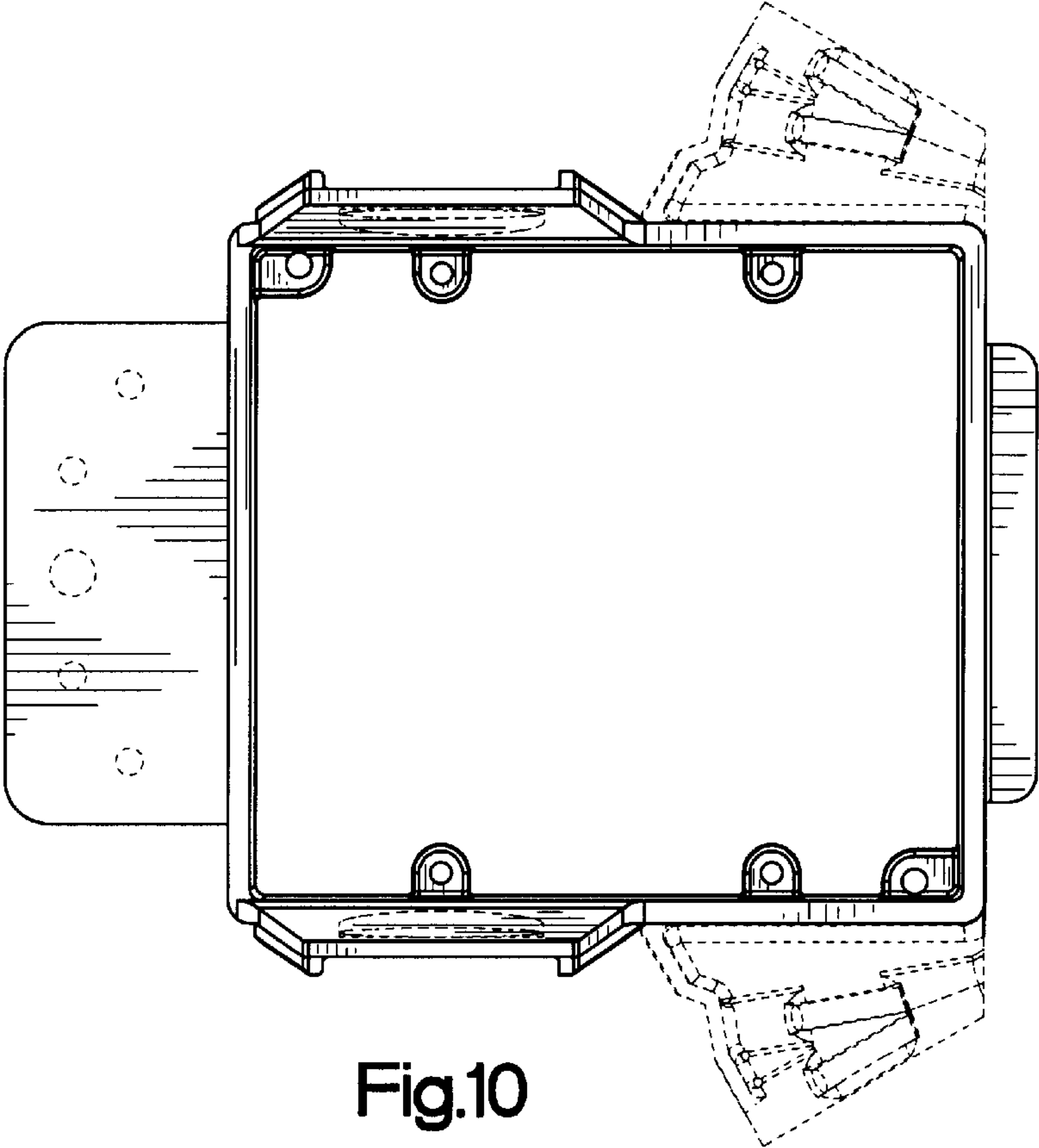


Fig.10

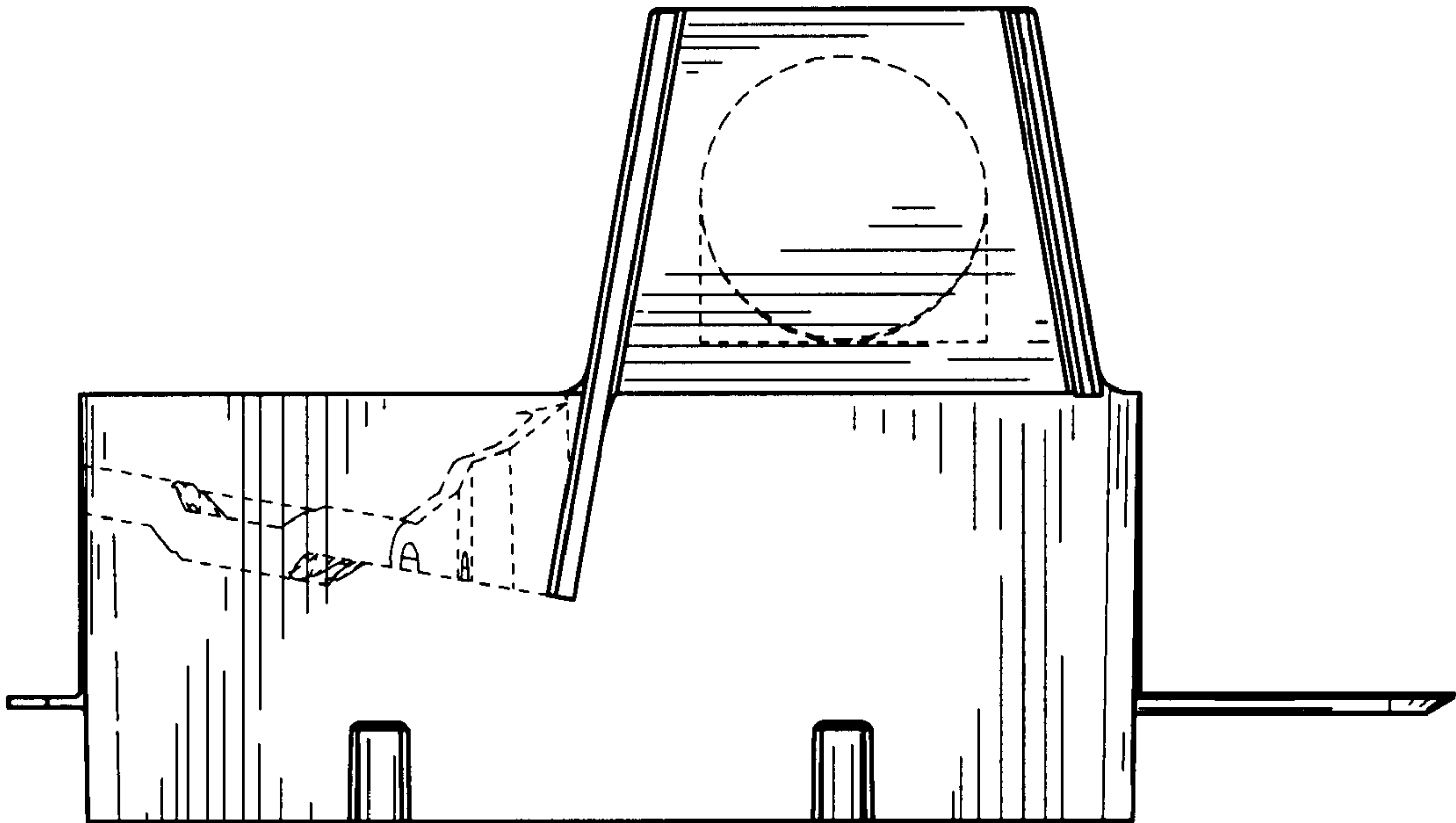


Fig.11

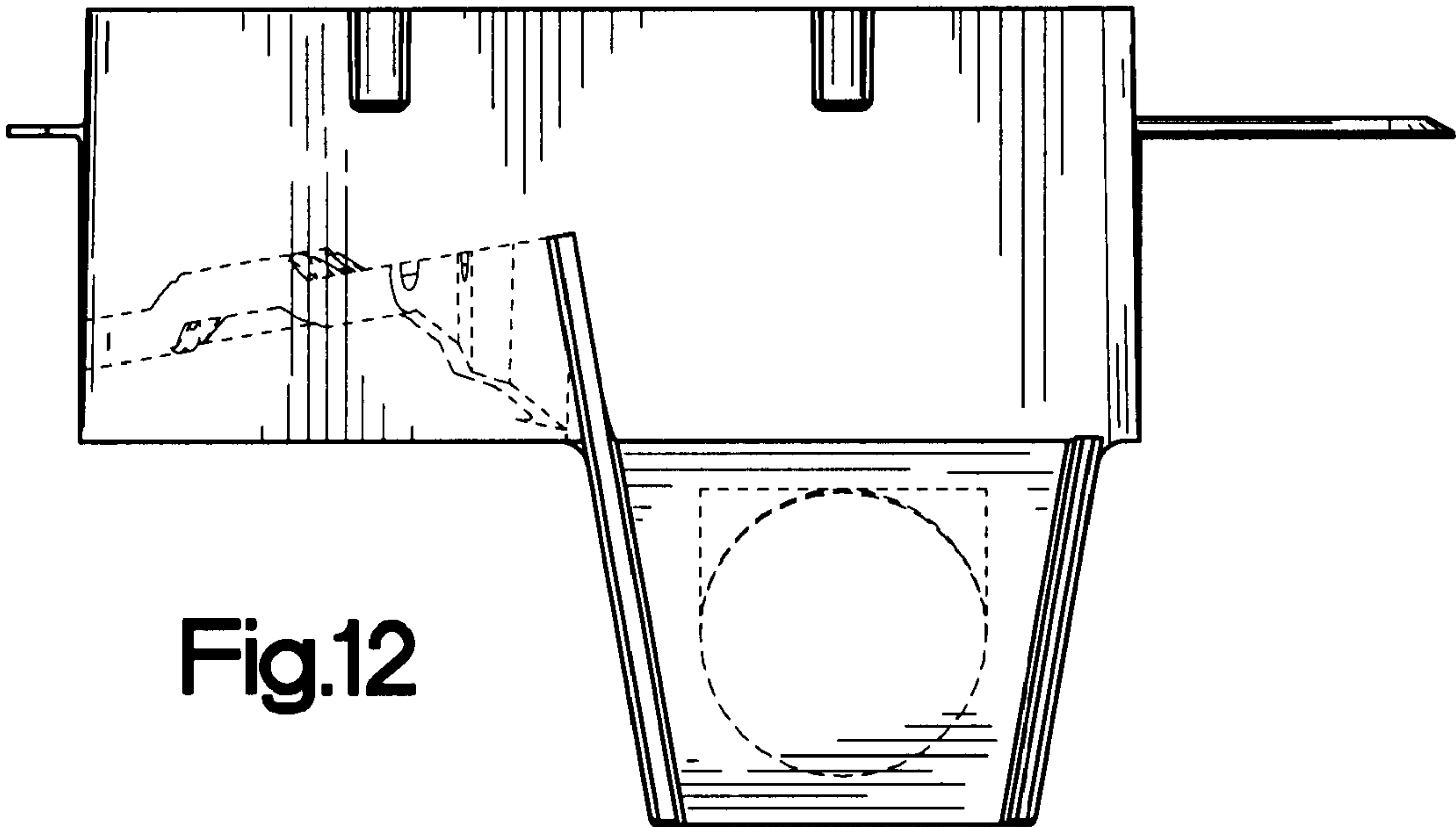


Fig.12

