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(12) **United States Design Patent**  
**Dow et al.**

(10) **Patent No.: US D460,968 S**

(45) **Date of Patent: \*\* Jul. 30, 2002**

(54) **CONTROL MODULE FOR A COMPUTER PERIPHERAL**

(75) Inventors: **James C. Dow; Modest Khovaylo,**  
both of Fort Collins, CO (US)

(73) Assignee: **Hewlett-Packard Company,** Palo Alto,  
CA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/091,208**

(22) Filed: **Jul. 24, 1998**

**Related U.S. Application Data**

(60) Continuation of application No. 29/063,320, filed on Dec. 5, 1996, now Pat. No. Des. 401,918, which is a division of application No. 29/032,547, filed on Dec. 21, 1994, now Pat. No. Des. 381,324.

(51) **LOC (7) Cl. .... 14-02**

(52) **U.S. Cl. .... D14/388**

(58) **Field of Search** ..... D14/168, 156,  
D14/169, 300, 356, 319, 388, 432, 441;  
D9/345-6; D6/626-7; D13/162; 361/724-6,  
622; 360/99.12

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D305,658 S \* 1/1990 Hosaka

- D310,834 S \* 9/1990 Shibata
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\* cited by examiner

*Primary Examiner*—M. H. Tung

(57) **CLAIM**

The ornamental design of a control module for a computer peripheral, as shown and described.

**DESCRIPTION**

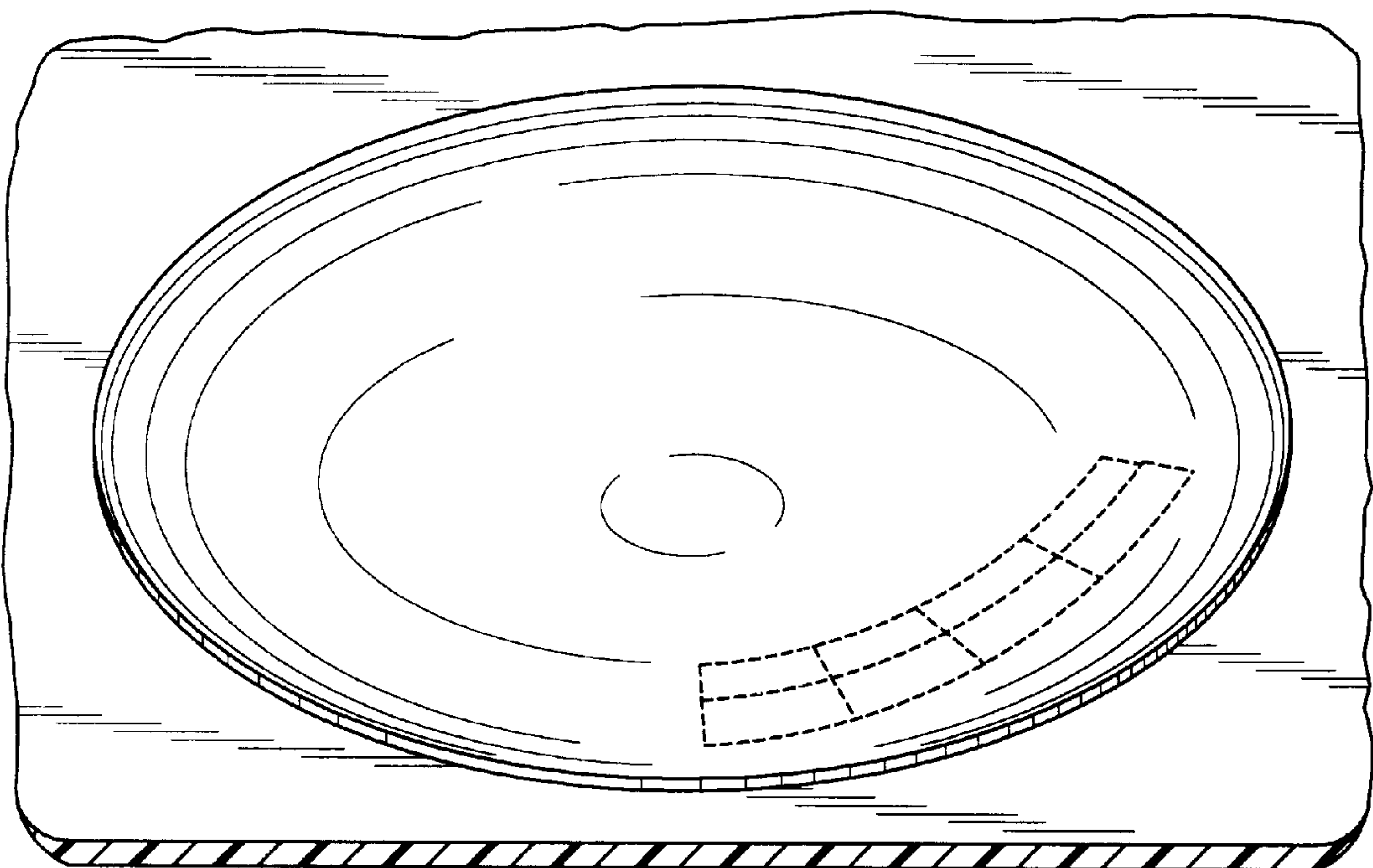
FIG. 1 is a top, rear perspective view of a control module for a computer peripheral showing our new design;

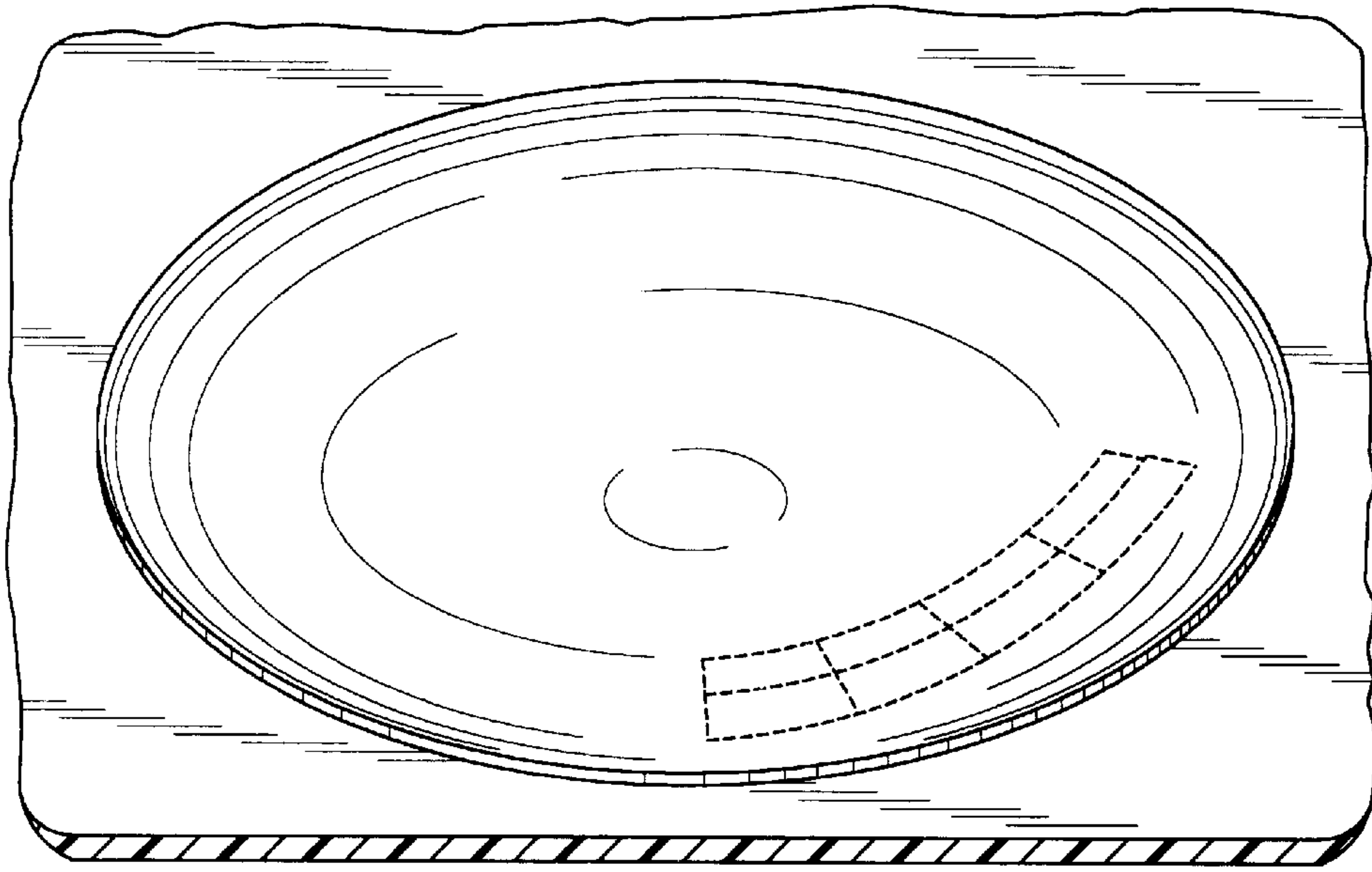
FIG. 2 is a top, left perspective view thereof; and,

FIG. 3 is a top plan view thereof.

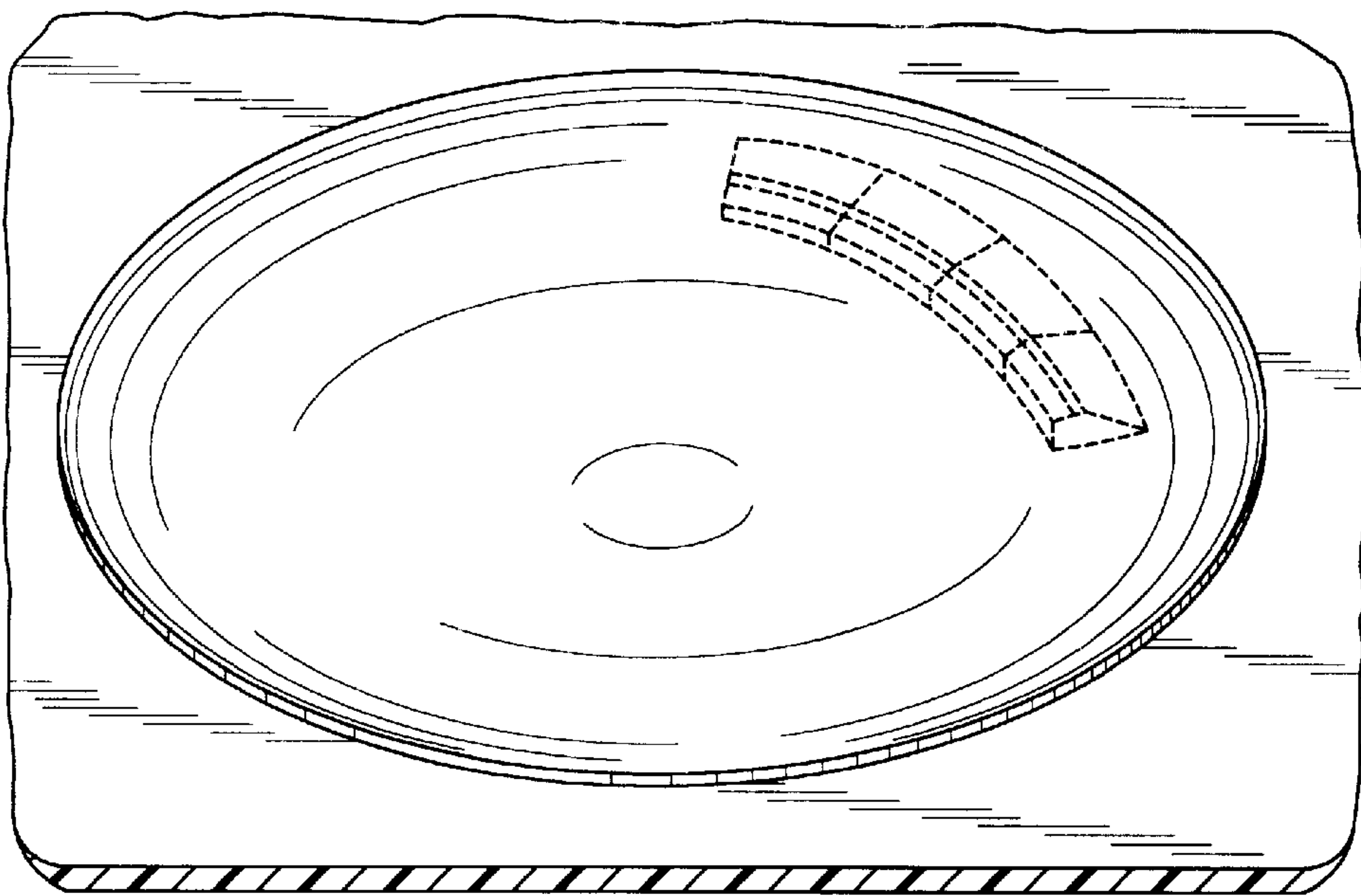
The broken lines immediately adjacent the shaded areas represent the bounds of the claim and all the broken lines are for illustrative purposes only and form no part of the claimed design.

**1 Claim, 2 Drawing Sheets**

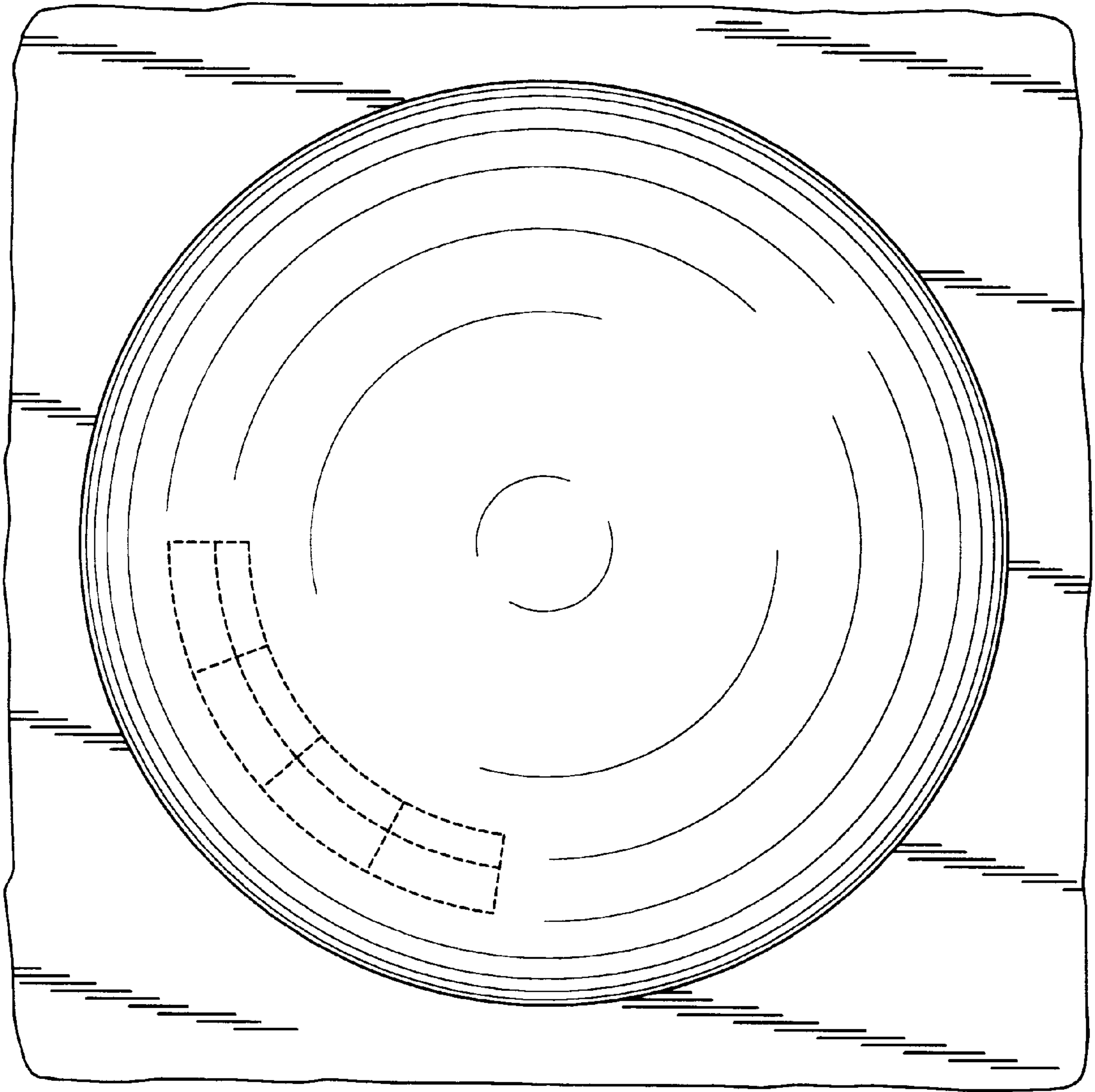




**FIG. 1**



**FIG. 2**



**FIG. 3**