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(12) **United States Design Patent**  
**Ebihara et al.**

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(54) **HYBRID INTEGRATED CIRCUIT DEVICE**

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(\*\*) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.** ..... **D13/182**

(58) **Field of Search** ..... D13/182, 123,  
D13/133, 180, 184, 199; D10/46, 75; 29/829-932,  
846; 174/250-251, 253-255, 260-261,  
268; 216/13; 292/314; 316/718, 748, 752-753,  
760, 783-784, 820; 411/500; 428/901;  
470/27

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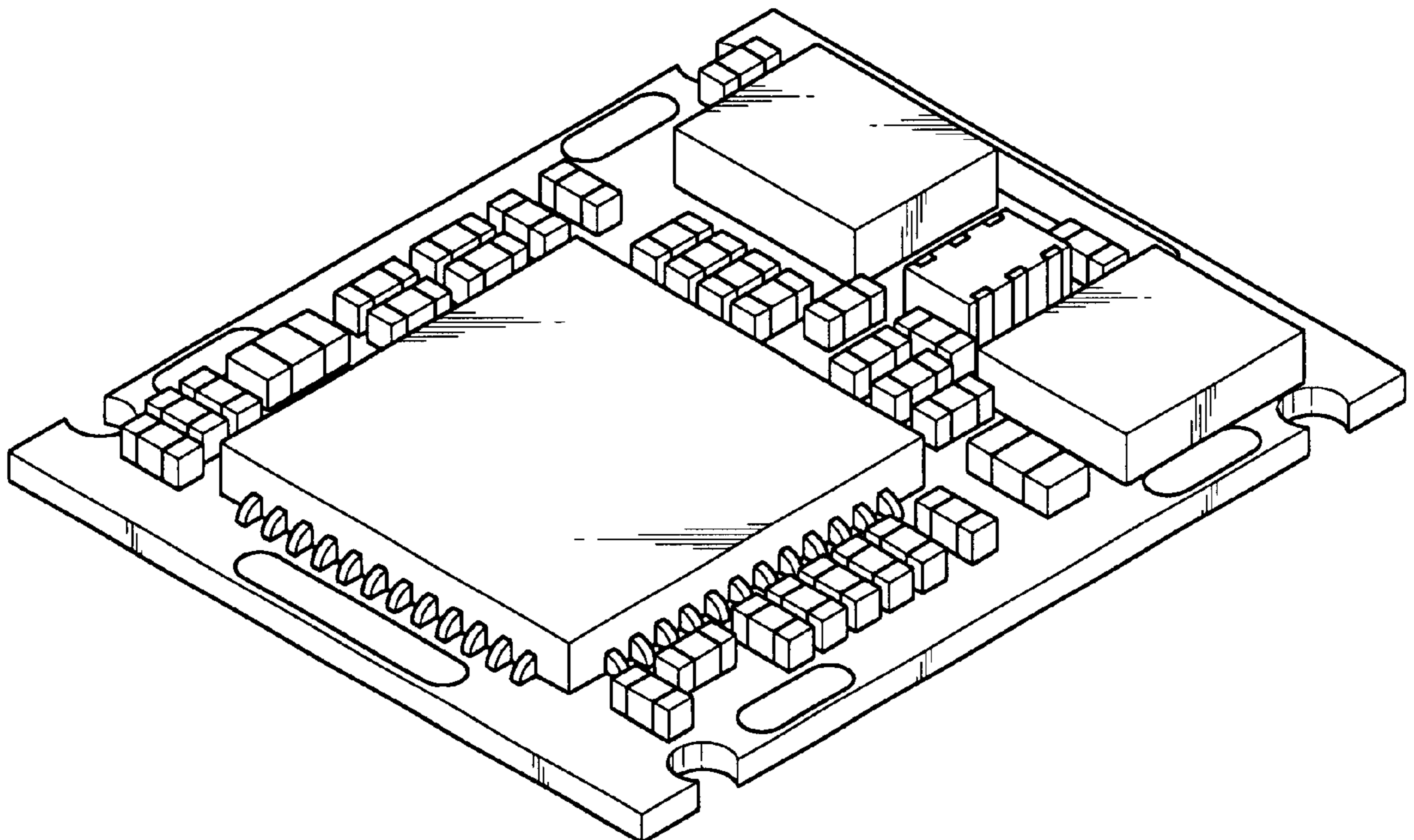
(57) **CLAIM**

The ornamental design for a hybrid integrated circuit device,  
as shown and described.

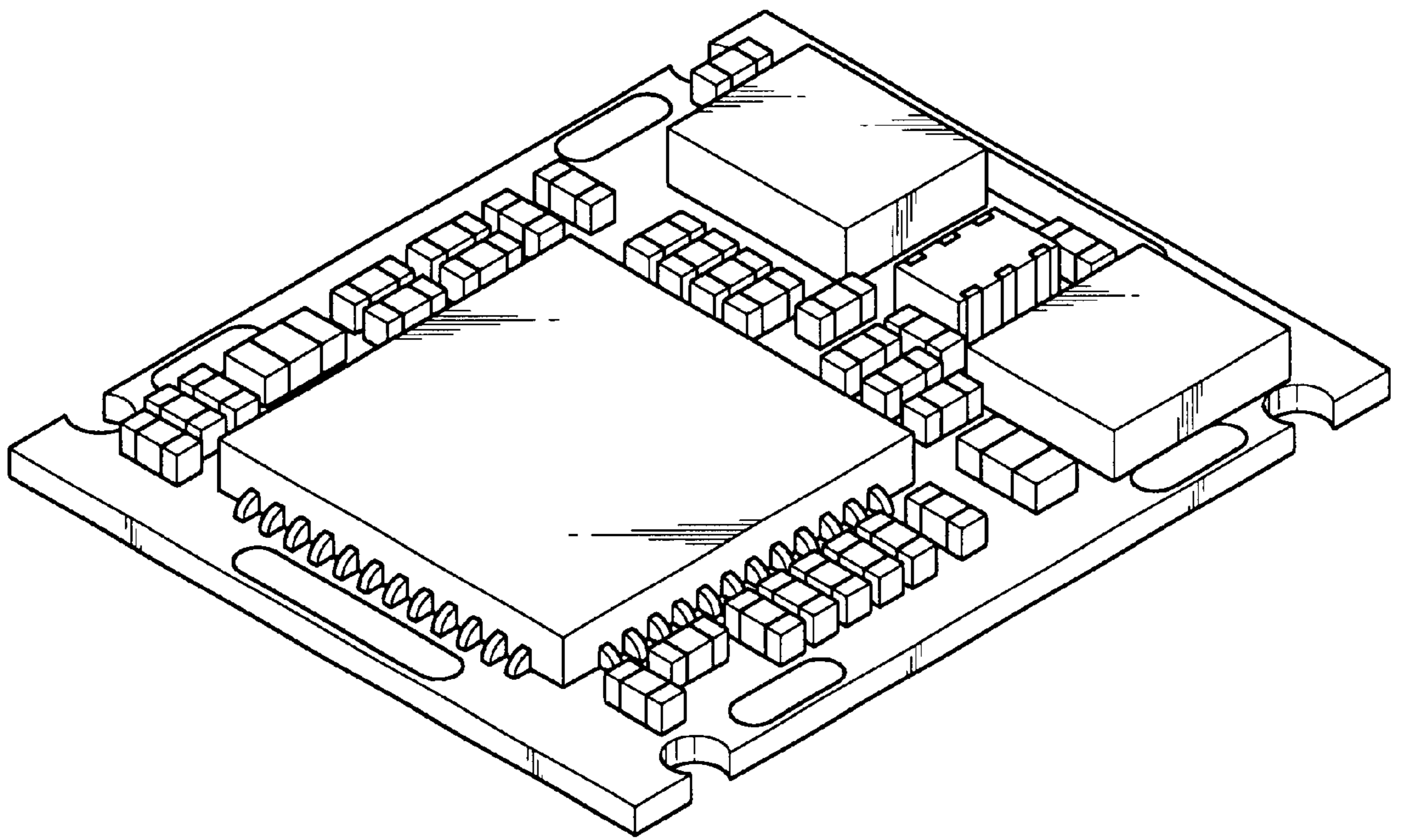
**DESCRIPTION**

FIG. 1 is a perspective view of a hybrid integrated circuit device showing an embodiment of our new design;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a rear elevational view thereof;  
FIG. 4 is a left side elevational view thereof;  
FIG. 5 is a right side elevational view thereof;  
FIG. 6 is a top plan view thereof; and,  
FIG. 7 is a bottom plan view thereof.

**1 Claim, 3 Drawing Sheets**



*FIG. 1*



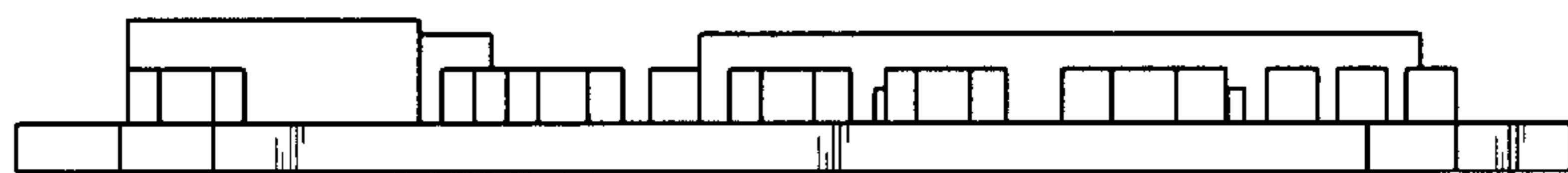
*FIG. 2*



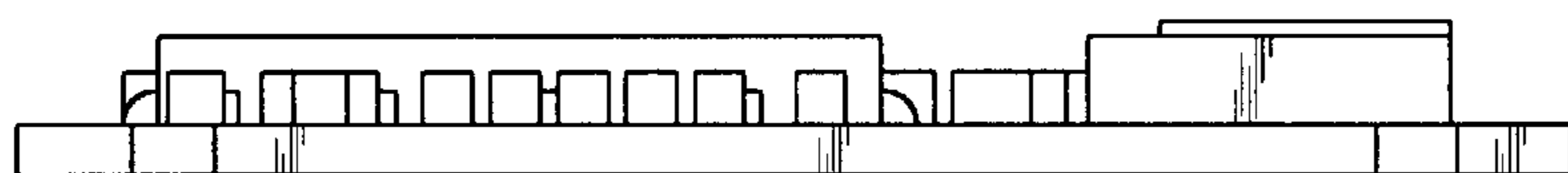
*FIG. 3*



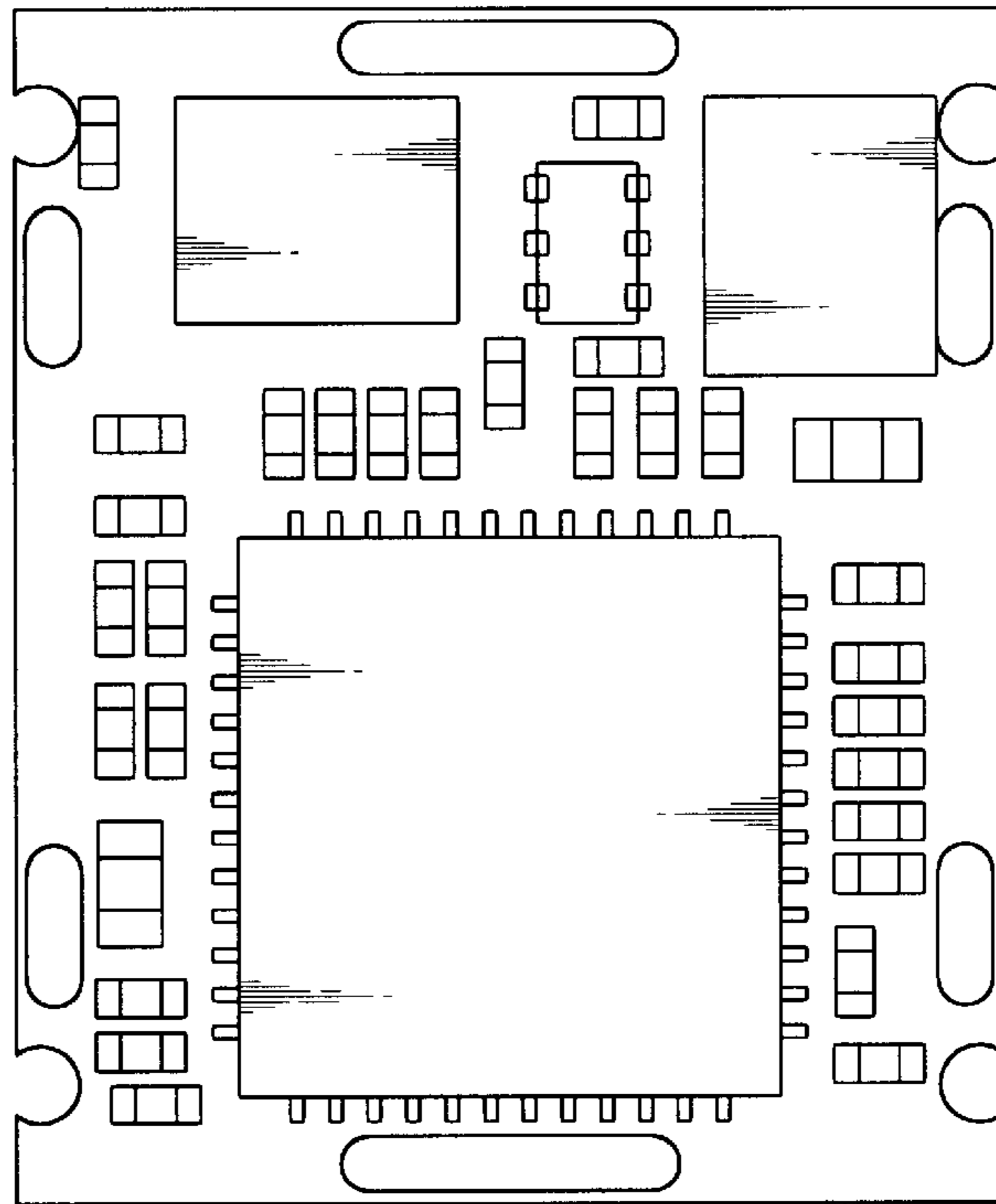
*FIG. 4*



*FIG. 5*



**FIG. 6**



**FIG. 7**

