



US00D458227S

(12) **United States Design Patent**  
**Aldridge**

(10) **Patent No.:** **US D458,227 S**

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(54) **PCB CONNECTOR**

(75) **Inventor:** **Ian Edward Aldridge**, Hampshire (GB)

(73) **Assignee:** **Contour Electronics Limited** (GB)

(\*\*) **Term:** **14 Years**

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(51) **LOC (7) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/147**

(58) **Field of Search** ..... **D13/147; 439/660,**  
**439/559, 562, 564, 55, 76.1, 78**

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

- 4,676,575 A \* 6/1987 Denlinger et al. .... 439/559 X
- 4,697,864 A \* 10/1987 Hayes et al. .... 439/444
- 5,279,507 A \* 1/1994 Kameyama ..... 439/552
- 5,464,355 A \* 11/1995 Rothenberger ..... 439/559
- 5,487,680 A \* 1/1996 Yamanashi ..... 439/552
- 5,562,292 A \* 10/1996 Roy et al. .... 439/556 X

- 5,564,952 A \* 10/1996 Davis ..... 439/682
- 5,621,328 A \* 4/1997 Yamanashi ..... 439/559 X
- 5,728,974 A \* 3/1998 Kitoh et al. .... 174/65 SS
- D415,999 S \* 11/1999 Esposito ..... D13/147 X
- 6,280,246 B1 \* 8/2001 Sawayanagi ..... 439/567

\* cited by examiner

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(57) **CLAIM**

The ornamental design for a PCB connector, as shown and described.

**DESCRIPTION**

FIG. 1 is a top plan view of a PCB connector showing my new design;

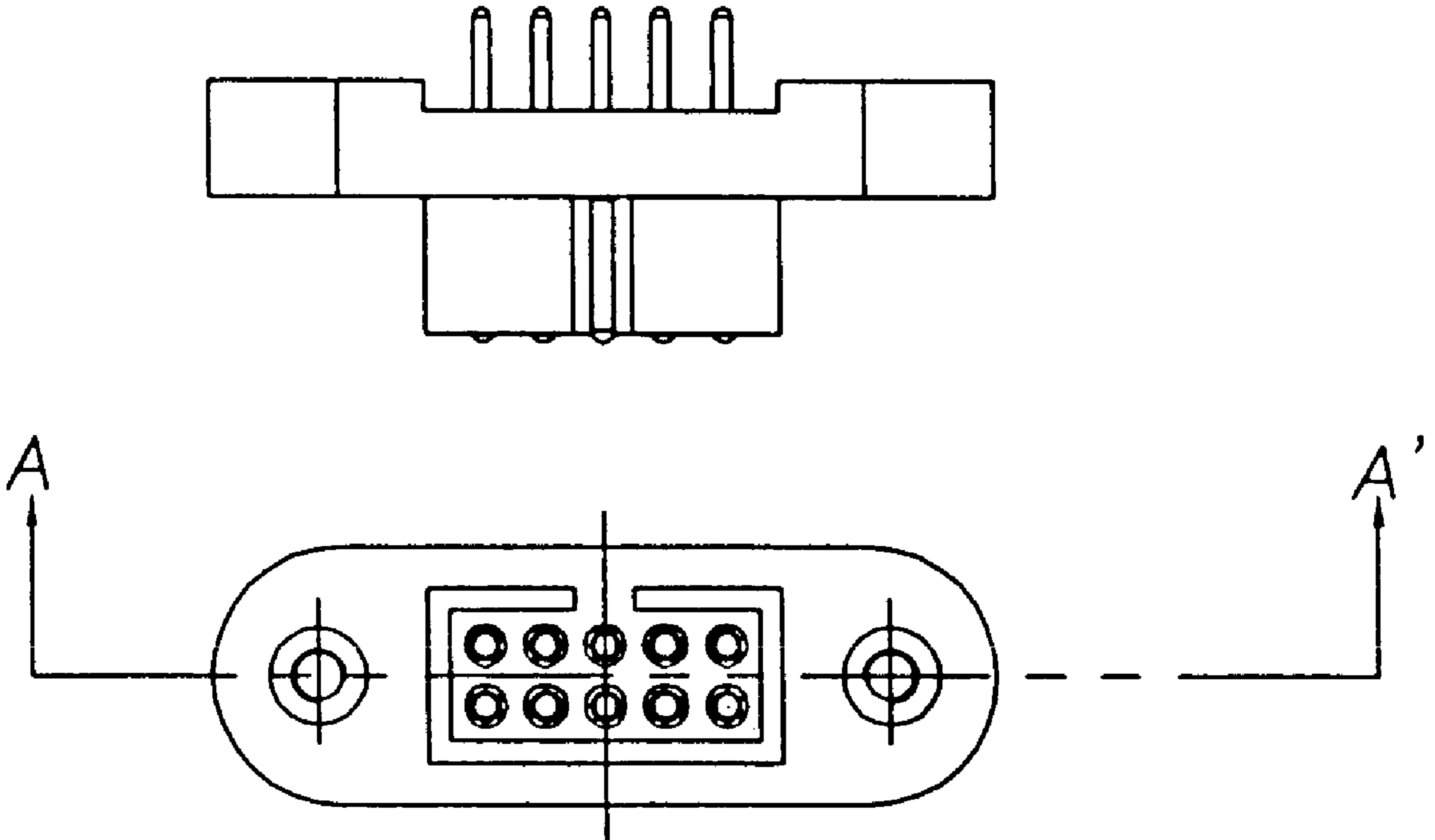
FIG. 2 is a front view thereof;

FIG. 3 is a cross-sectional view from 3 to 3 in FIG. 2;

FIG. 4 is a view from one side; and,

FIG. 5 is a view from the other side.

**1 Claim, 1 Drawing Sheet**



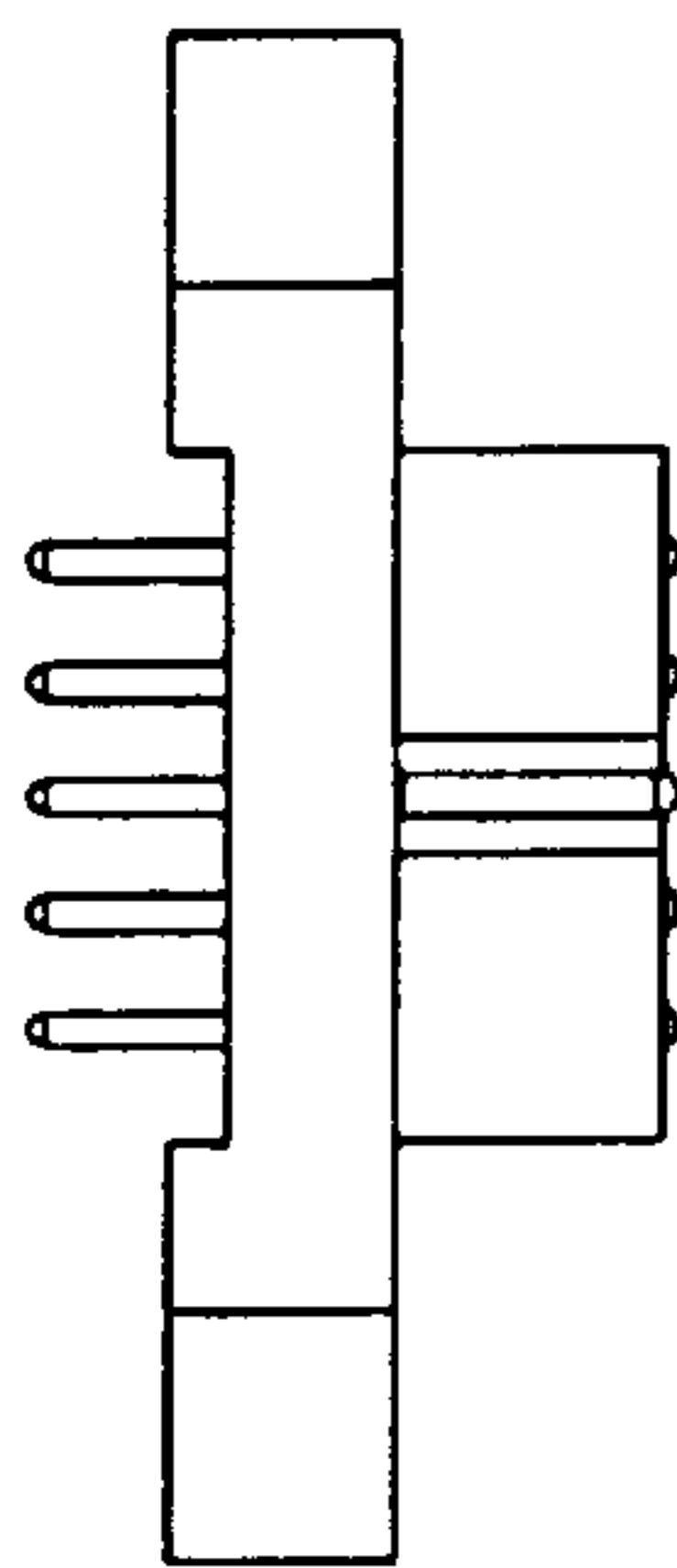


FIG. 1

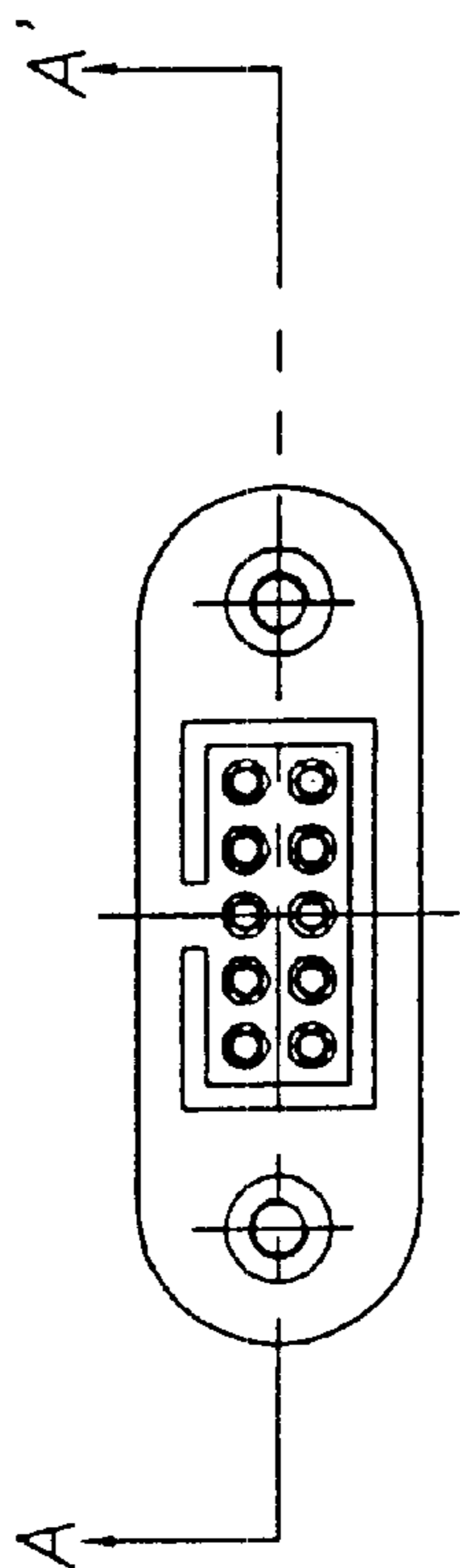


FIG. 2

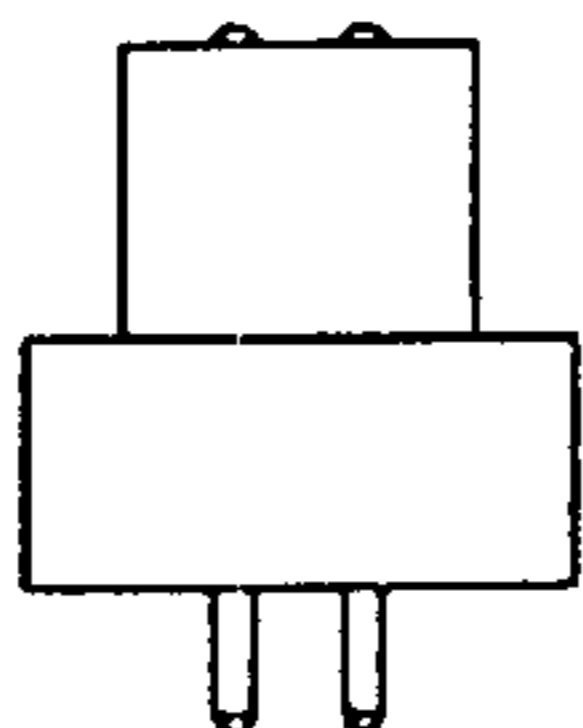


FIG. 4

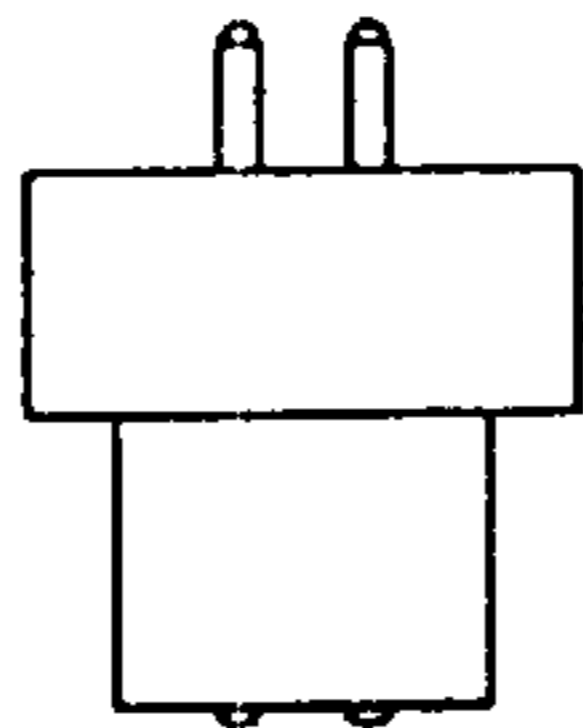


FIG. 5

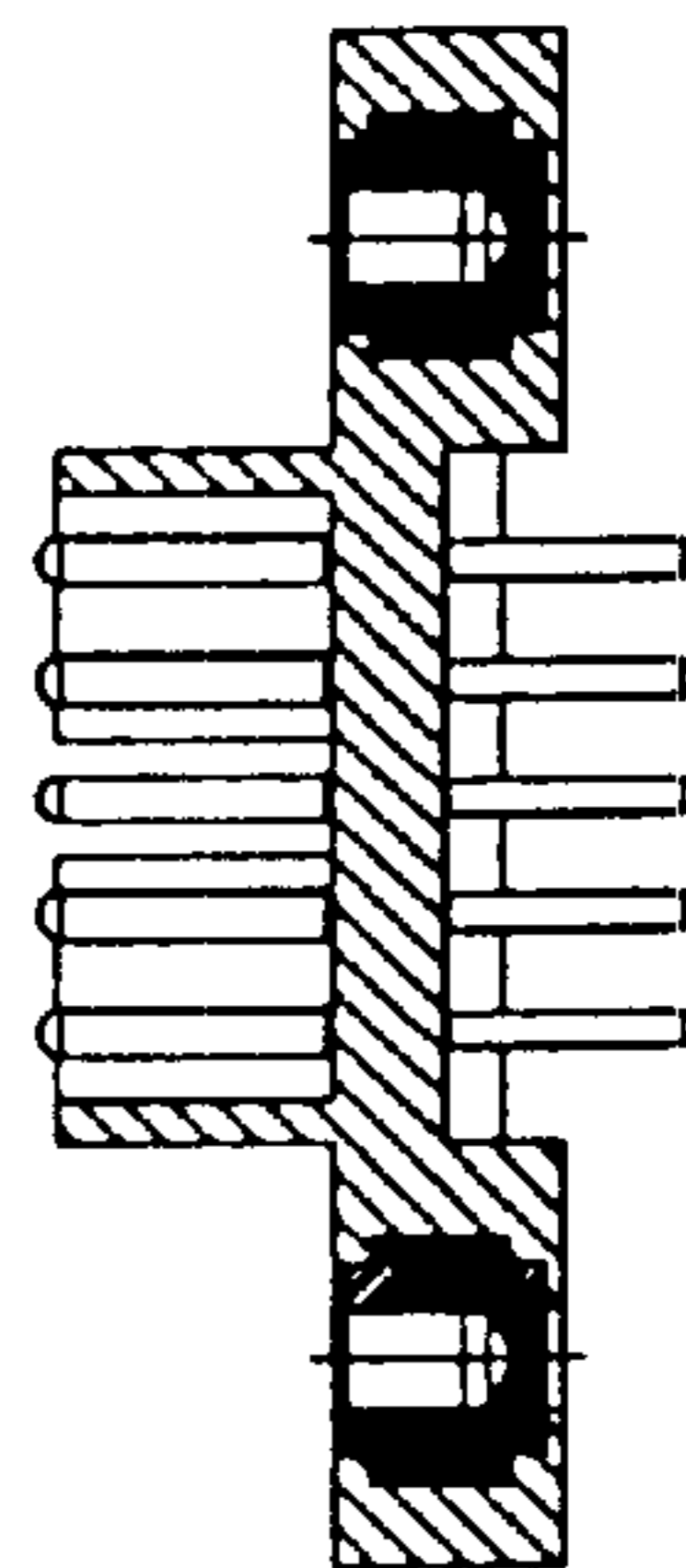


FIG. 3