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(12) **United States Design Patent**
Wasada et al.

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(54) **HYBRID INTEGRATED CIRCUIT DEVICE**

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(73) Assignee: **Taiyo Yuden Co., Ltd.**, Tokyo (JP)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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Dec. 8, 2000 (JP) 2000-038089

(51) **LOC (7) Cl. 13-03**

(52) **U.S. Cl. D13/182**

(58) **Field of Search** D13/182, 110,
D13/123, 133, 199; D10/46, 75; 29/829-832,
846; 174/250-251, 253-255, 260-261,
268; 216/13; 292/314; 361/718, 748, 752-753,
760, 783-784, 820; 411/500; 428/901;
470/27

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(57) **CLAIM**

The ornamental design for a hybrid integrated circuit device,
as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a hybrid integrated circuit device showing an embodiment of our new design;
FIG. 2 is a front elevational view thereof, which is symmetrical to a rear elevational view not shown herein;
FIG. 3 is a left side elevational view thereof;
FIG. 4 is a right side elevational view thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a bottom plan view thereof; and,
FIG. 7 is a cross sectional view, taken along the line A—A of FIG. 5.

1 Claim, 4 Drawing Sheets

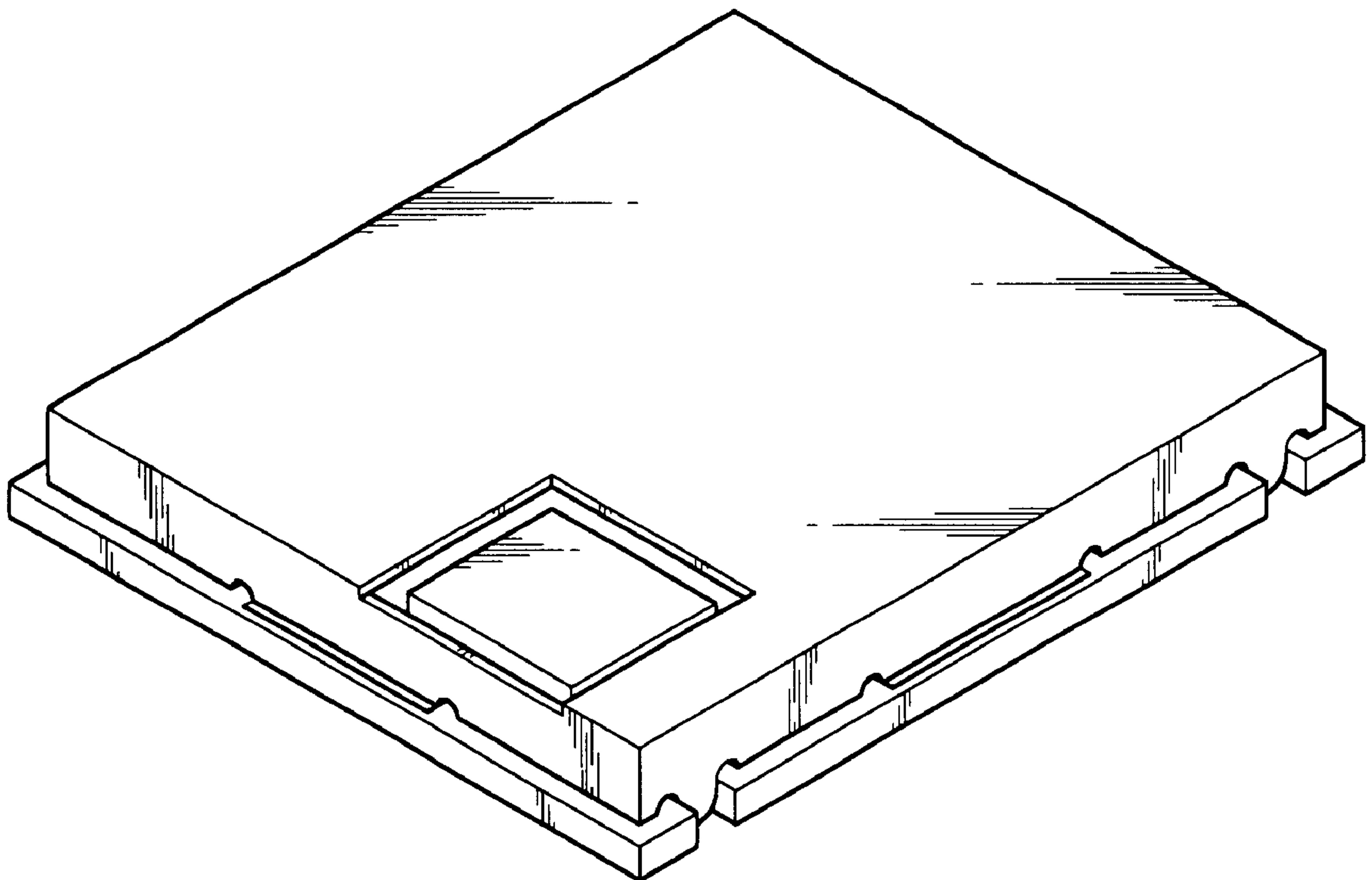


FIG. 1

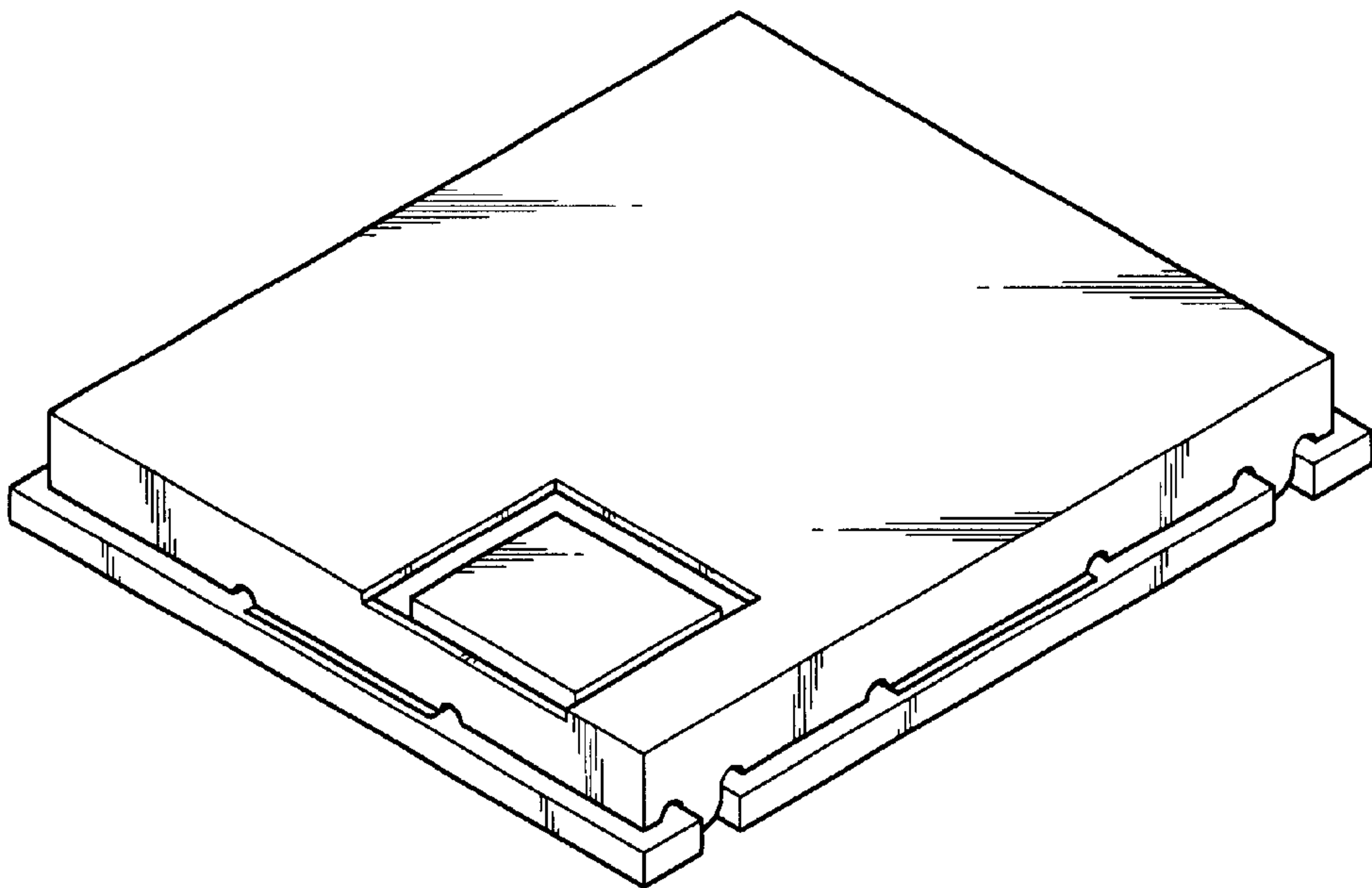


FIG. 2

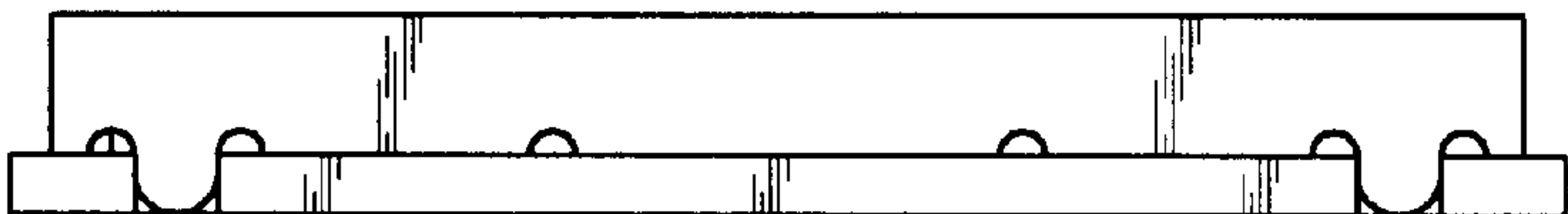


FIG. 3

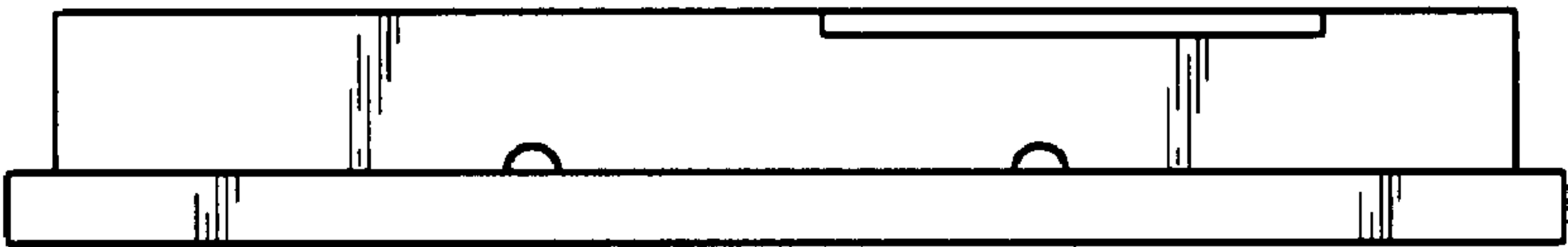


FIG. 4

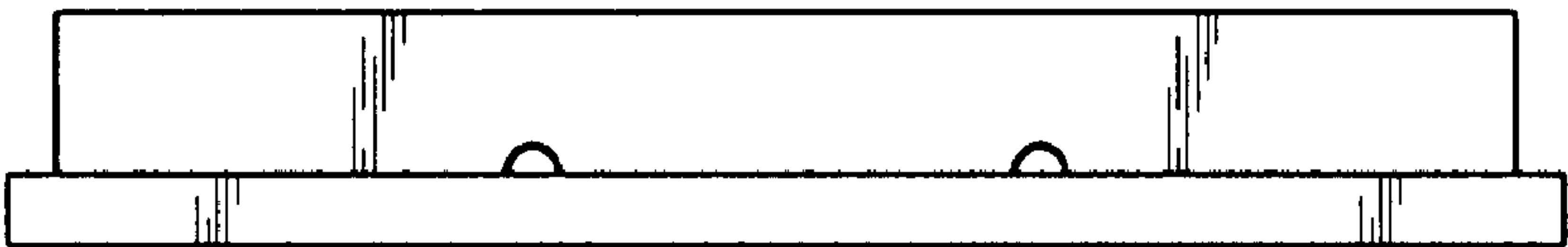


FIG. 5

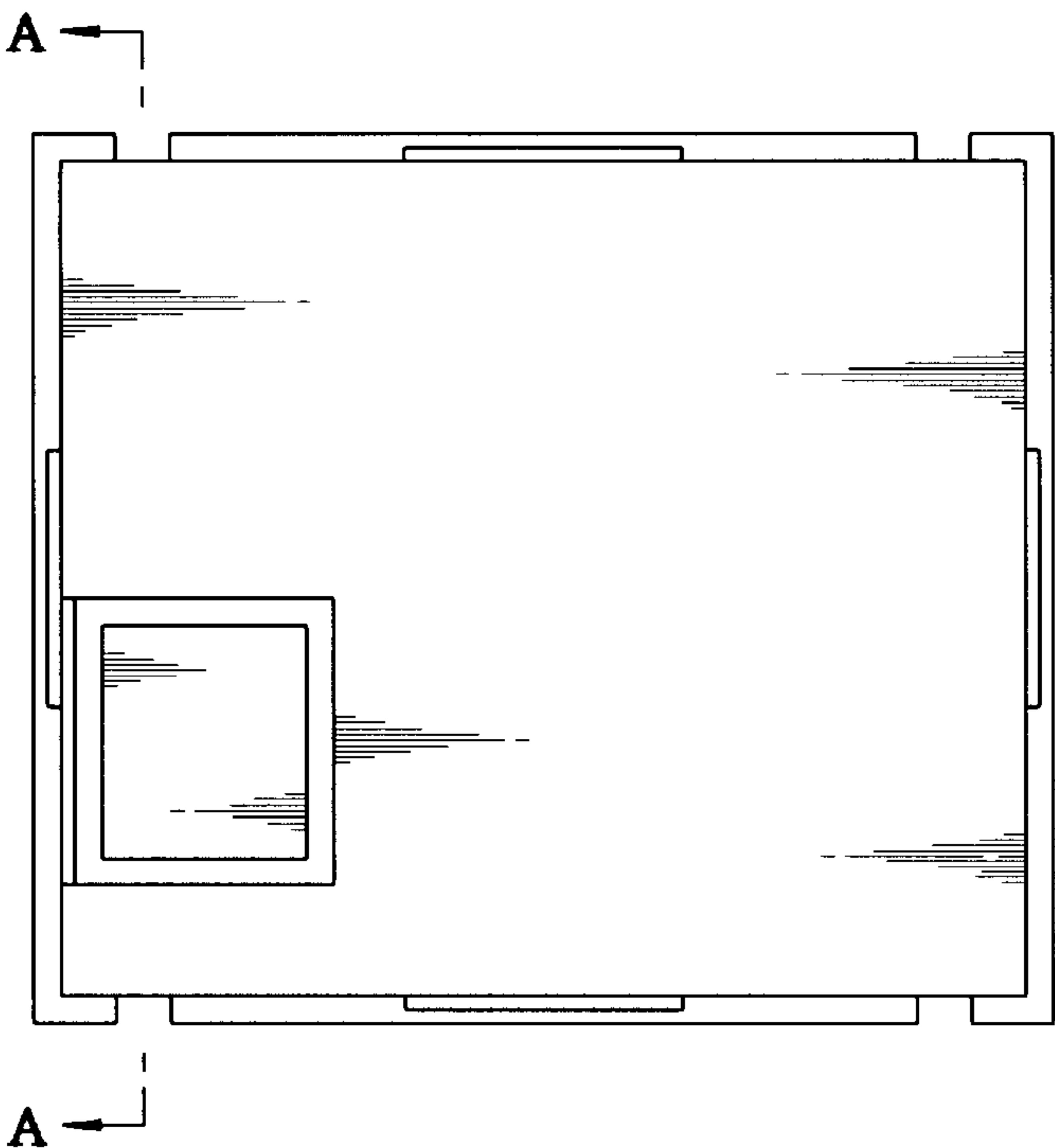


FIG. 6

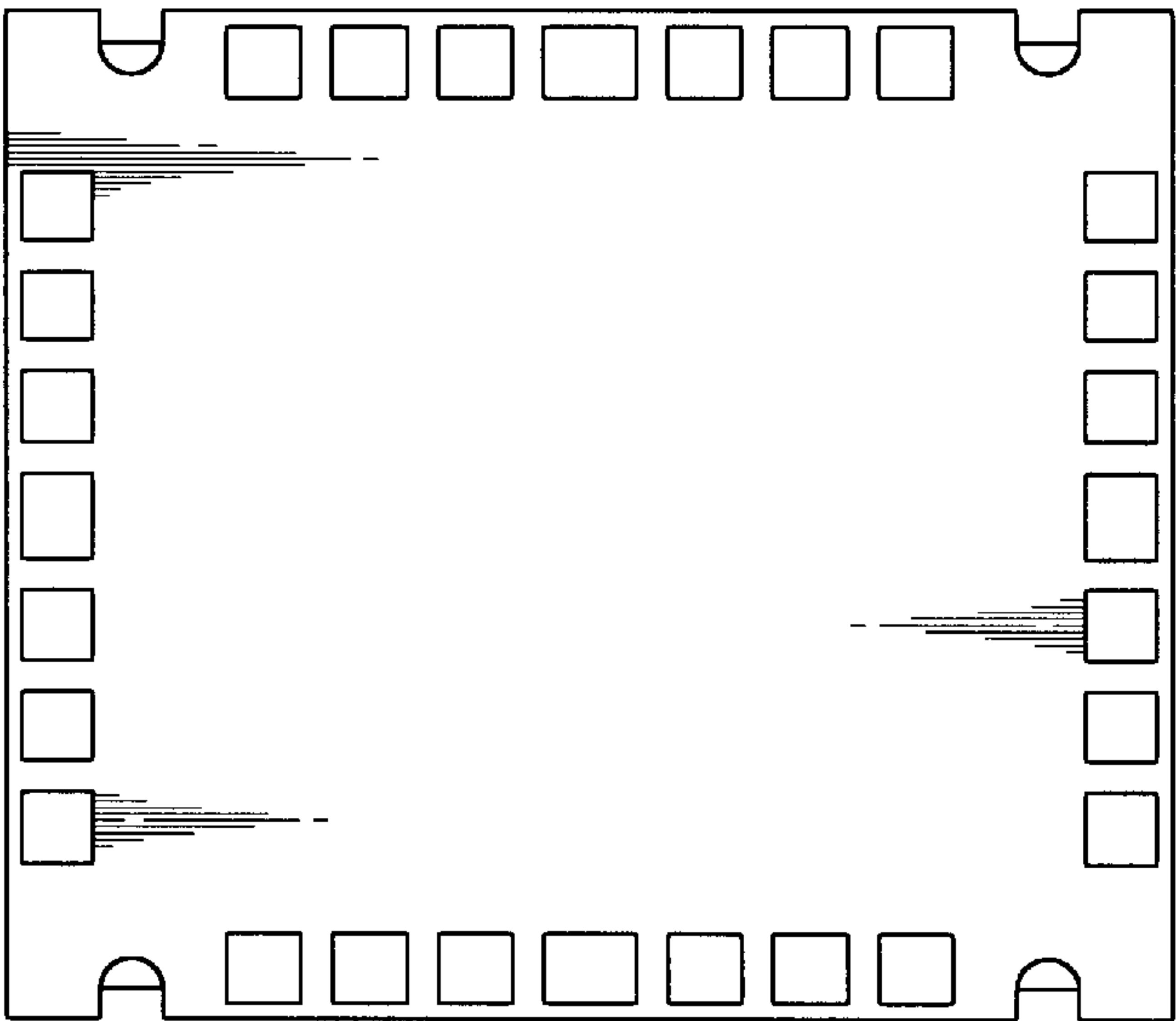


FIG. 7

