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(12) **United States Design Patent**
Lai

(10) **Patent No.:** **US D456,690 S**

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(54) **DUAL LOCKING CIRCUIT BOARD SUPPORT**

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(73) Assignee: **Richco Inc.**, Chicago, IL (US)

(**) Term: **14 Years**

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(51) **LOC (7) Cl.** **08-05**

(52) **U.S. Cl.** **D8/354**

(58) **Field of Search** D8/354, 387; D13/199;
174/138 D; 24/336; 248/222.41

(56) **References Cited**

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(57) **CLAIM**

The ornamental design for the dual locking circuit board support, as shown.

DESCRIPTION

FIG. 1 is a front elevational view of a dual locking circuit board support embodying the present invention.

FIG. 2 is a rear elevational view of the dual locking circuit board support shown in FIG. 1.

FIG. 3 is a left side view of the dual locking circuit board support shown in FIG. 1.

FIG. 4 is a right side view of the dual locking circuit board support shown in FIG. 1.

FIG. 5 is a top view of the dual locking circuit board support shown in FIG. 1; and,

FIG. 6 is bottom view of the dual locking circuit board support shown in FIG. 1.

1 Claim, 1 Drawing Sheet

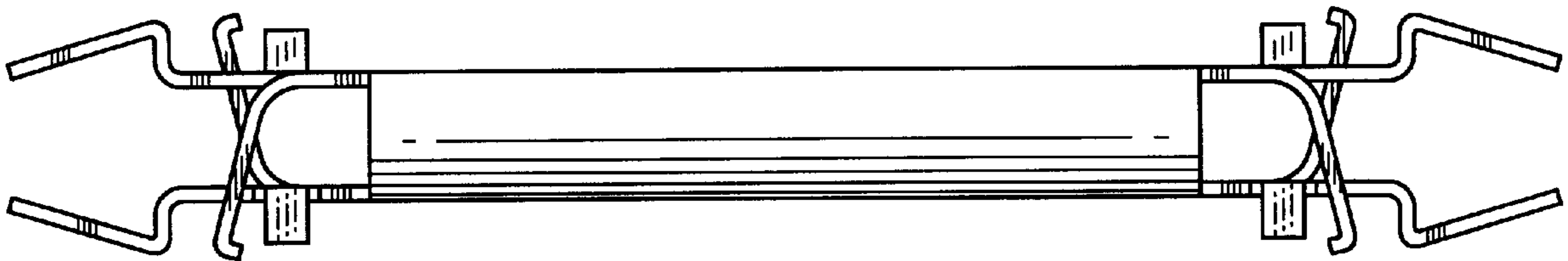


FIG. 1

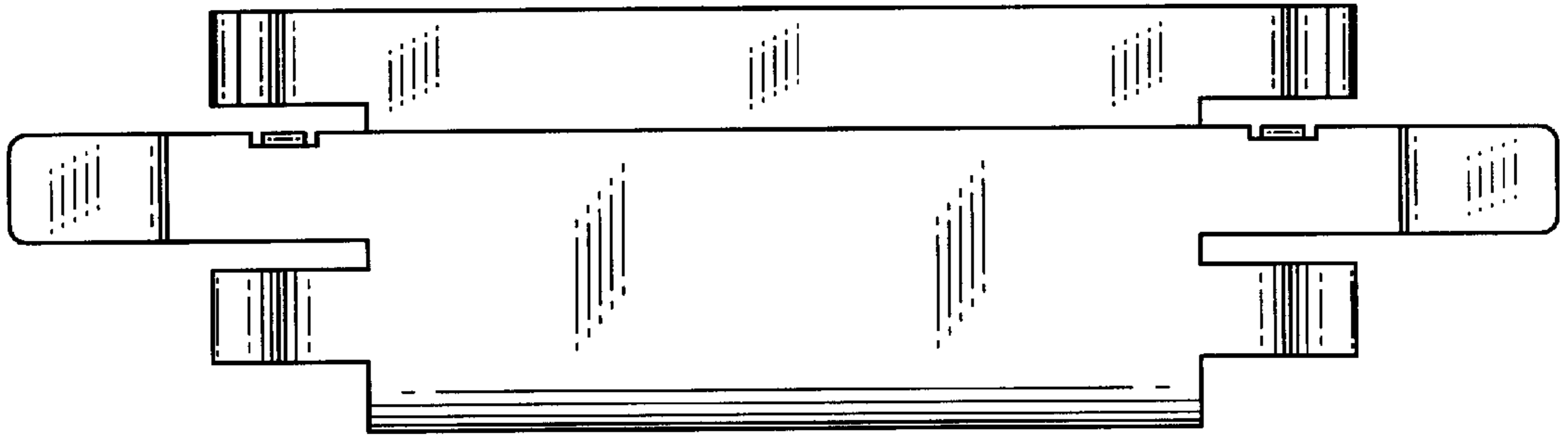


FIG. 2

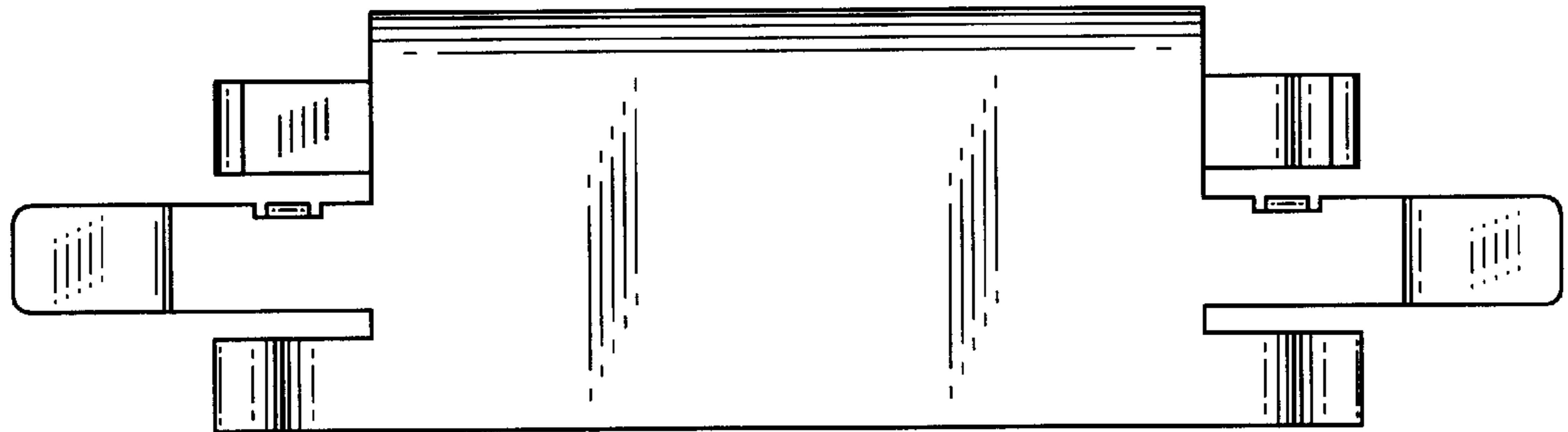


FIG. 3

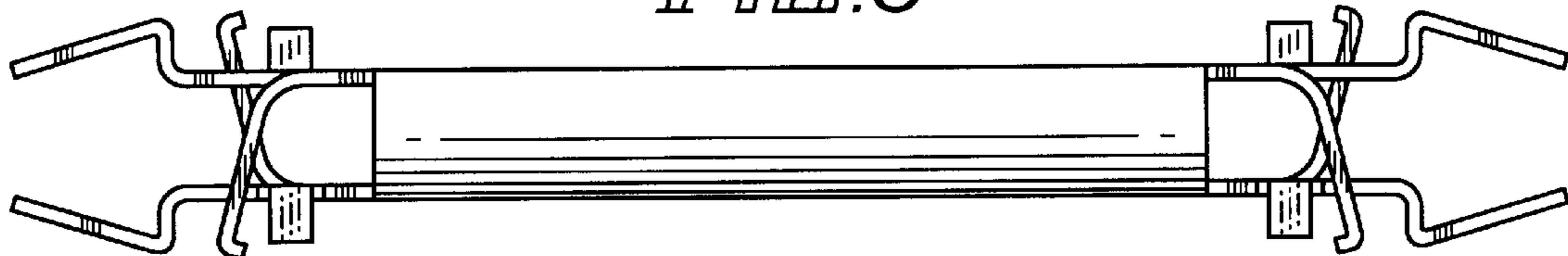


FIG. 4

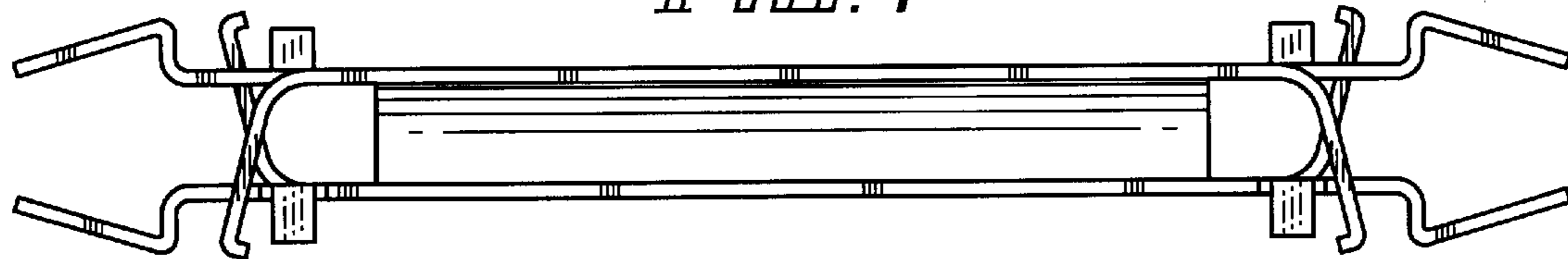


FIG. 5

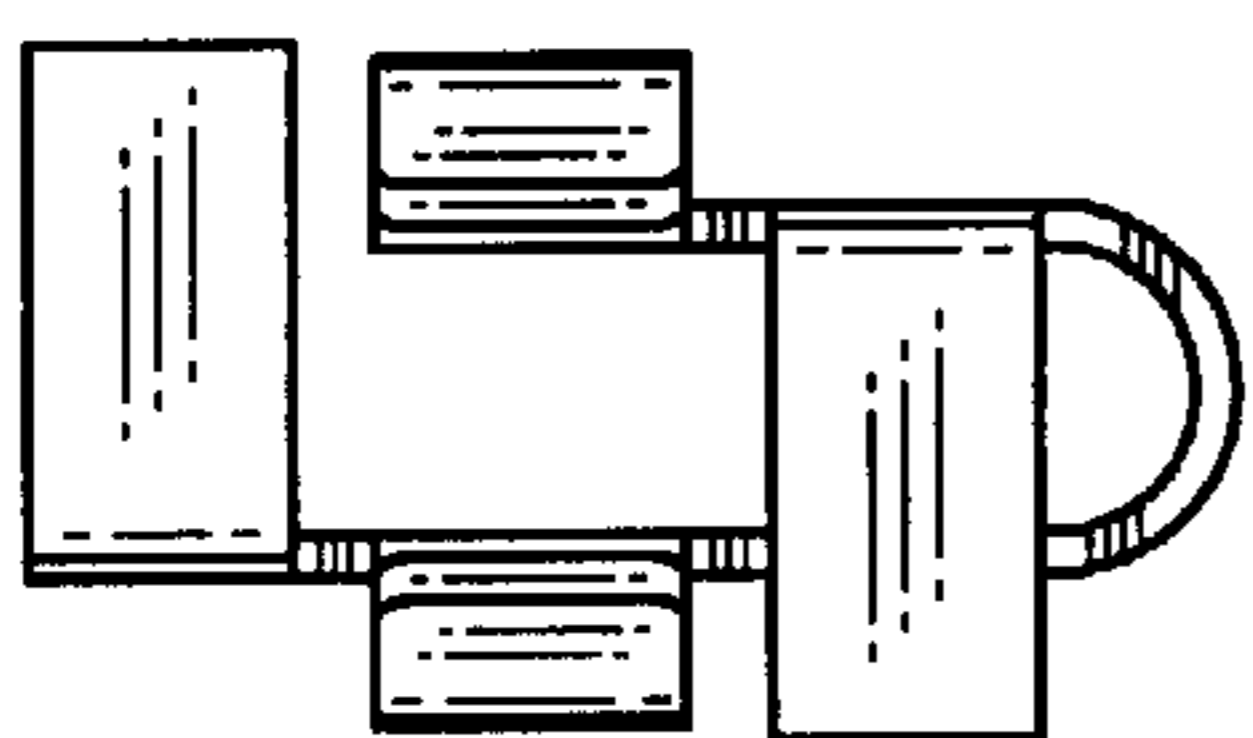


FIG. 6

