



US00D456367S

(12) **United States Design Patent**
Matteson

(10) **Patent No.:** **US D456,367 S**

(45) **Date of Patent:** **** Apr. 30, 2002**

(54) **SEMICONDUCTOR CHIP**

(75) Inventor: **Frederick M. Matteson**, Phoenix, AZ
(US)

(73) Assignee: **Protek Devices, LP**, Tempe, AZ (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/134,204**

(22) Filed: **Dec. 15, 2000**

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/105, 107,
D13/114, 110, 117, 182; 174/52.1, 52.2,
52.4, 52.5; 361/679, 728, 736, 737, 742,
752, 802, 818; 257/679; 439/328, 377

(56) **References Cited**

U.S. PATENT DOCUMENTS

D365,092 S	*	12/1995	Mundigl et al.	D14/117
D406,822 S	*	3/1999	Huber et al.	D13/182
6,013,946 A	*	1/2000	Lee et al.	257/666
D421,743 S	*	3/2000	Ueda et al.	D13/182
6,201,296 B1	*	3/2001	Fries et al.	257/679
6,211,572 B1	*	4/2001	Fjelstad et al.	257/781
D441,726 S	*	5/2001	Sofue et al.	D13/182
D441,727 S	*	5/2001	Sekimoto	D13/182
6,297,561 B1	*	10/2001	Liu et al.	257/779

6,300,685 B1	*	10/2001	Hasegawa et al.	257/780
6,303,982 B2	*	10/2001	Murakami et al.	257/666
6,307,269 B1	*	10/2001	Akiyama et al.	257/778

* cited by examiner

Primary Examiner—Ted Shooman

Assistant Examiner—Selina Sikder

(74) *Attorney, Agent, or Firm*—Cahill, Sutton & Thomas P.L.C.

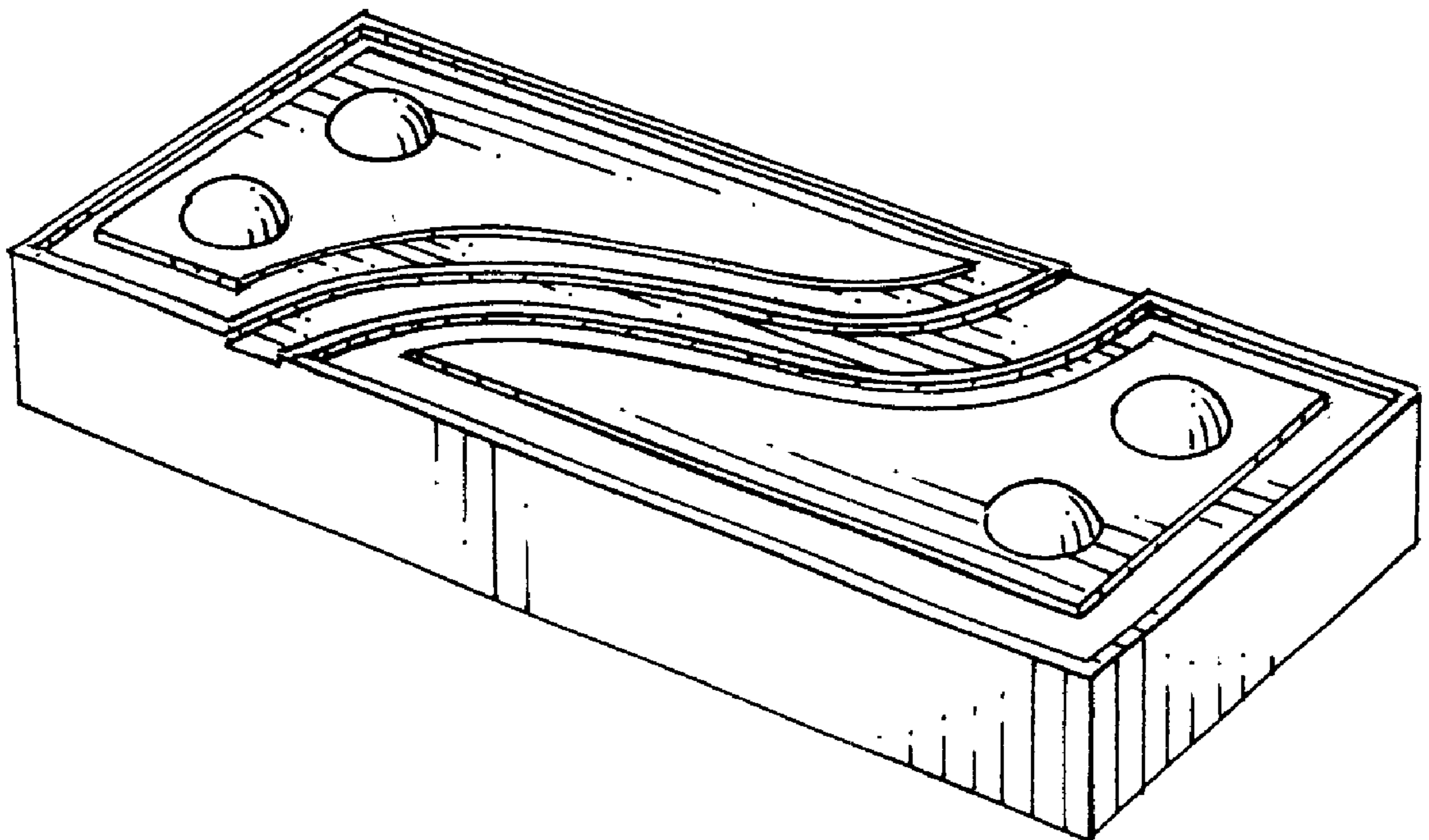
(57) **CLAIM**

The ornamental design for a semiconductor chip, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a semiconductor chip illustrating my new design;
 FIG. 2 is a top view thereof;
 FIG. 3 is a front elevational view showing one side of the semiconductor chip;
 FIG. 4 is an end view of the semiconductor chip of FIG. 2 showing the left end thereof;
 FIG. 5 is an end view of the semiconductor chip of FIG. 2 showing the right end thereof;
 FIG. 6 is a side elevational view of the semiconductor chip of FIG. 2 showing a side opposite to that shown in FIG. 3;
 and,
 FIG. 7 is a bottom view thereof.

1 Claim, 1 Drawing Sheet



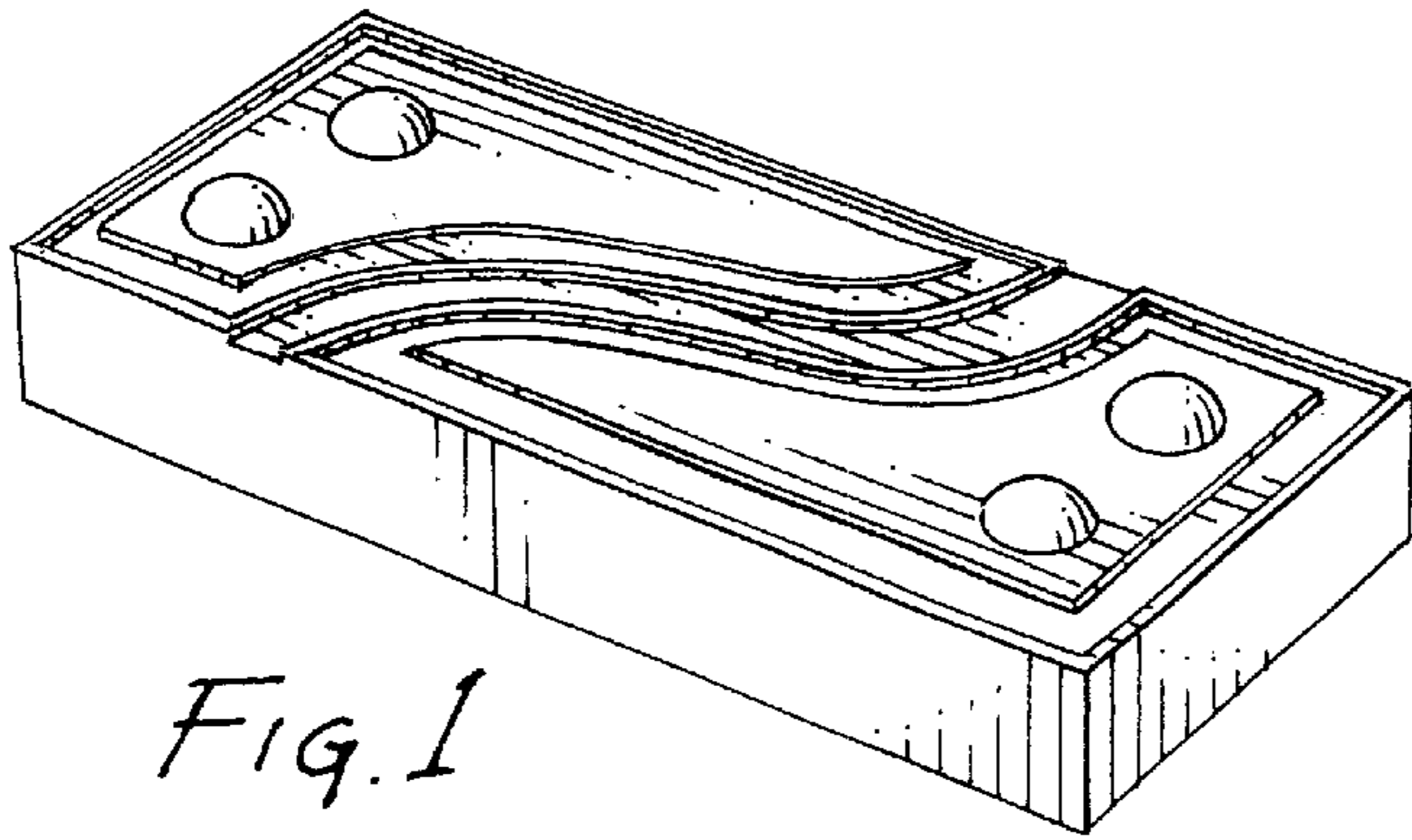


FIG. 1

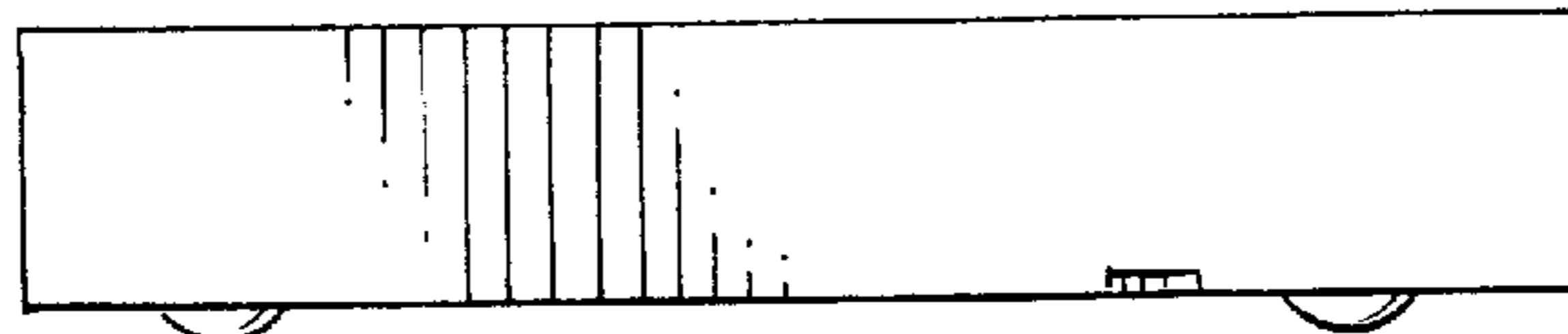


FIG. 3

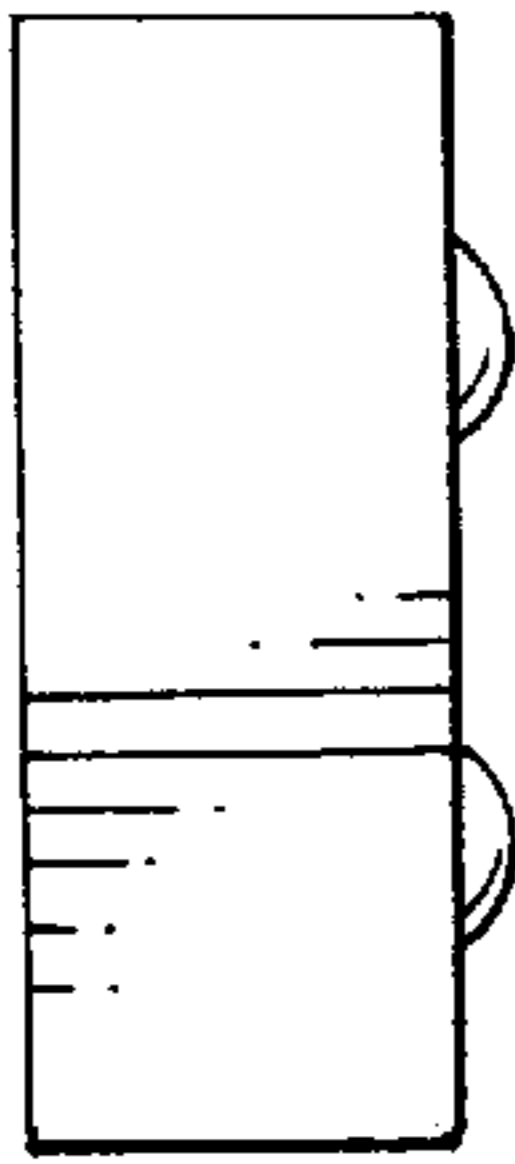


FIG. 4

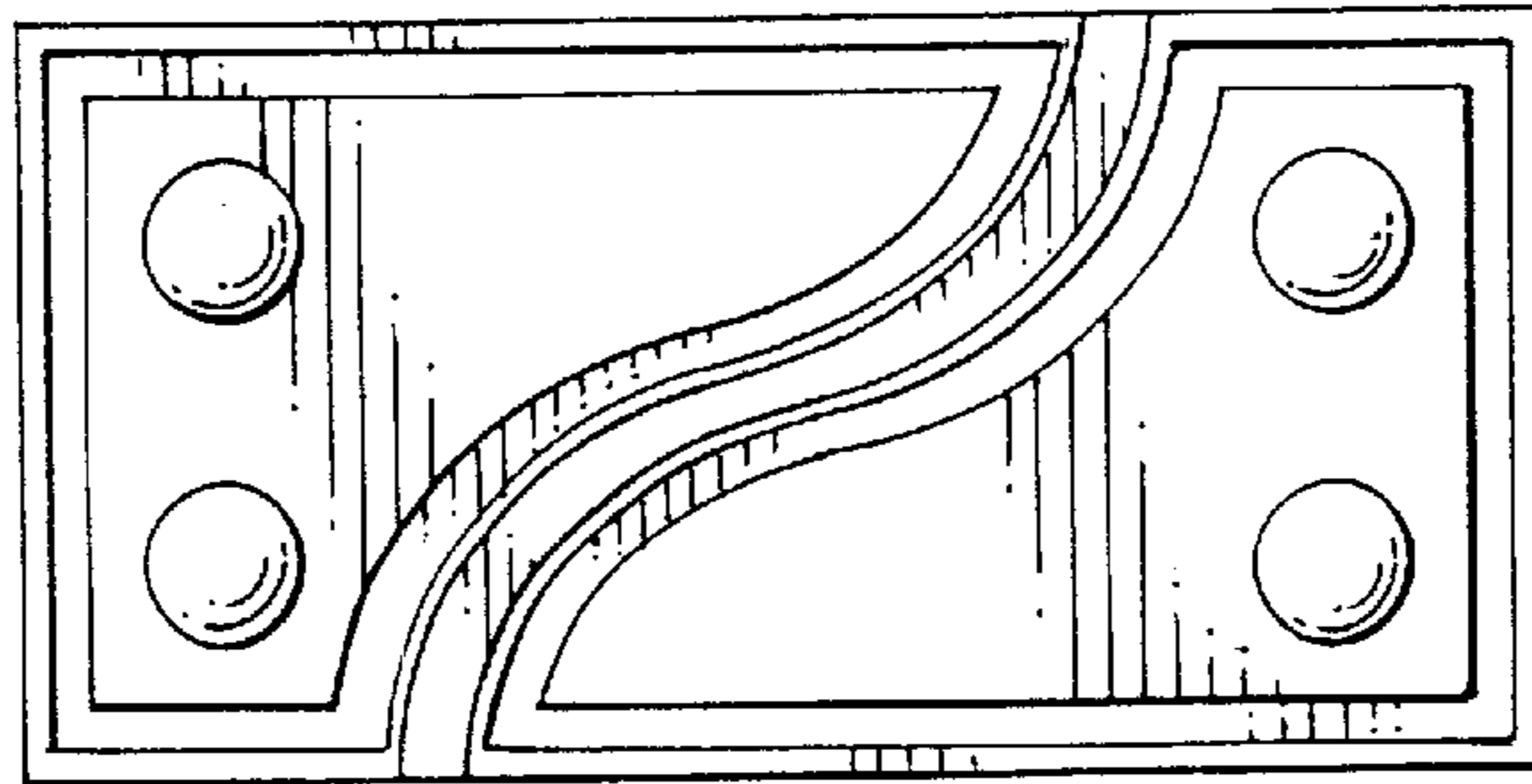


FIG. 2

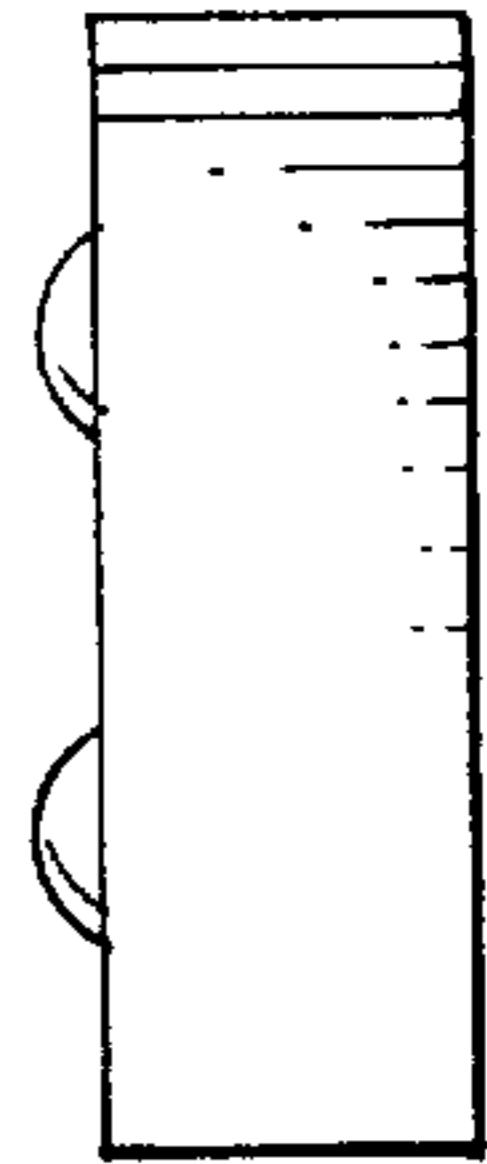


FIG. 5

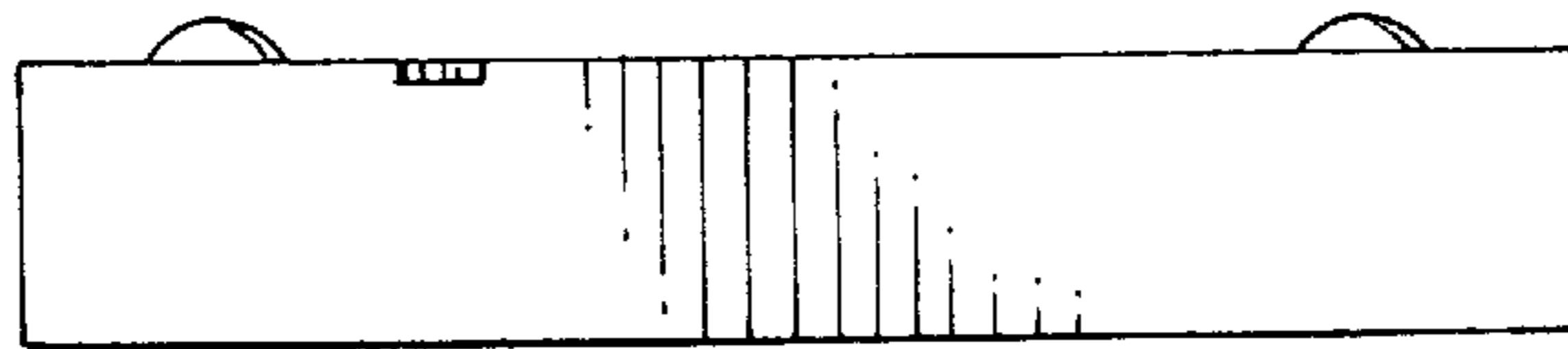


FIG. 6

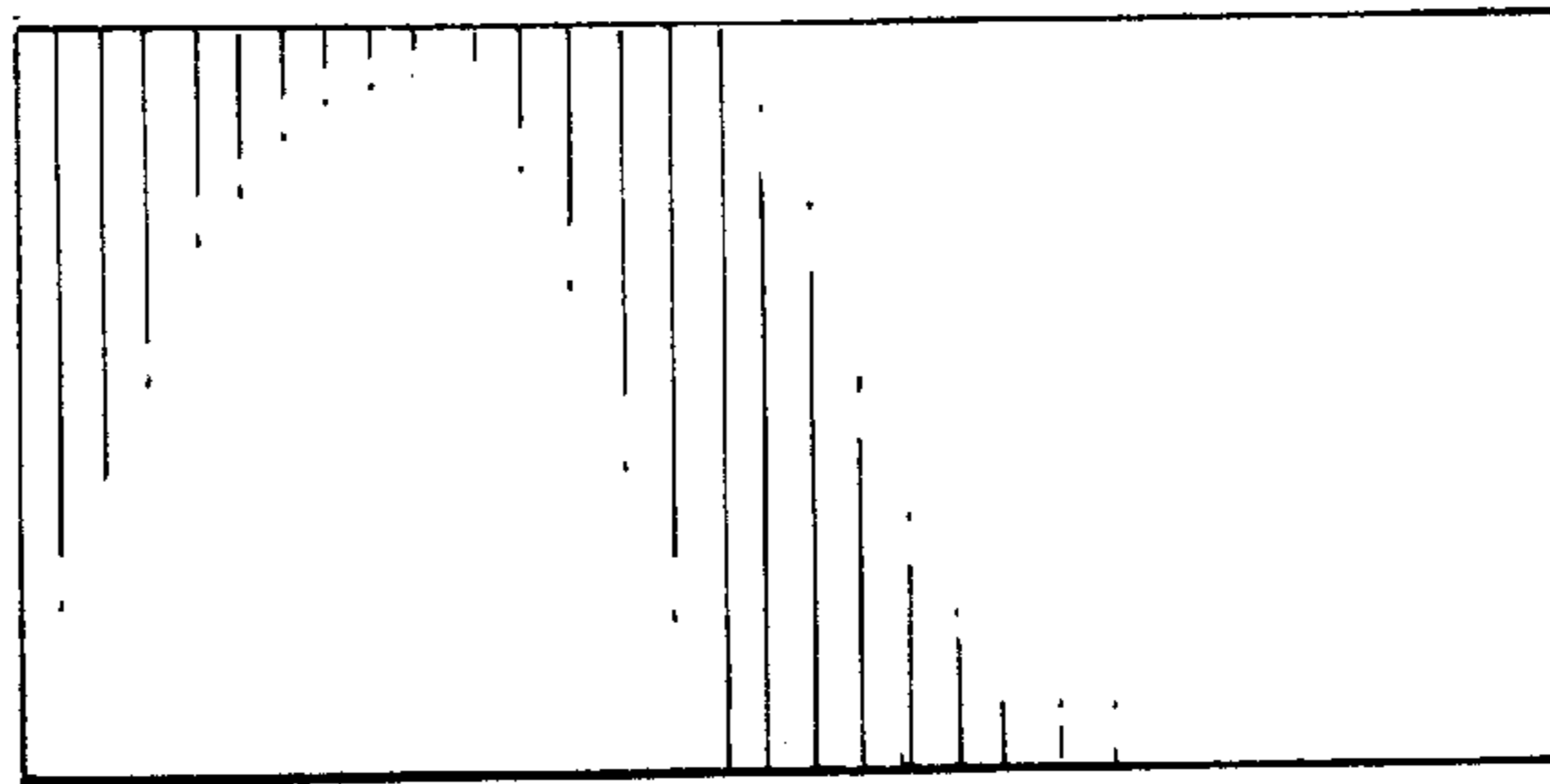


FIG. 7