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(12) **United States Design Patent** (10) **Patent No.:** **US D449,032 S**
Kiesekamp et al. (45) **Date of Patent:** **** Oct. 9, 2001**

(54) **FACEPLATE FOR A CIRCUIT PACK**

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(73) Assignee: **Alcatel Canada Inc.** (CA)

(**) Term: **14 Years**

(21) Appl. No.: **29/119,093**

(22) Filed: **Feb. 23, 2000**

(51) **LOC (7) Cl.** **13-99**

(52) **U.S. Cl.** **D13/184; D13/177**

(58) **Field of Search** **D13/158-160, D13/173, 184, 177; 361/600, 627, 640, 679, 728, 730, 829, 937**

(56) **References Cited**

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Primary Examiner—Brian N. Vinson

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(57) **CLAIM**

The ornamental design of a faceplate for a circuit pack, as shown and described.

DESCRIPTION

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FIG. 1 is a left elevational view of a first embodiment of the faceplate for a circuit pack;

FIG. 2 is a right elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a top elevational view thereof, the bottom elevational view being a mirror view of said top elevational view;

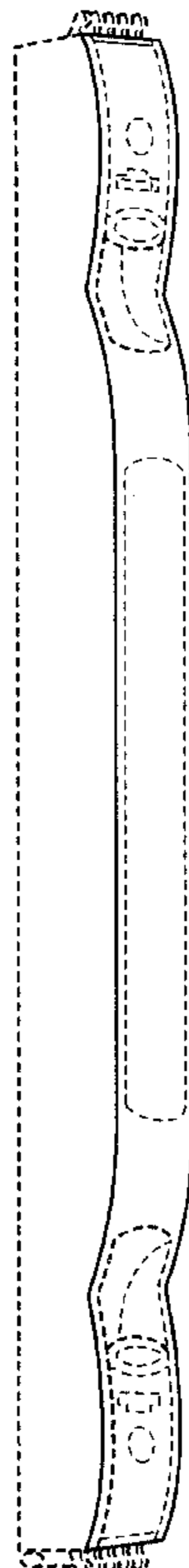
FIG. 5 is a front elevational view thereof;

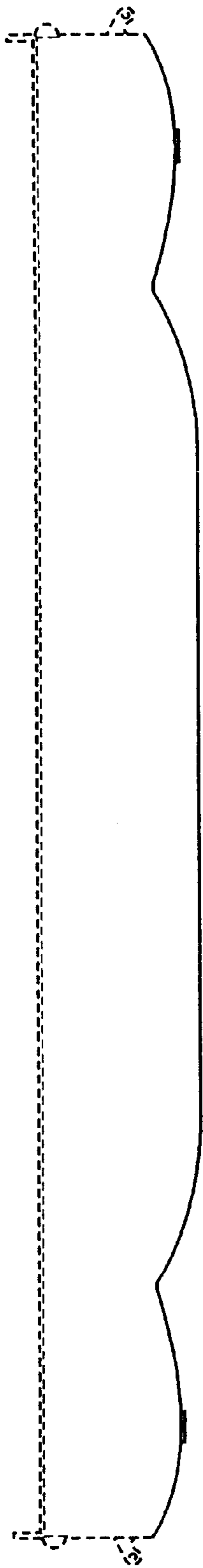
FIG. 6 is a front isometric view thereof; and,

FIG. 7 is a front isometric view of a second embodiment of the faceplate for a circuit pack, showing said design of FIGS. 1 to 7 when disposed in two sets at the front of a shelf for circuit packs.

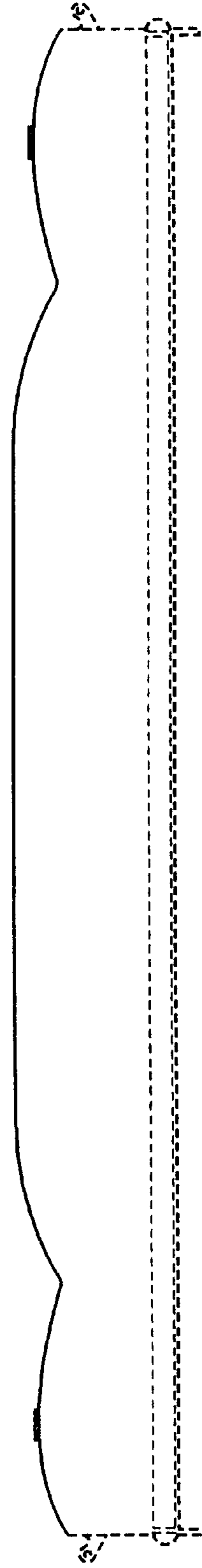
The broken line showings of FIGS. 1 to 7 are for illustrative purposes only and form no part of the claimed design.

1 Claim, 4 Drawing Sheets

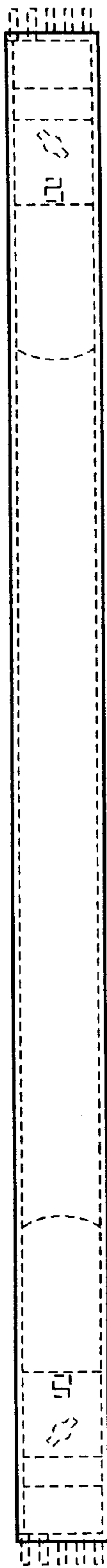




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Figure 1



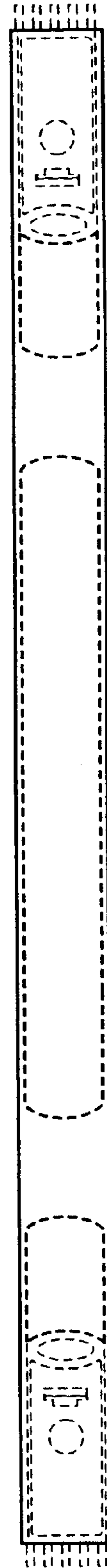
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Figure 2



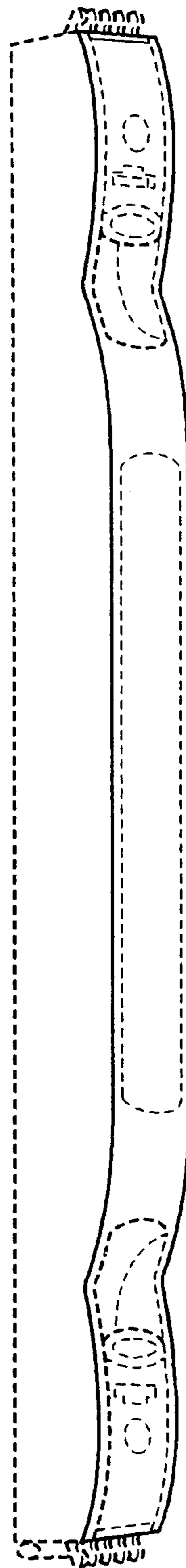
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Figure 3



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Figure 4

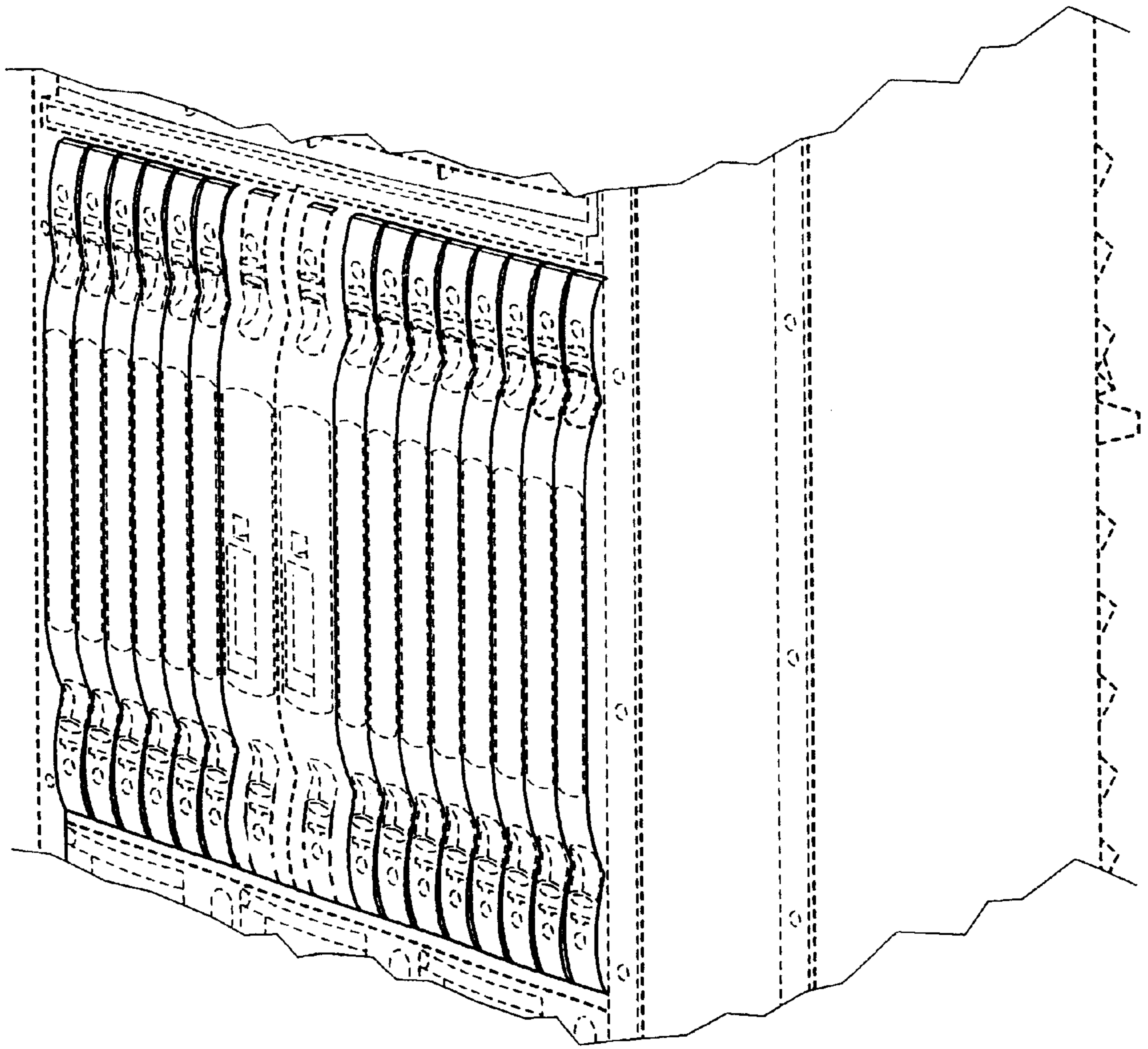


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Figure 5



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Figure 6



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Figure 7