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(12) **United States Design Patent**
Sekimoto

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(54) **SEMICONDUCTOR ELEMENT**

D. 427,977 * 7/2000 Takizawa et al. D13/182

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OTHER PUBLICATIONS

(73) Assignee: **Sony Corporation**, Tokyo (JP)

Catalog "Semiconductor '97 Package Manual," "AS-A3," Sony, 1997.

(**) Term: **14 Years**

* cited by examiner

(21) Appl. No.: **29/130,541**

Primary Examiner—Brian N. Vinson

(22) Filed: **Oct. 4, 2000**

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(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/182; 174/524,
174/525; 257/690; 361/752, 798, 820, 748,
736, 737, 761

(57) **CLAIM**

The ornamental design for a semiconductor element, as shown and described.

(56) **References Cited**

DESCRIPTION

U.S. PATENT DOCUMENTS

D. 288,557	*	3/1987	Bois	D13/182
D. 359,028	*	6/1995	Siegel et al.	D13/182
D. 395,423		6/1998	Koyama et al.	D13/182
D. 395,638		6/1998	Koyama et al.	D13/182
D. 396,696		8/1998	Takagi et al.	D13/182
D. 418,485	*	1/2000	Kawafuji et al.	D13/182

FIG. 1 is a top, front perspective view of a semiconductor element showing my new design;

FIG. 2 is a front elevational view thereof; and,

FIG. 3 is a rear elevational view thereof.

Portions in broken lines are for illustrative purposes only and form no part of claimed design.

1 Claim, 2 Drawing Sheets

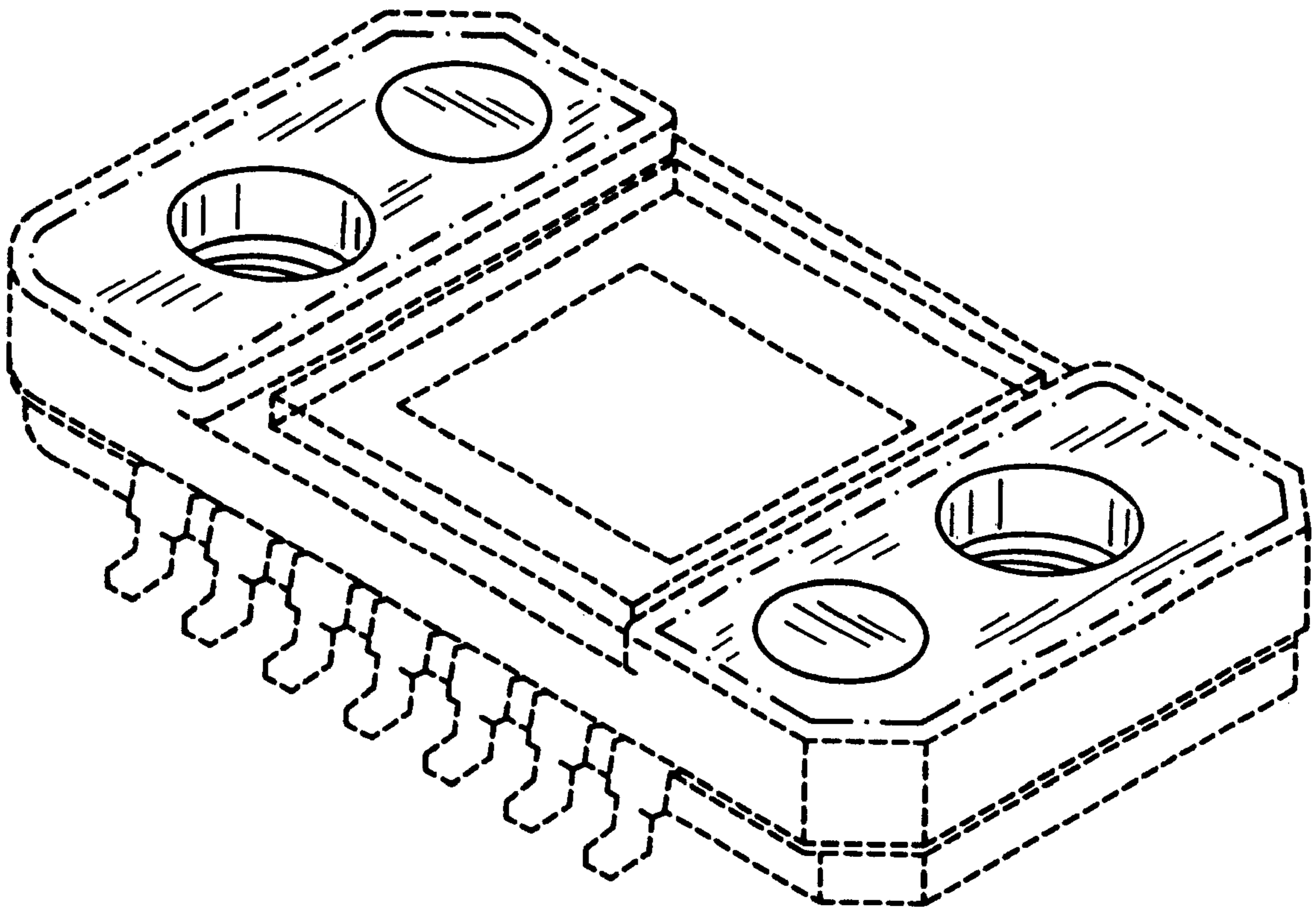


FIG. 1

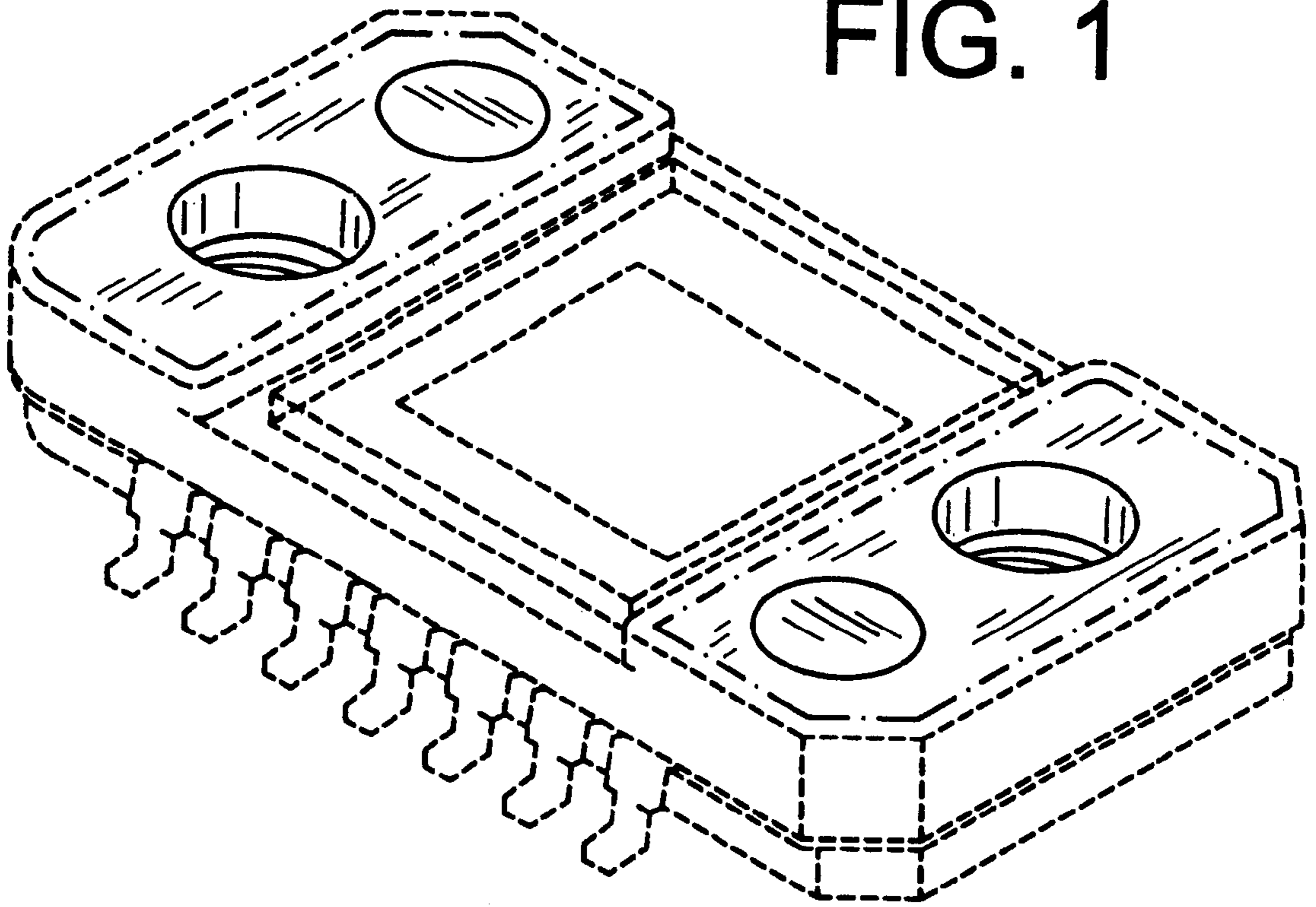


FIG. 2

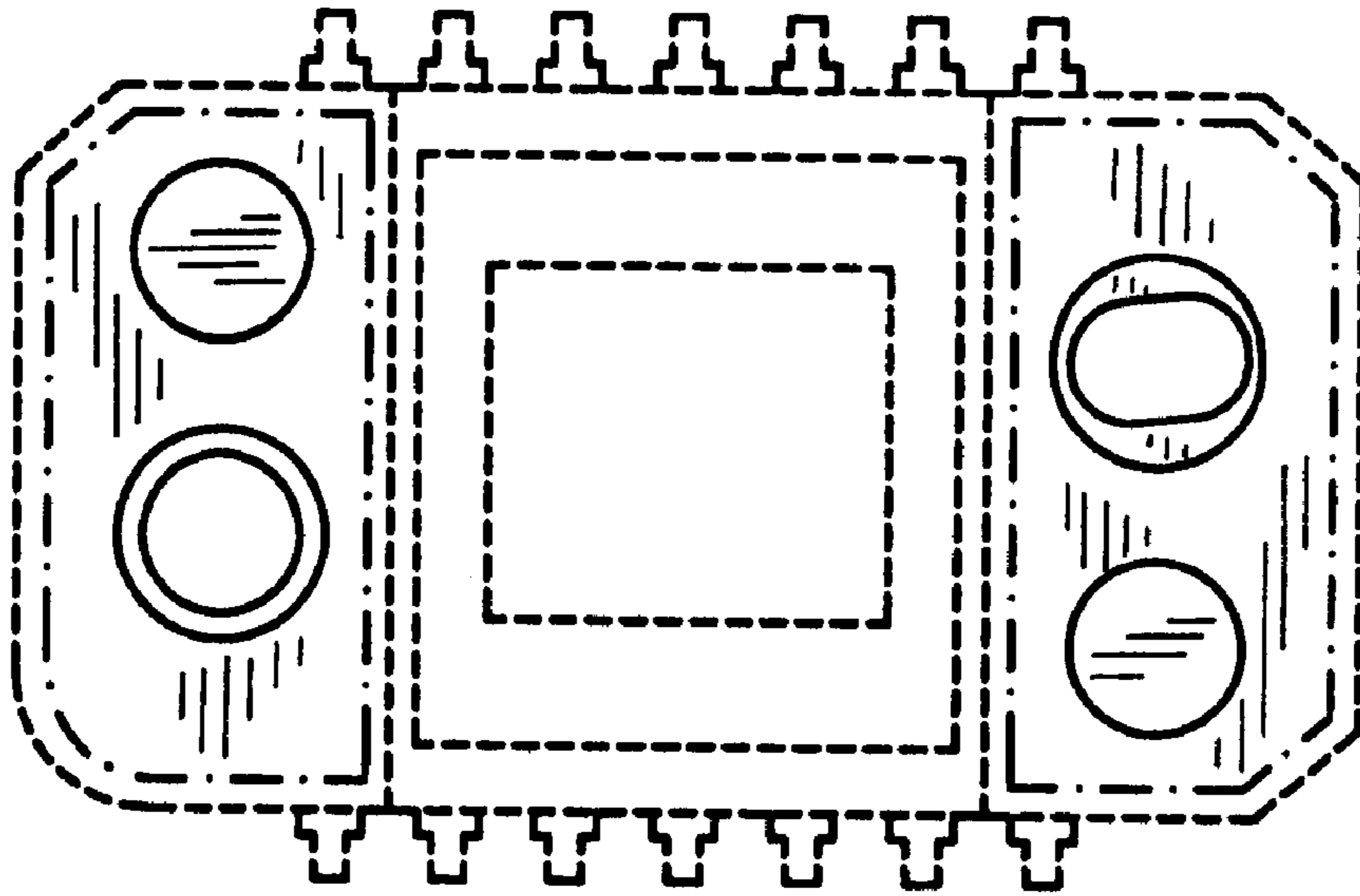


FIG. 3

