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(12) **United States Design Patent**
Wrisley et al.

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(54) **PROCESSOR AND DISPLAY MODULE FOR
A MODULAR MEASUREMENT
INSTRUMENT**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

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(51) **LOC (7) Cl.** **10-04**

(52) **U.S. Cl.** **D10/78**

(58) **Field of Search** D10/46, 75, 78,
D10/81; D14/100; 73/336.5, 431; 128/630,
745, 746; 340/573.1, 578.3, 601, 576, 521;
600/301, 300, 368; 364/413.07, 413.02,
413.08, 413.09; 324/158

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(57) **CLAIM**

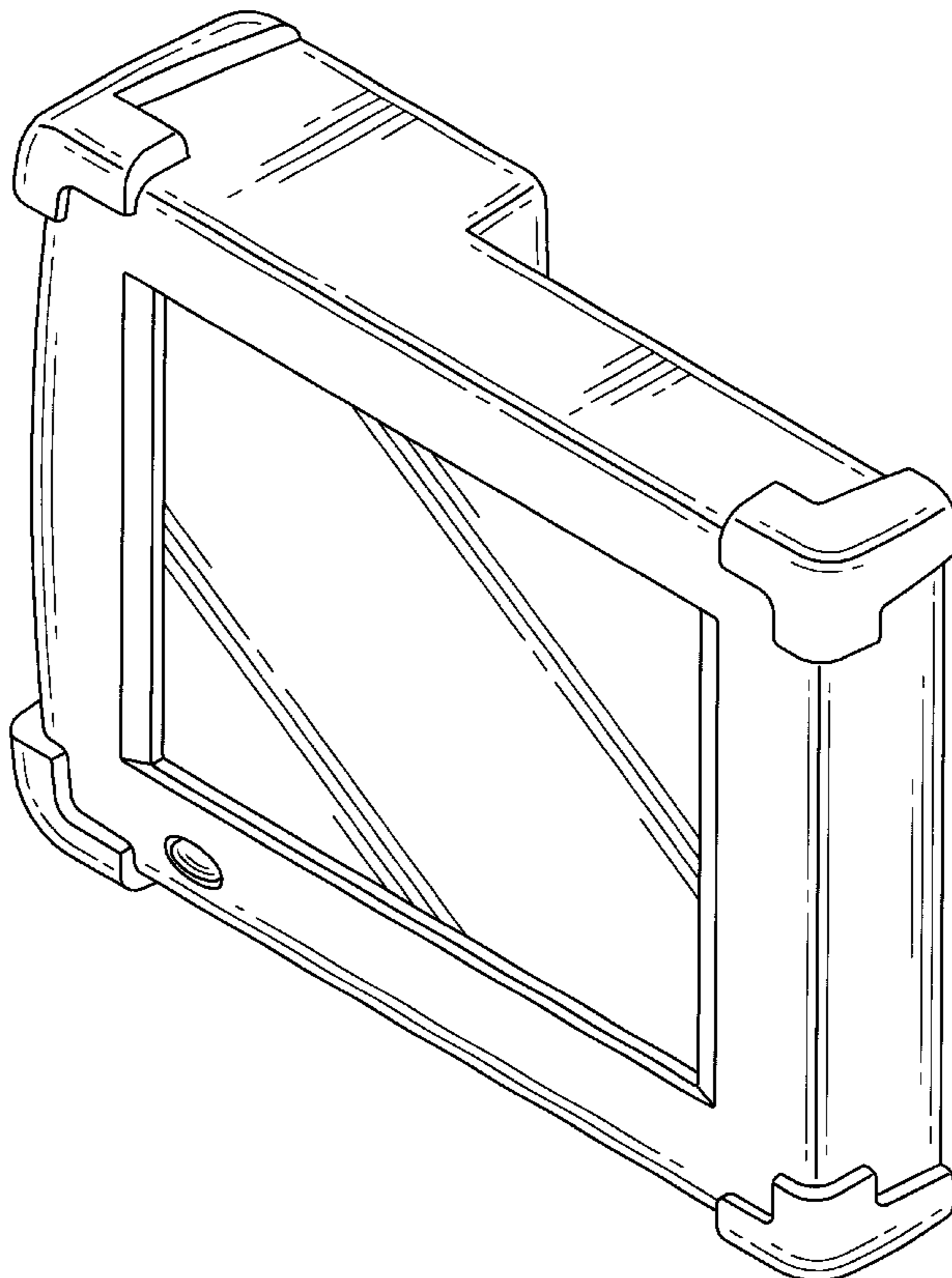
The ornamental design for a processor and display module for a modular measurement instrument, as shown and described.

DESCRIPTION

The ornamental design disclosed in this application is for a processor and display module for a modular for a modular measurement instrument having raised bumper guards at the corners of the module, and a bi-level rear surface.

FIG. 1 is a perspective view of a processor and display module for a modular measurement instrument;
 FIG. 2 is a front elevation view of the processor and display module for a modular measurement instrument;
 FIG. 3 is a rear elevation view of the processor and display module for a modular measurement instrument;
 FIG. 4 is a left side elevation view of the processor and display module for a modular measurement instrument;
 FIG. 5 is a right side elevation view of the processor and display module for a modular measurement instrument.
 FIG. 6 is a top plan view of the processor and display module for a modular measurement instrument; and,
 FIG. 7 is a bottom plan view of the processor and display module for a modular measurement instrument.

1 Claim, 5 Drawing Sheets



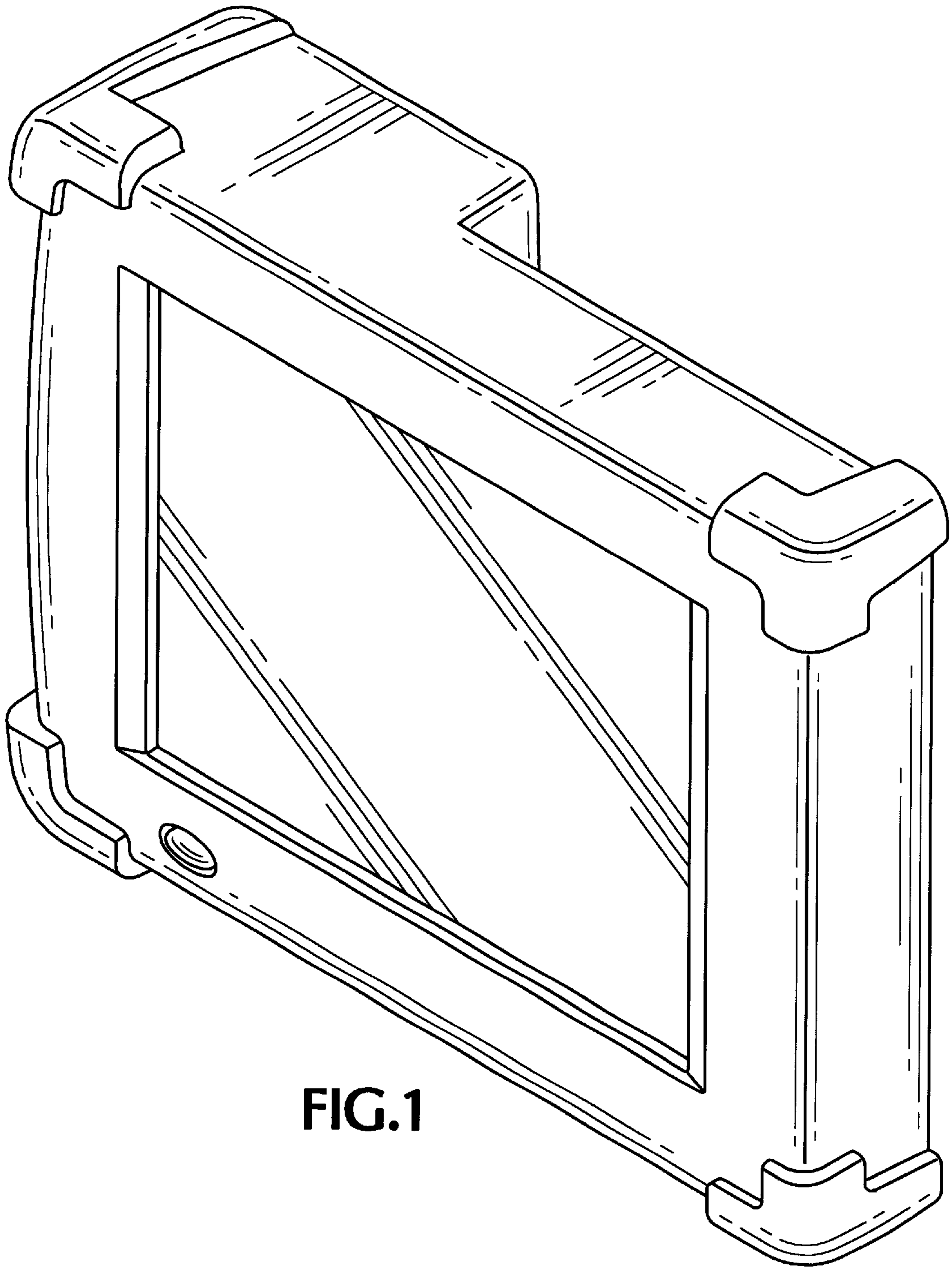


FIG.1

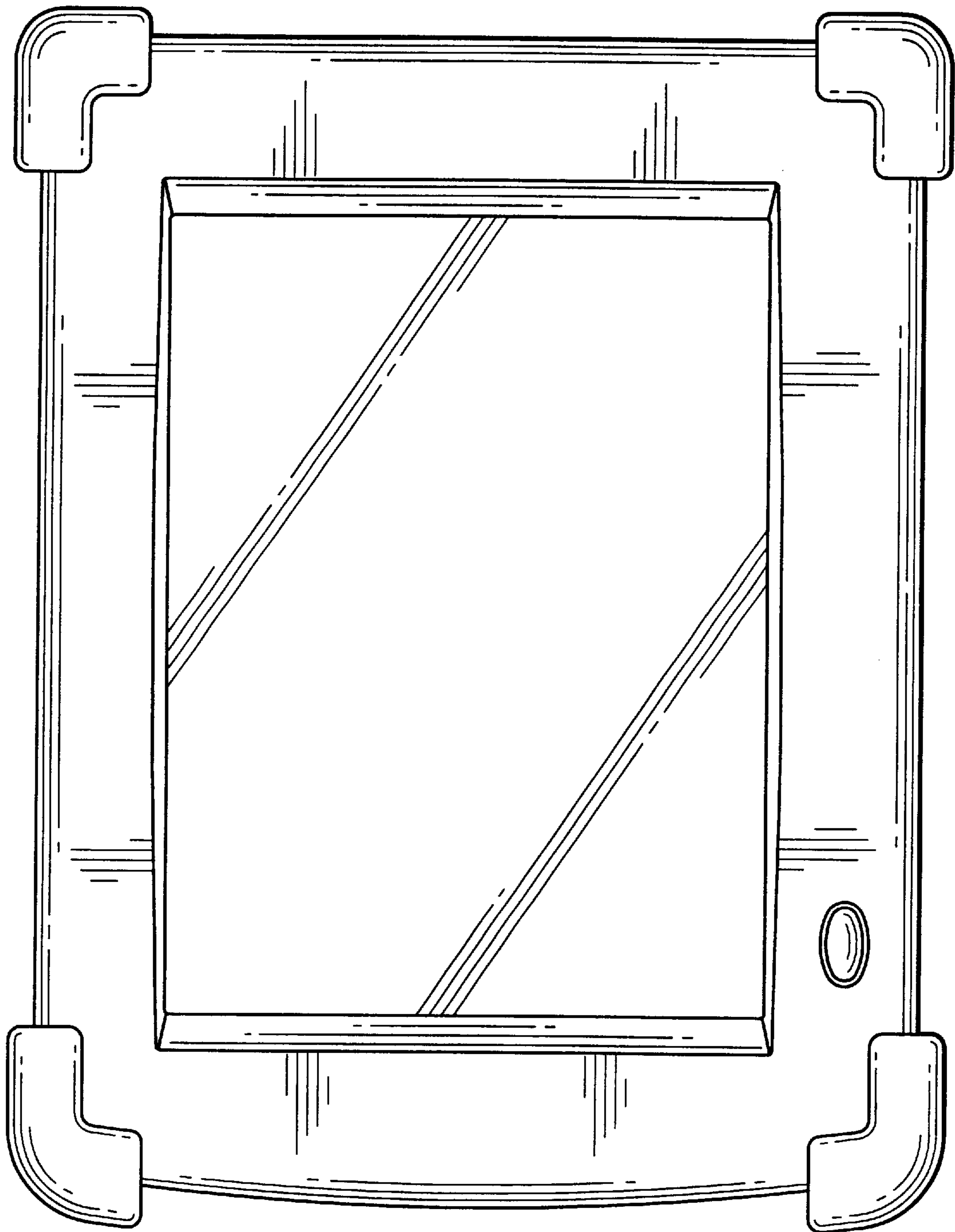


FIG.2

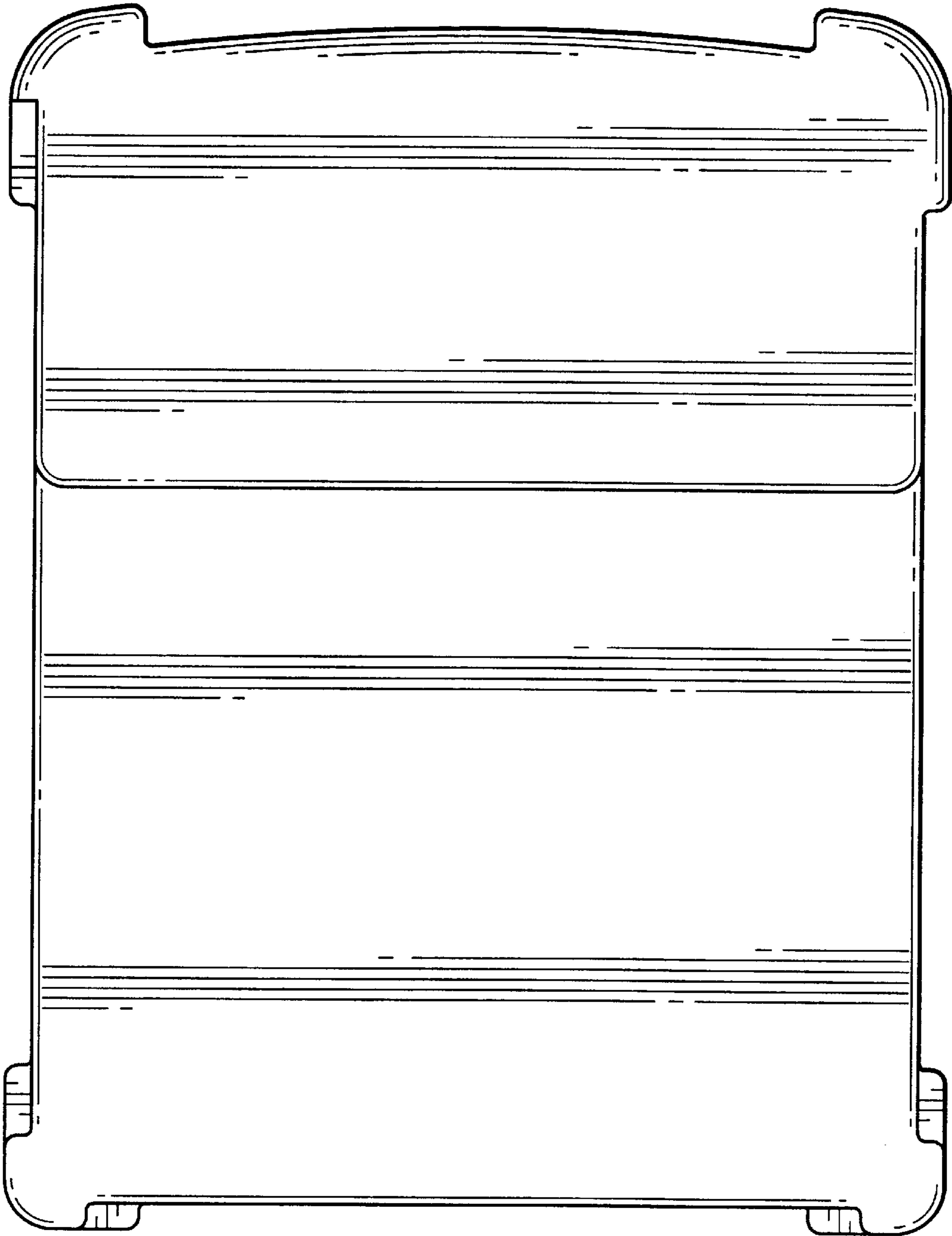


FIG. 3

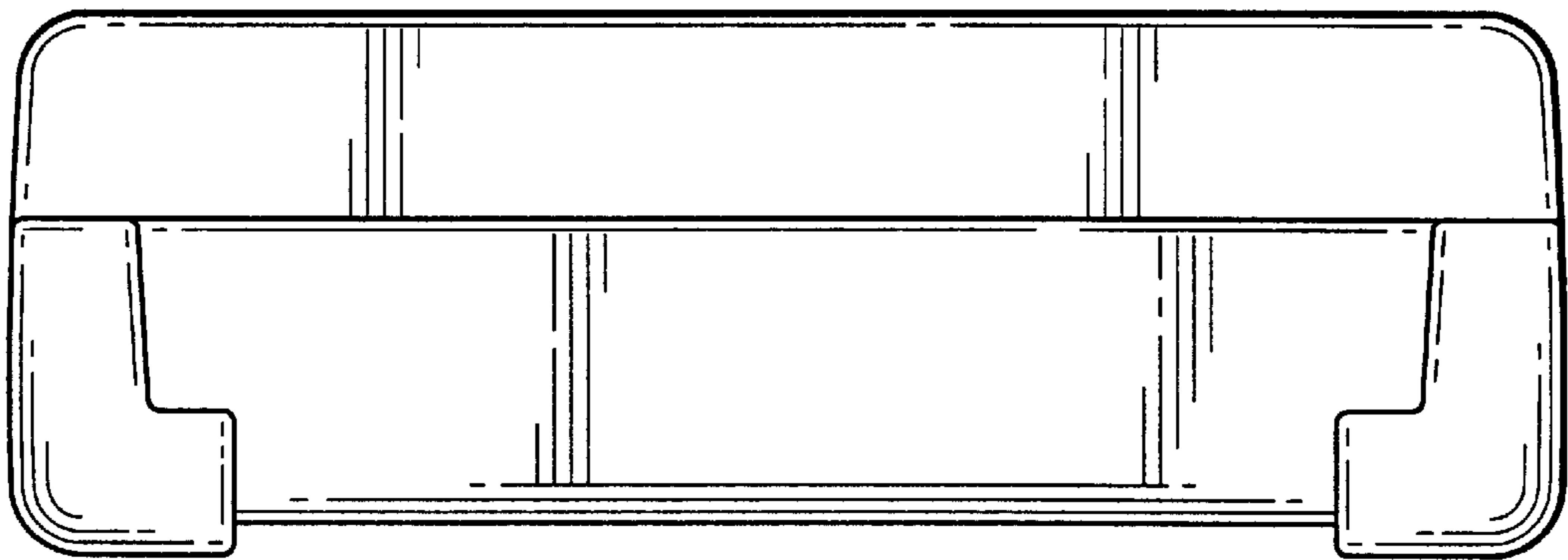


FIG. 5

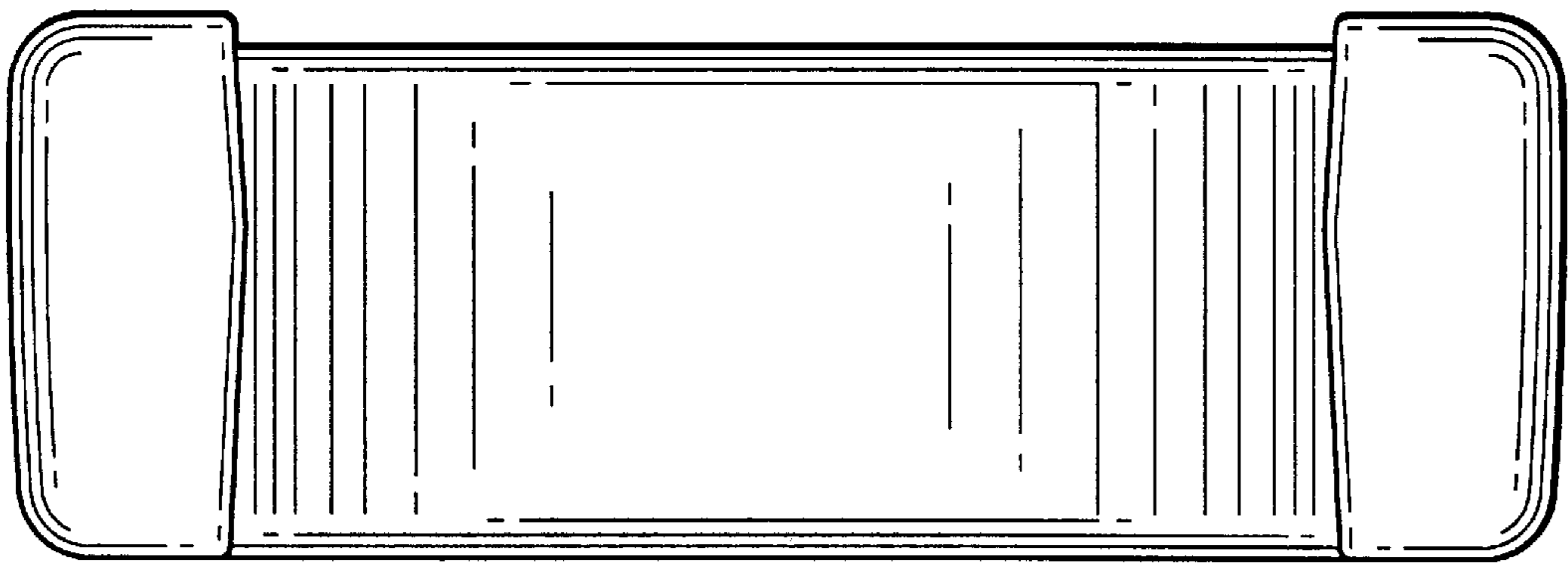


FIG. 4

FIG.6

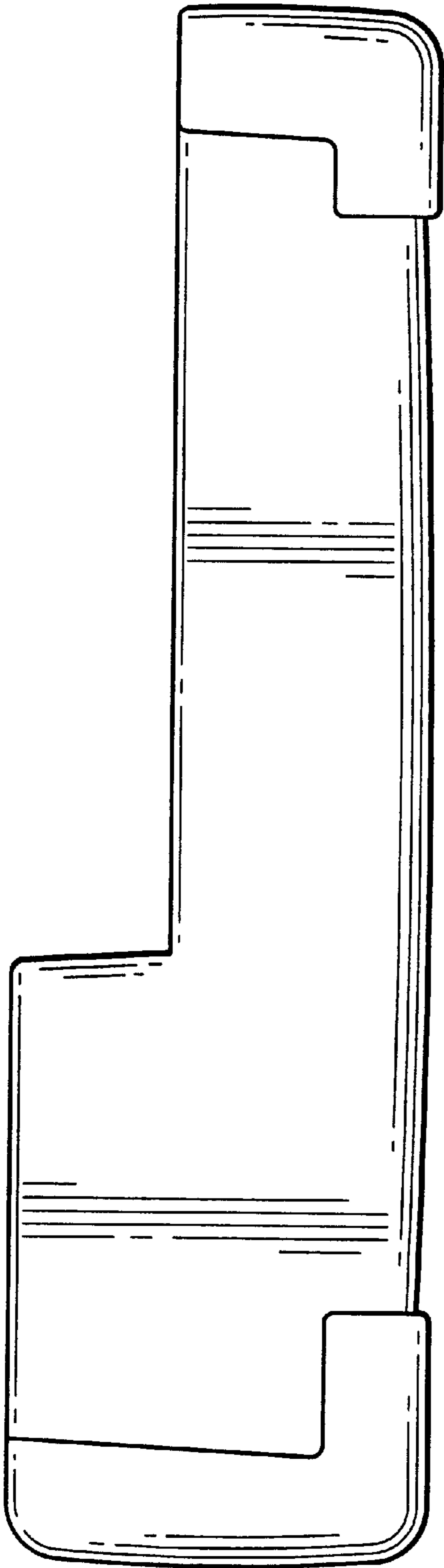


FIG.7

