



US00D442950S

(12) **United States Design Patent**
Sugano et al.

(10) **Patent No.:** **US D442,950 S**

(45) **Date of Patent:** **** May 29, 2001**

(54) **ELECTRONIC COMPUTER**

D. 426,817 * 6/2000 Sugano et al. D14/322

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(73) Assignee: **Kabushiki Kaisha Toshiba**, Kawasaki (JP)

(**) Term: **14 Years**

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(21) Appl. No.: **29/125,627**

Primary Examiner—Freda Nunn

(22) Filed: **Jun. 28, 2000**

(74) *Attorney, Agent, or Firm*—Banner & Witcoff, Ltd.

(51) **LOC (7) Cl.** **14-02**

(57) **CLAIM**

(52) **U.S. Cl.** **D14/323**

The ornamental design for an electronic computer, as shown and described.

(58) **Field of Search** D14/315, 318,
D14/319, 321–323, 326, 327; D18/1, 7;
235/145 A, 145 R; 341/22, 23; 345/156,
168, 169, 172, 173

DESCRIPTION

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FIG. 1 is a top, front and right side perspective view of an electronic computer showing our new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a right side elevational view thereof, FIG. 4 is a left side elevational view thereof, FIG. 5 is a rear elevational view thereof, FIG. 6 is a top plan view thereof, FIG. 7 is a bottom plan view thereof, FIG. 8 is a top plan view showing the article in a closed position thereof, FIG. 9 is a front elevational view showing the article in the closed position thereof; and, FIG. 10 is a right side elevational view showing the article in the closed position thereof.

1 Claim, 5 Drawing Sheets

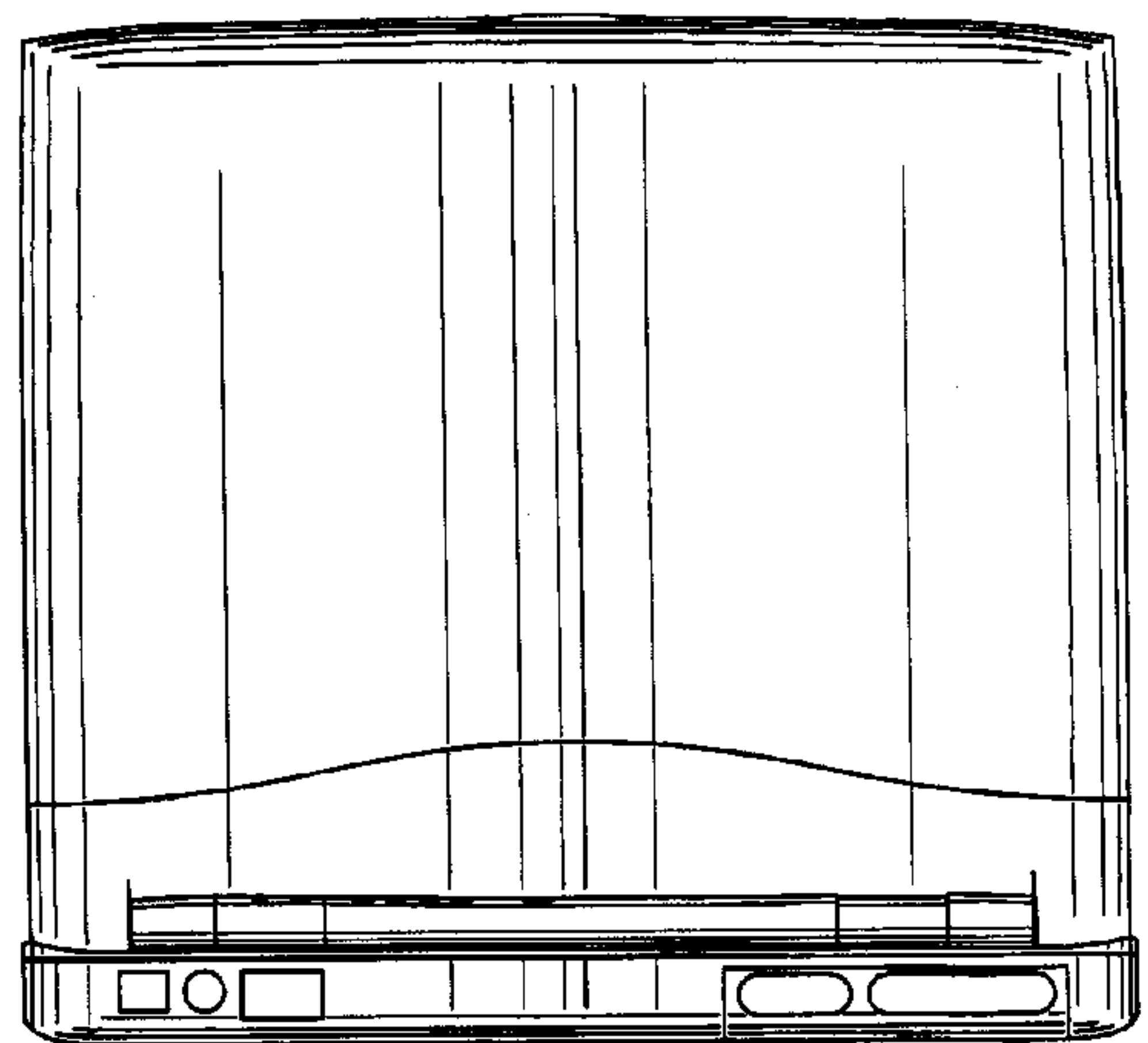
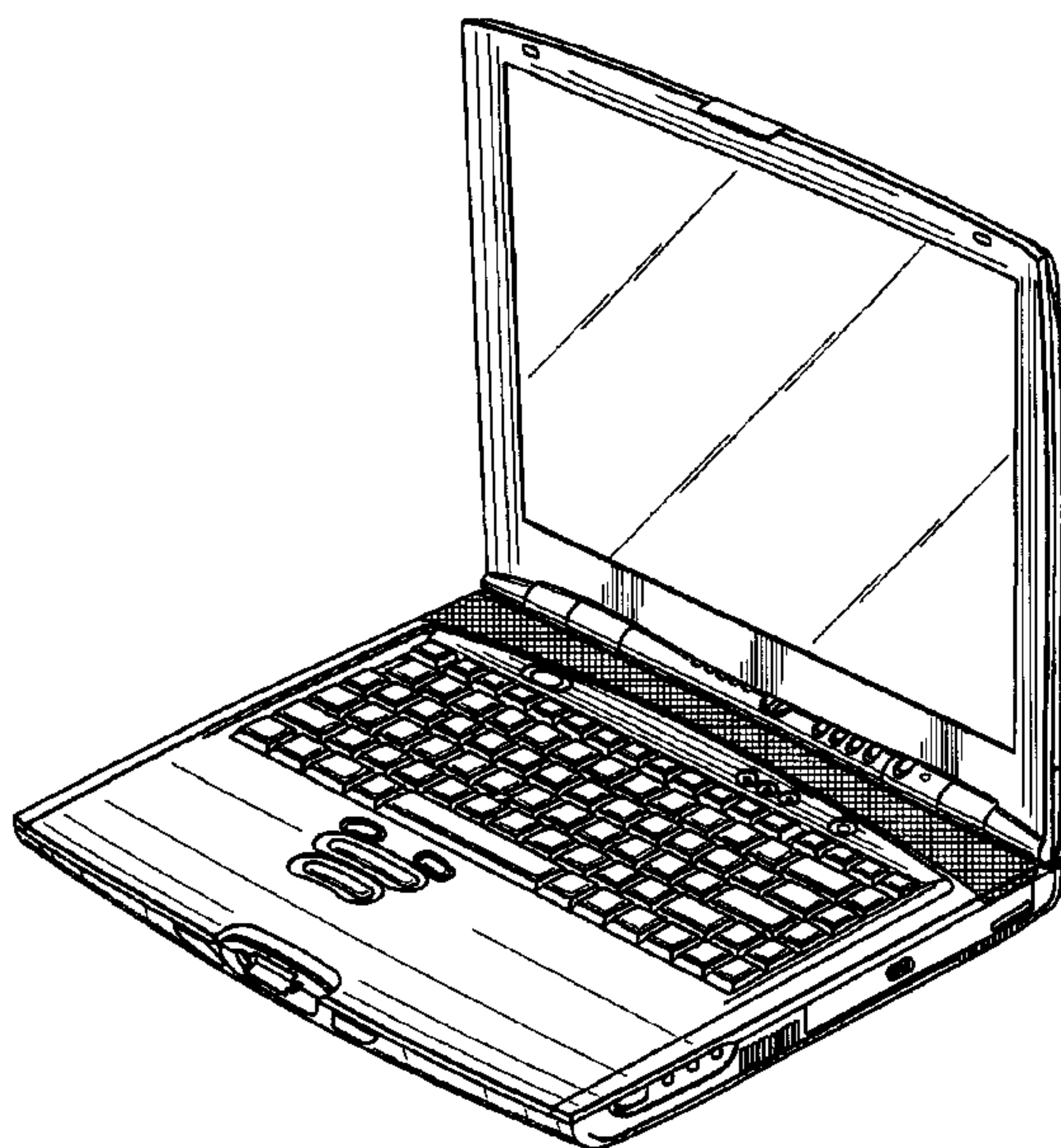


FIG. 1

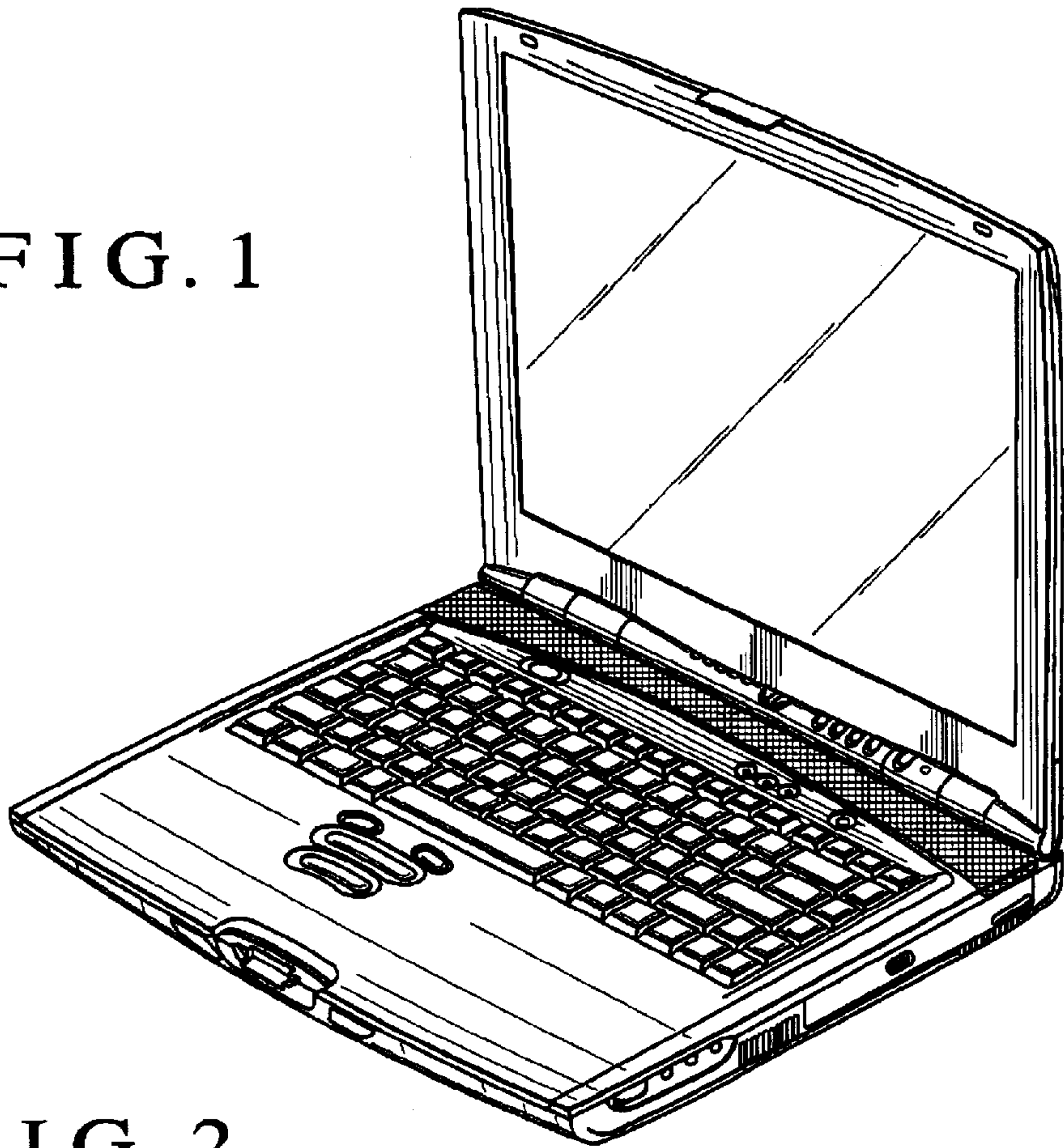


FIG. 2

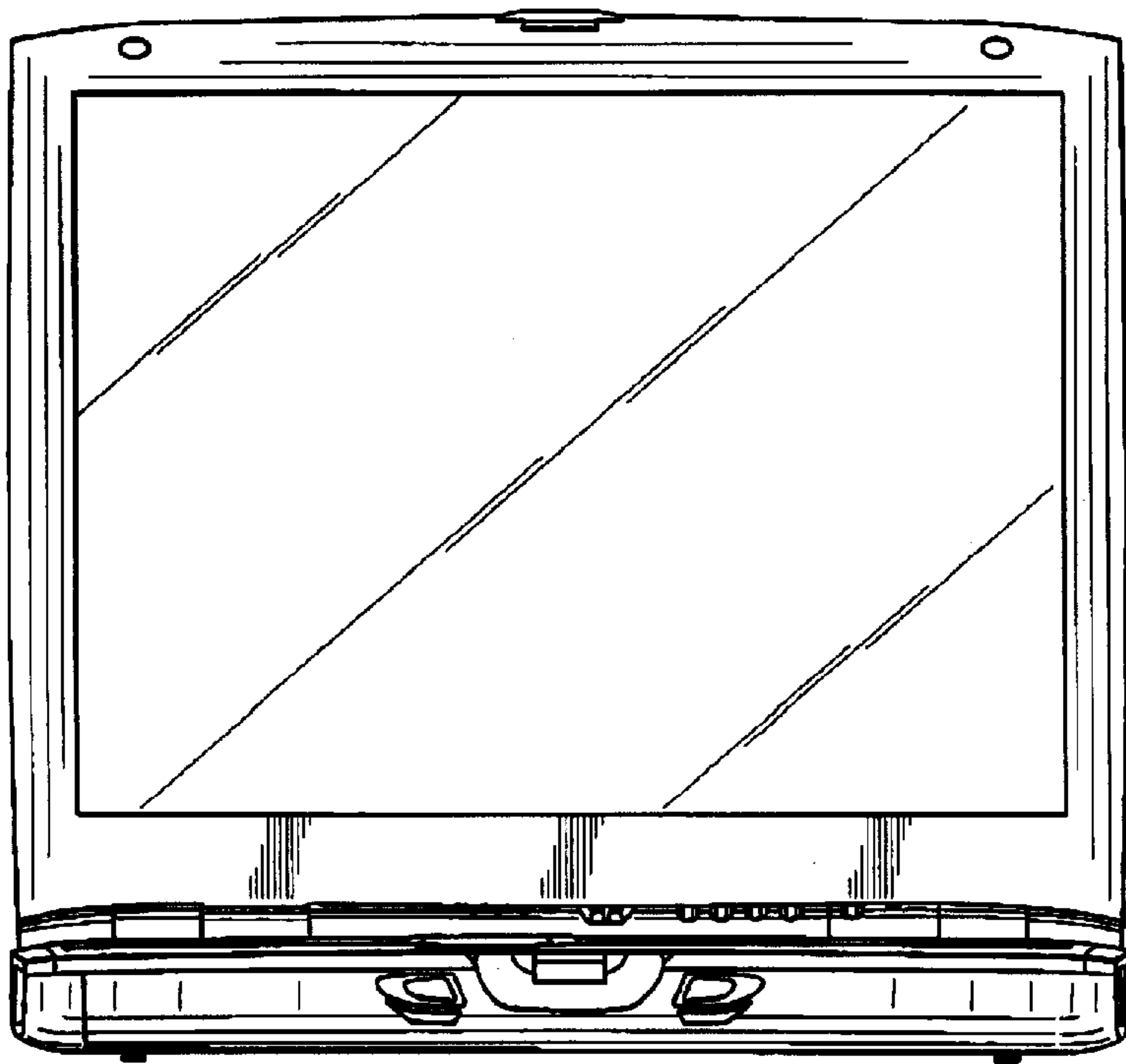


FIG. 3

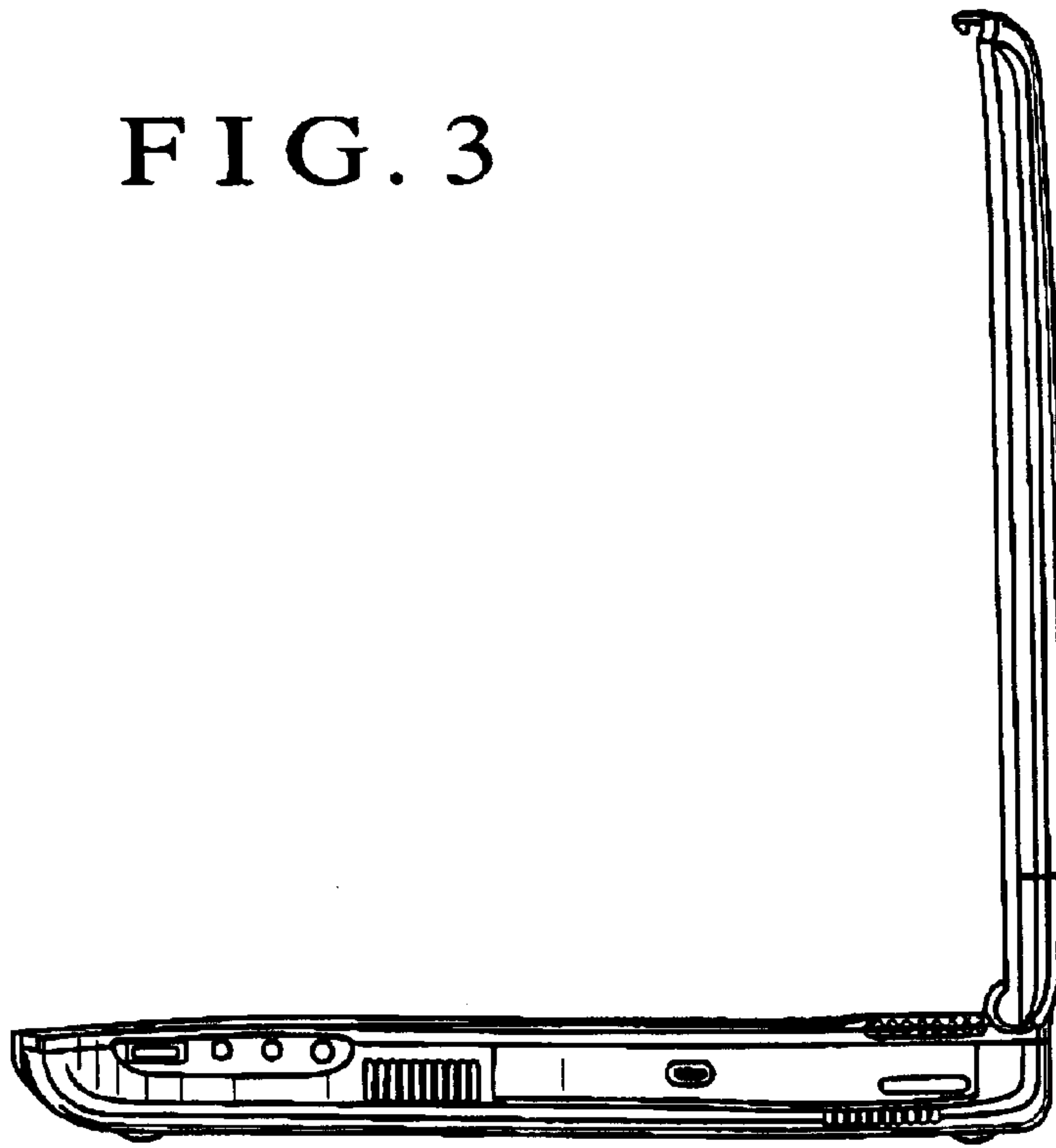


FIG. 4

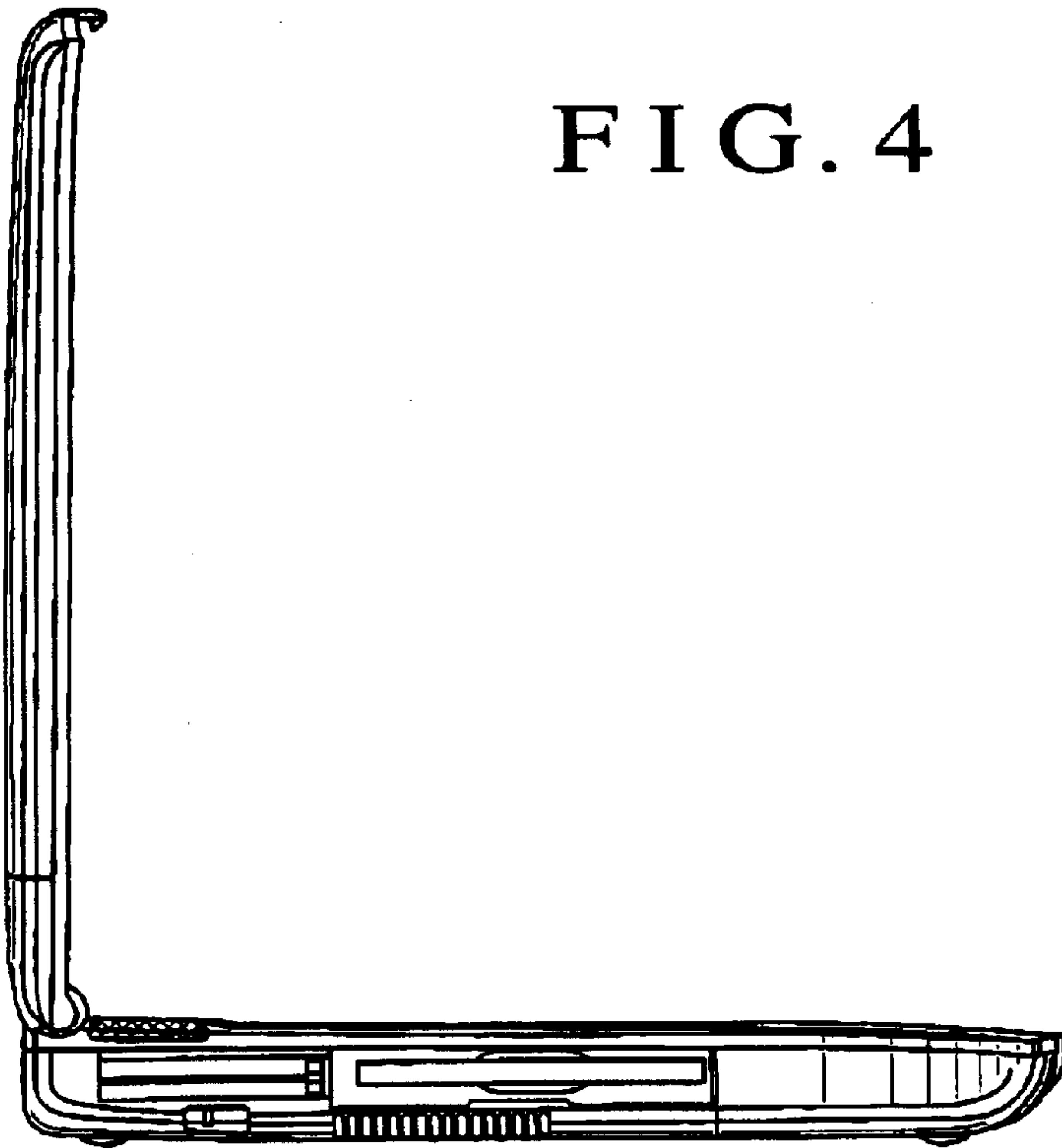


FIG. 5

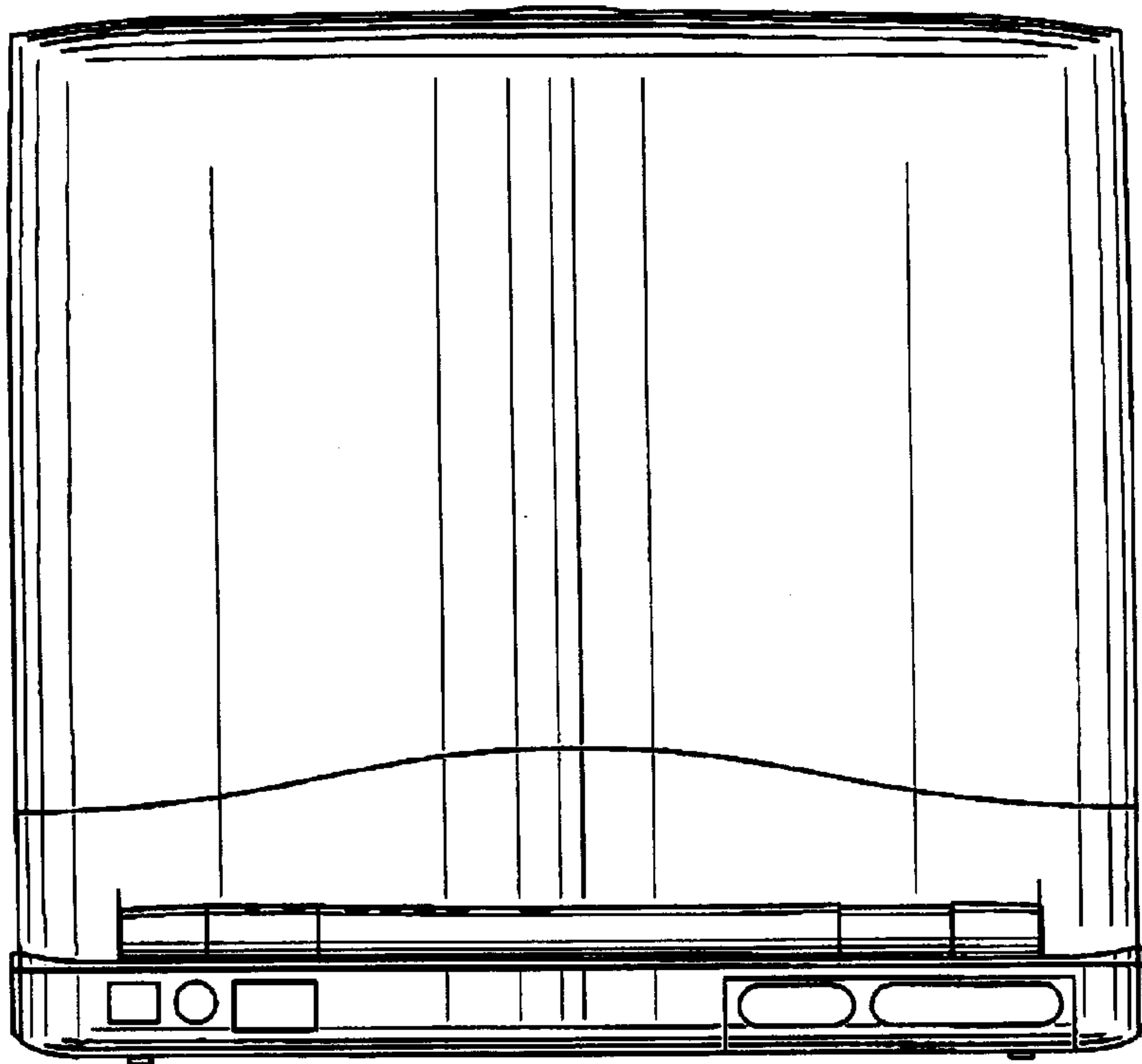


FIG. 6

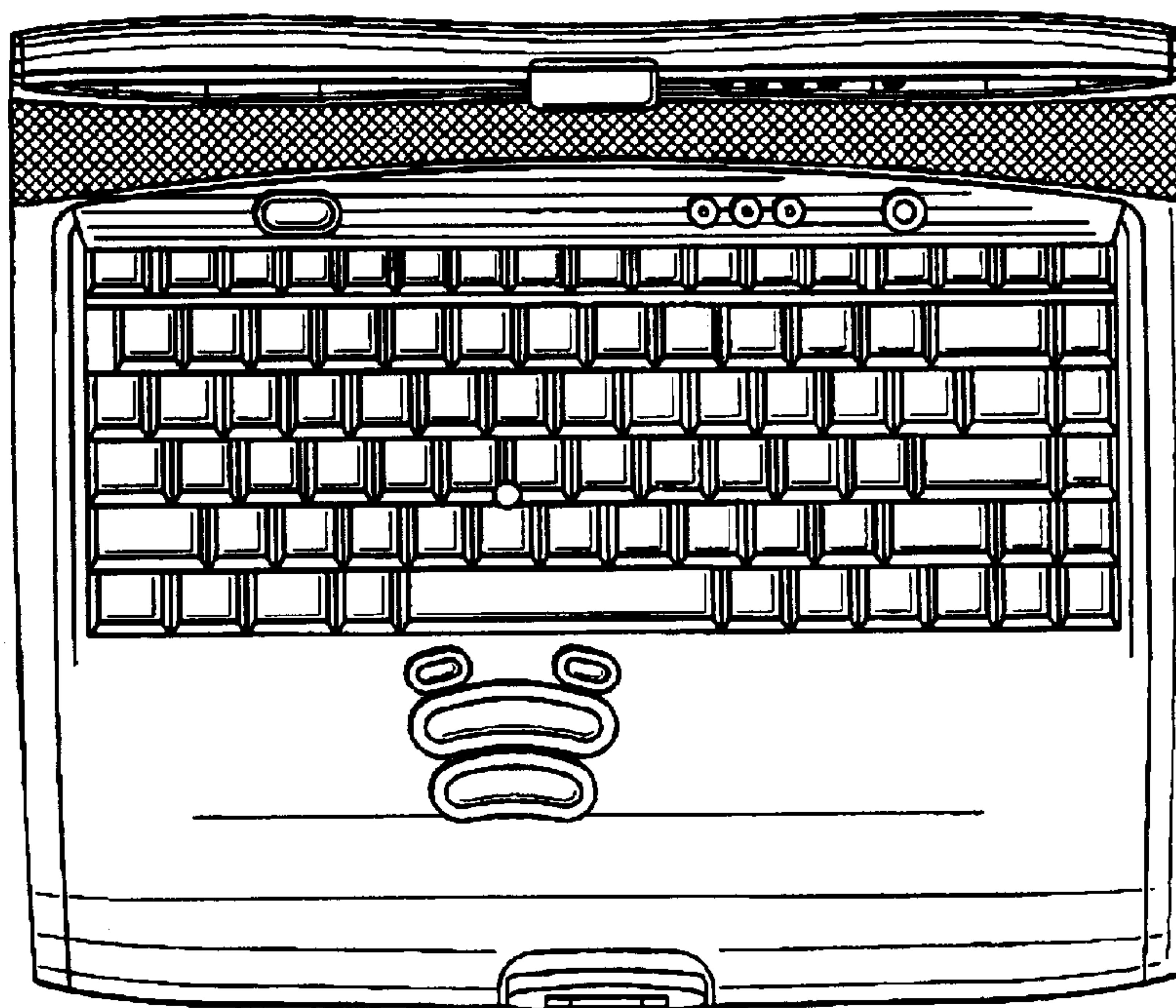


FIG. 7

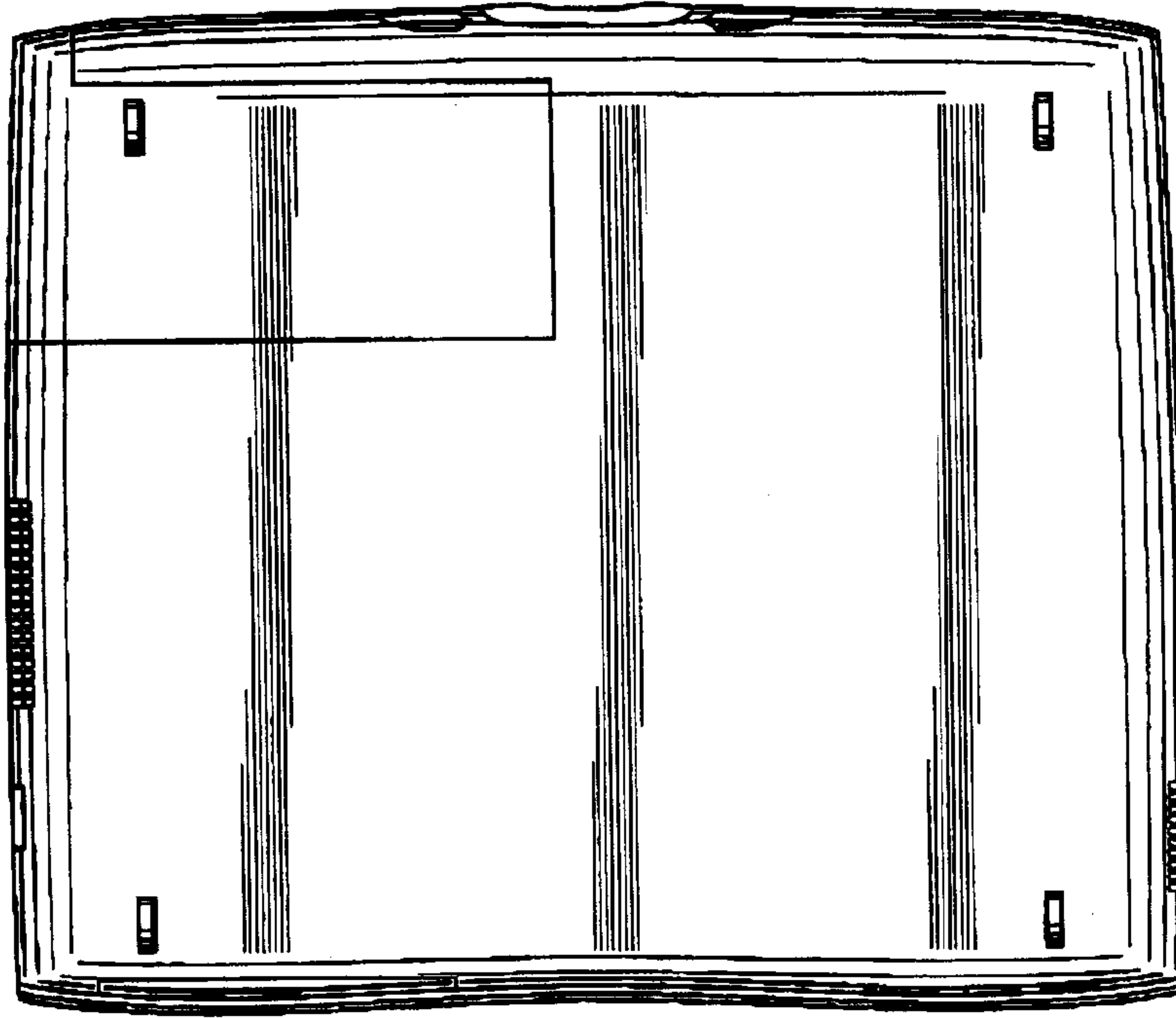


FIG. 8

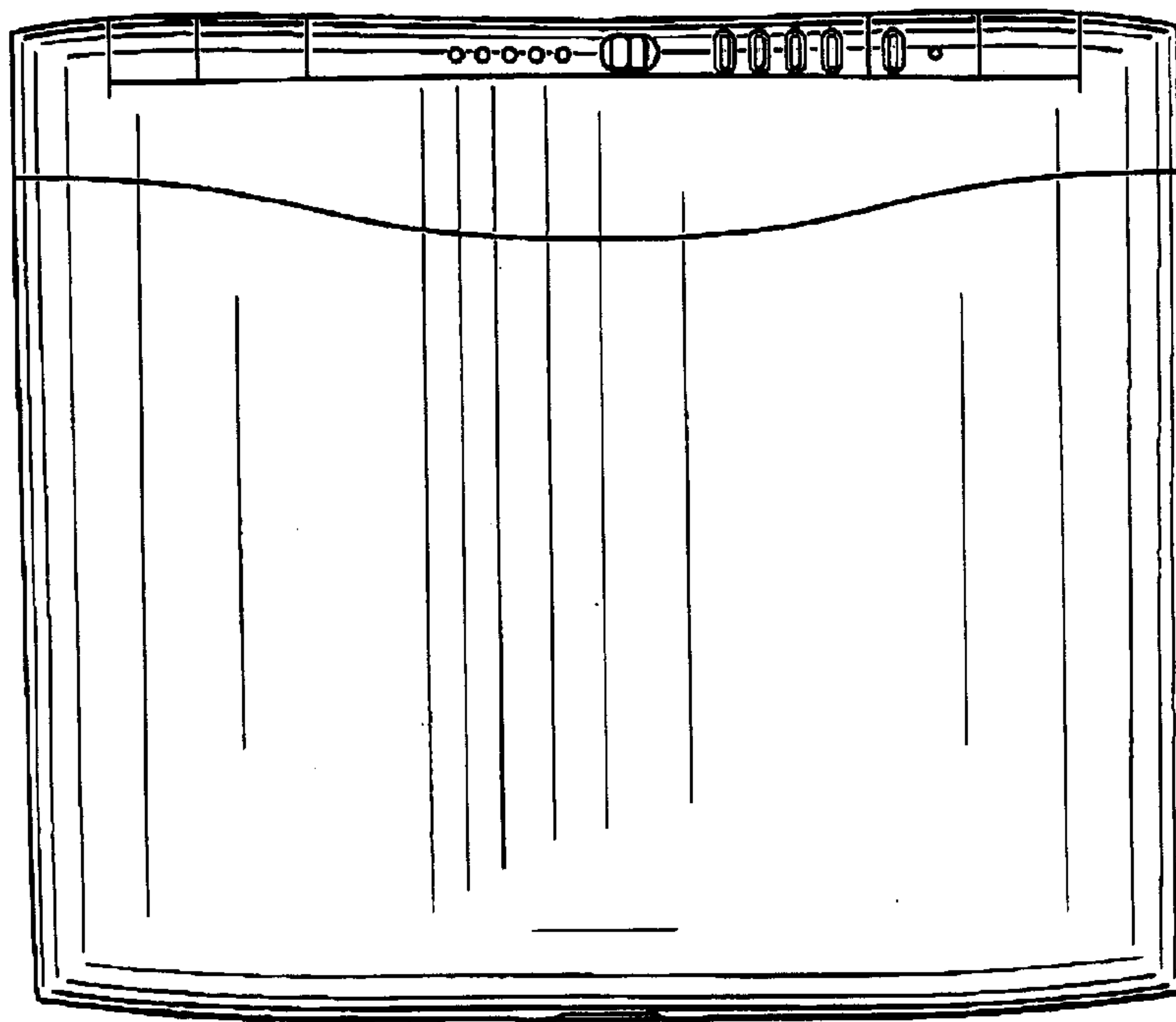


FIG. 9

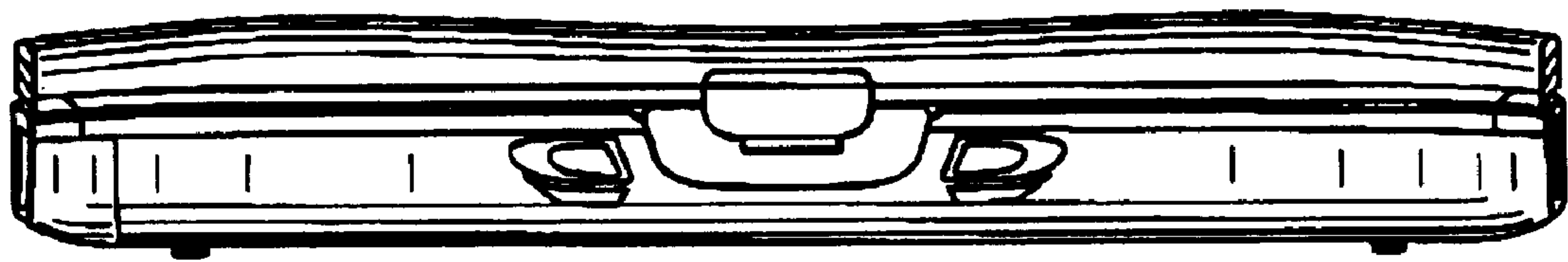


FIG. 10

