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(12) **United States Design Patent**
Suzuki

(10) **Patent No.:** **US D441,371 S**

(45) **Date of Patent:** **** May 1, 2001**

(54) **PORTION OF AN ELECTRONIC COMPUTER**

5,995,373 * 11/1999 Nagai .

(75) Inventor: **Shogo Suzuki**, Matsudo (JP)

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(73) Assignee: **Kabushiki Kaisha Toshiba**, Kawasaki (JP)

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(**) Term: **14 Years**

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(21) Appl. No.: **29/114,141**

NEC Catalog MC-R520, exact date unknown but obtained before May 21, 1999.

(22) Filed: **Nov. 19, 1999**

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(30) **Foreign Application Priority Data**

Novatel Wireless Catalog, Contact, Aug. 10, 1998.

May 21, 1999 (JP) 11-13297

Ascentia 910N Catalog, AST Research, Inc., Aug. 10, 1995.

(51) **LOC (7) Cl.** **14-02**

* cited by examiner

(52) **U.S. Cl.** **D14/439**

(58) **Field of Search** D14/314-16, 318, D14/432, 439, 455, 466; D8/323-29; 16/267, 285, 342, 354; 361/681, 683, 687, 755

Primary Examiner—M. H. Tung

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(56) **References Cited**

(57) **CLAIM**

U.S. PATENT DOCUMENTS

The ornamental design for a portion of an electronic computer, as shown and described.

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DESCRIPTION

FIG. 1 is a top, front and right side perspective view of portion of an electronic computer showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a right side elevational view thereof, the left side elevational view being a mirror image, FIG. 4 is a rear elevational view thereof, FIG. 5 is a top plan view thereof, FIG. 6 is a bottom plan view thereof; and, FIG. 7 is a perspective view showing the portion of an electronic computer with the lid being in an open position. The broken line showing of the remainder of the electronic computer in all views is for illustrative purposes only and forms no part of the claimed design.

1 Claim, 3 Drawing Sheets

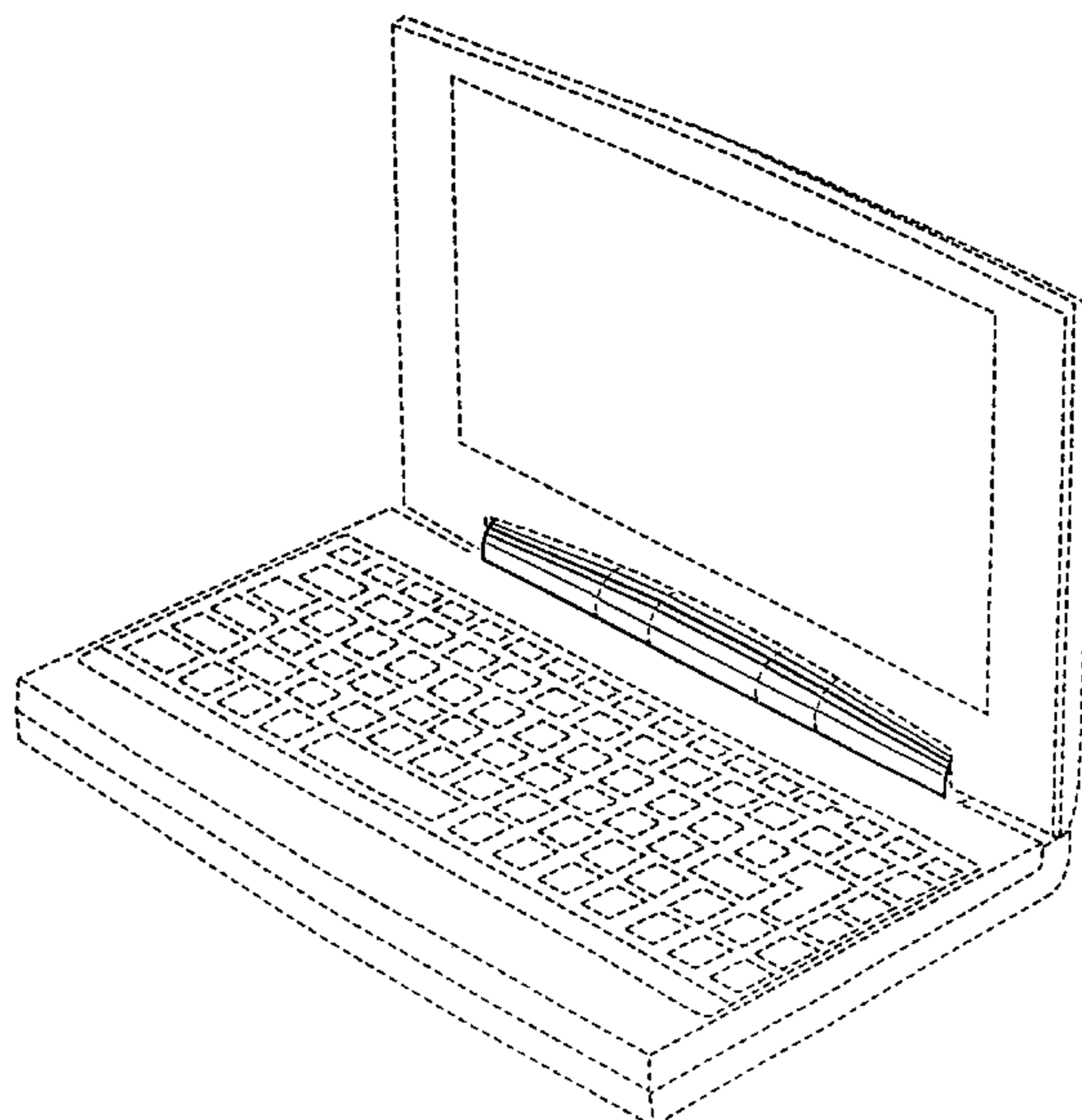


FIG. 1

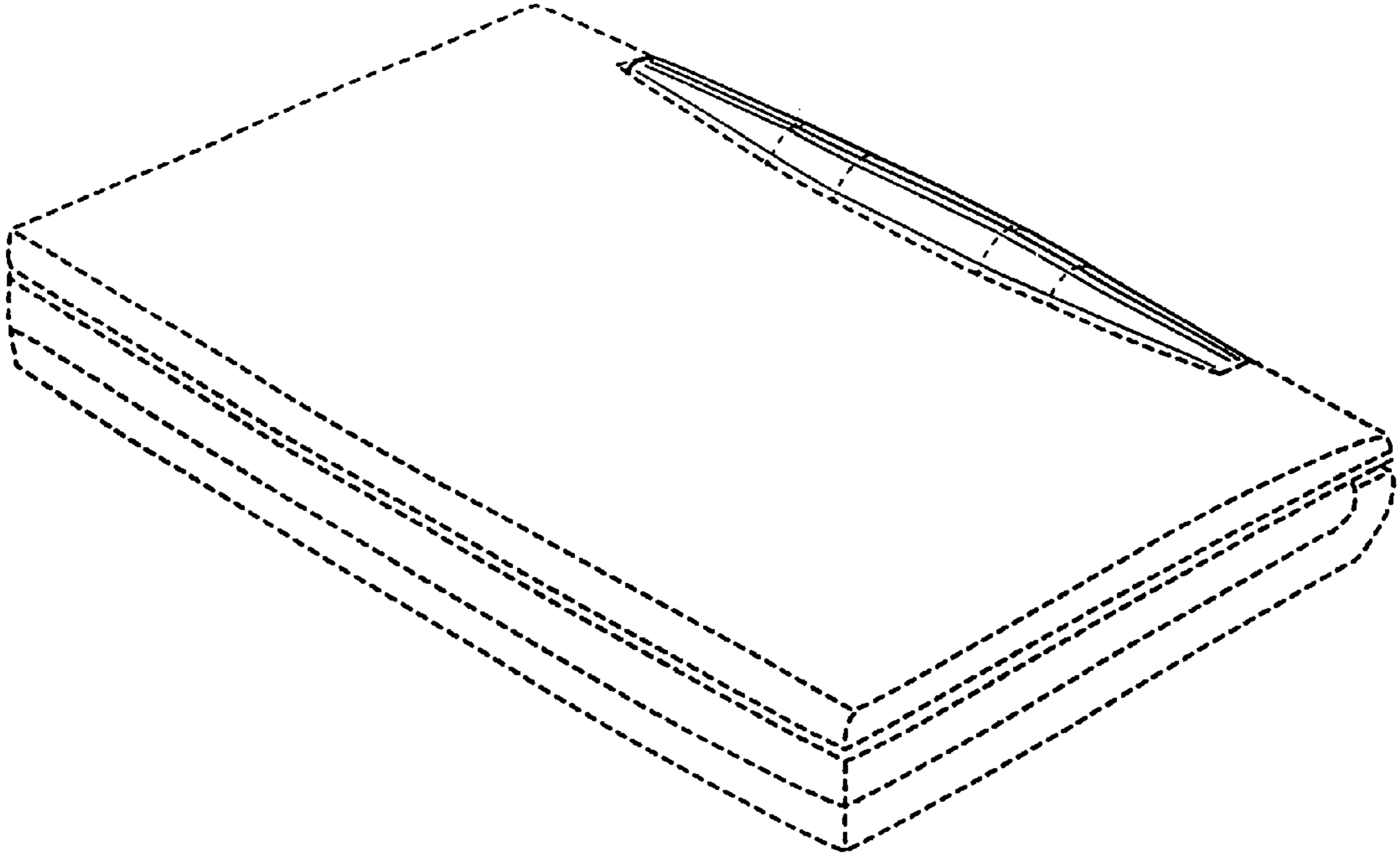


FIG. 2

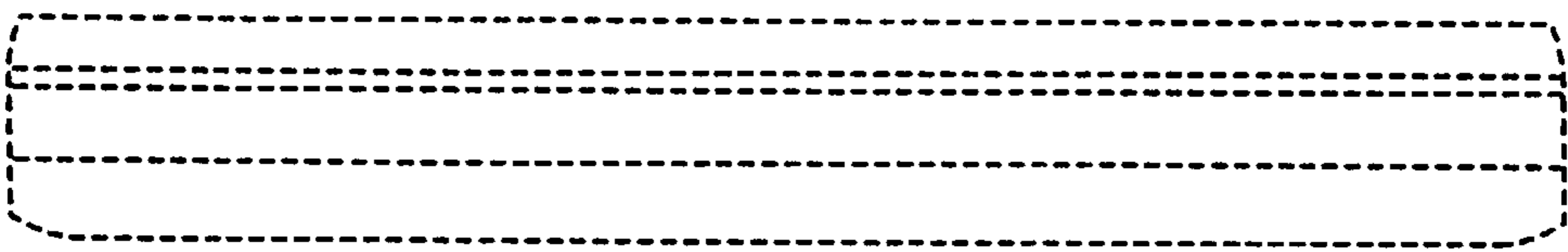


FIG. 3

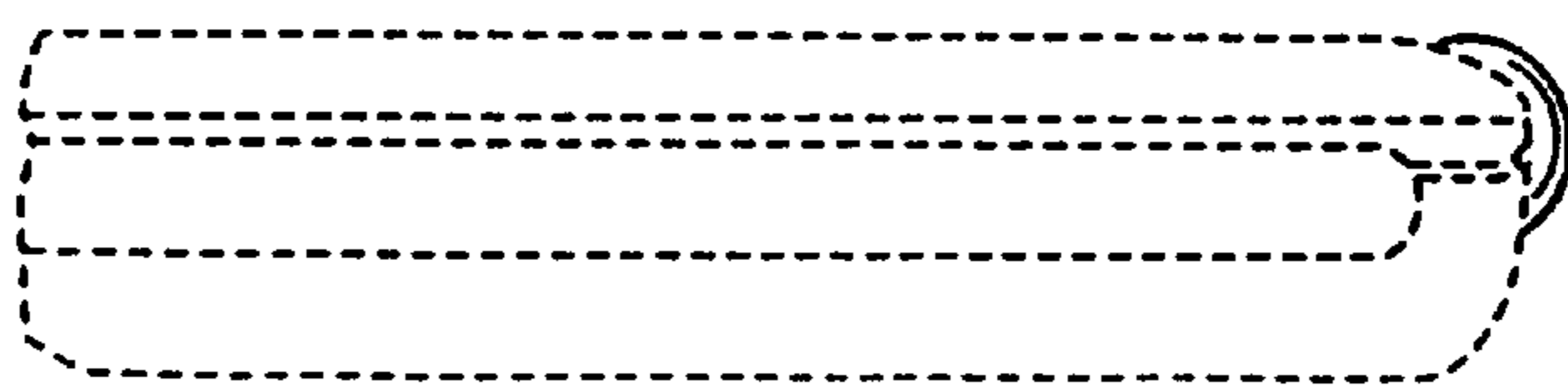


FIG. 4

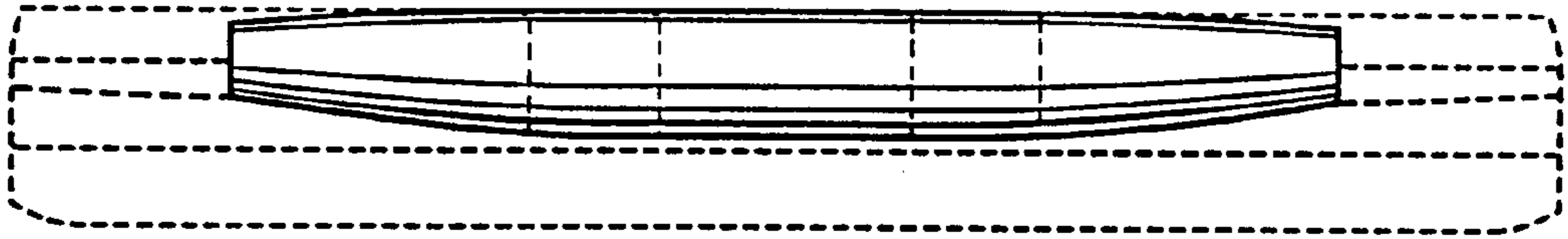


FIG. 5

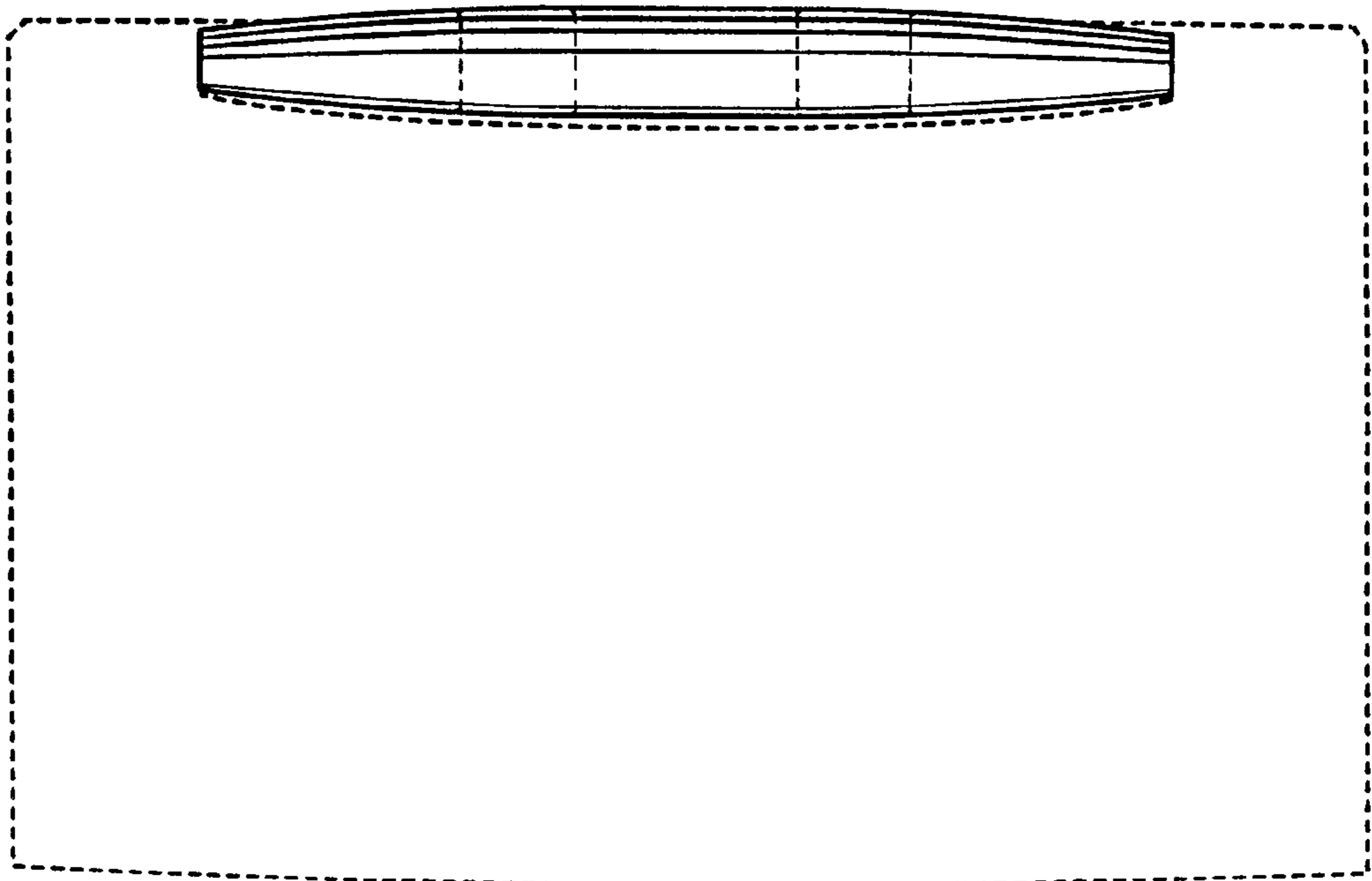


FIG. 6

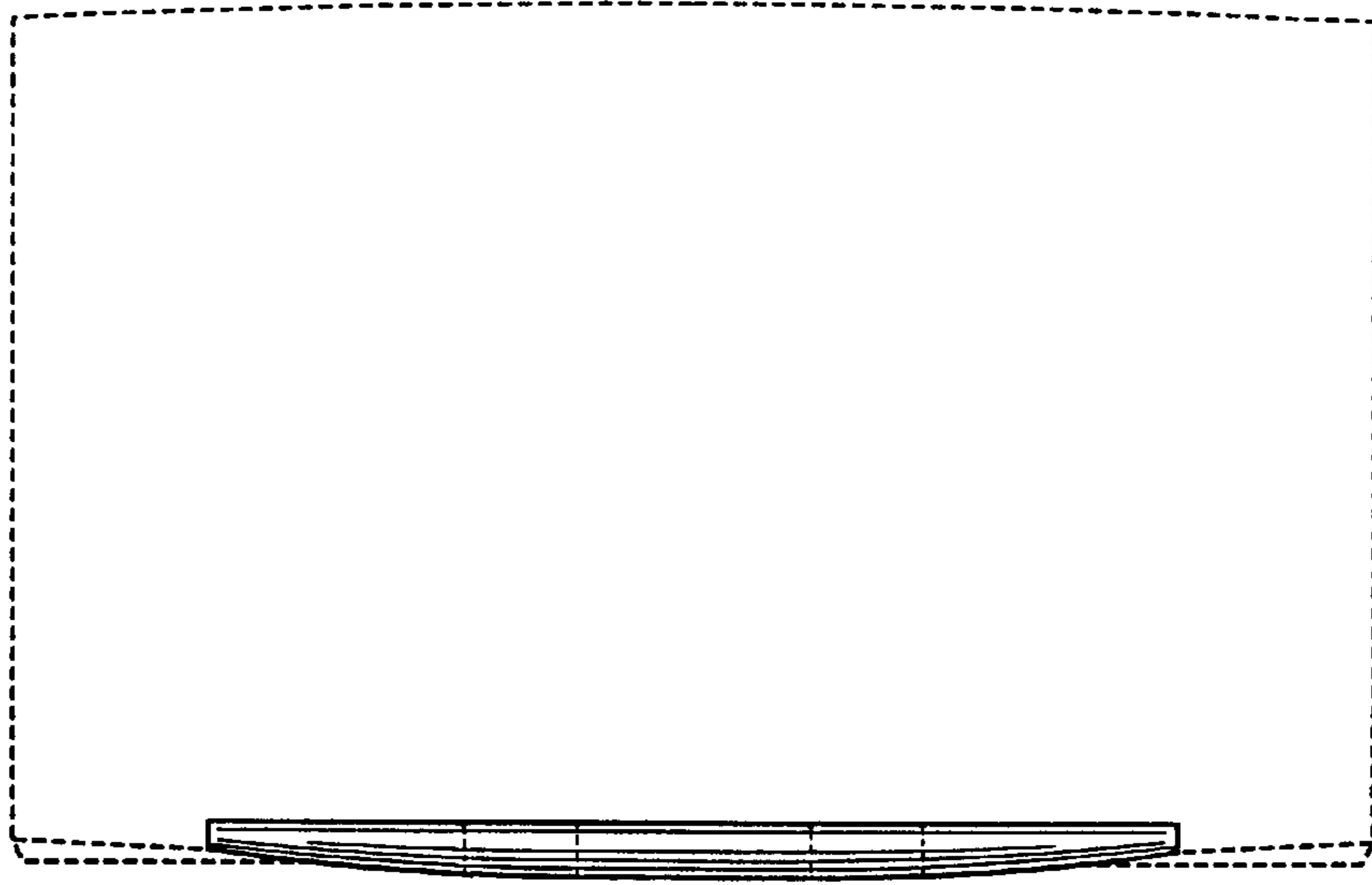


FIG. 7

