

US00D439566S

(12) **United States Design Patent** (10) **Patent No.:** **US D439,566 S**
Ohanian (45) **Date of Patent:** **** Mar. 27, 2001**

(54) **CIRCUIT CARD CAGE**

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(73) Assignee: **Inet Technologies, Inc.**, Richardson, TX (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/113,634**

(22) Filed: **Nov. 9, 1999**

Related U.S. Application Data

(63) Continuation-in-part of application No. 09/251,898, filed on Feb. 19, 1999.

(51) **LOC (7) Cl.** **13-95**

(52) **U.S. Cl.** **D13/199; D13/182; D14/439**

(58) **Field of Search** **D13/182, 184; D14/432, 433, 434, 435, 436, 437, 439; 361/800-802, 756, 799; 21/41.17; 439/377**

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(57) **CLAIM**

I claim the ornamental design for a circuit card cage, as shown and described.

DESCRIPTION

This application is related to concurrently filed and commonly assigned U.S. Design Patent Application Ser. No. 29/113,654.

FIG. 1 is an isometric view from the front showing the front, right, and top sides of my design for a circuit card cage, wherein the irregular broken line is intended to indicate the unclaimed boundary of the portion of the cage which constitutes the claimed design, with the remaining portions of the cage, and the exemplary circuit card shown in the various views being understood as environmental structure forming no part of the claimed design;

FIG. 2 is an elevation view from the front showing the front side of my design for a circuit card cage;

FIG. 3 is an elevation view from the right showing the right side of my design for a circuit card cage;

FIG. 4 is an elevation view from the back showing the back side of my design for a circuit card cage;

FIG. 5 is an elevation view from the left showing the left side of my design for a circuit card cage;

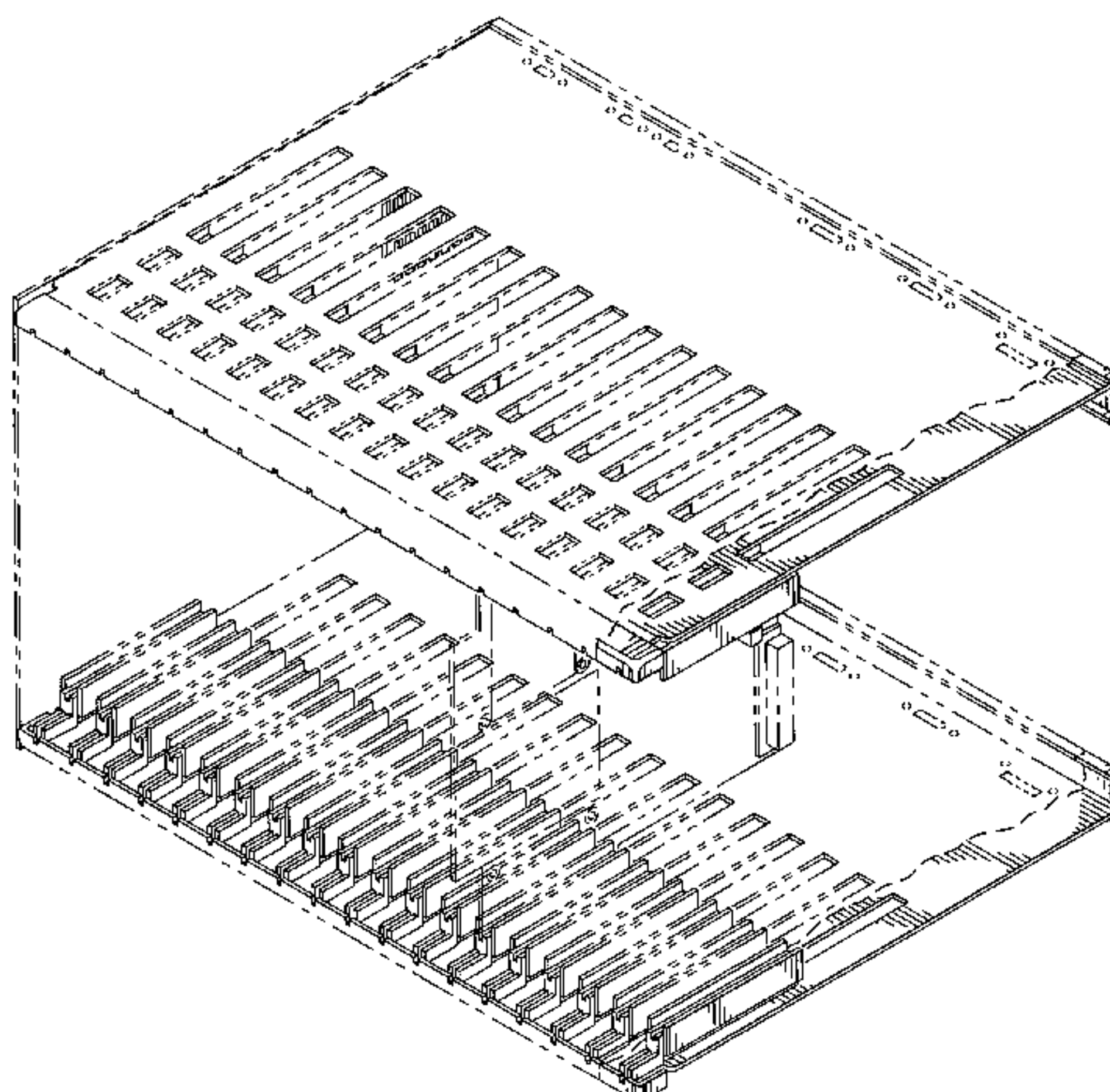
FIG. 6 is a plan view from the top of my design for a circuit card cage;

FIG. 7 is a plan view from the bottom of my design for a circuit card cage; and,

FIG. 8 is an isometric view from the front showing the front, right, and top sides of my design for a circuit card cage, illustrating the top side of the circuit card cage offset toward the back of the circuit card cage.

It should be understood that the labels used herein for describing orientation (e.g., front, back, top, bottom, right, and left) are merely for reference, and the design may be disposed in any manner and still be within the scope of the present invention.

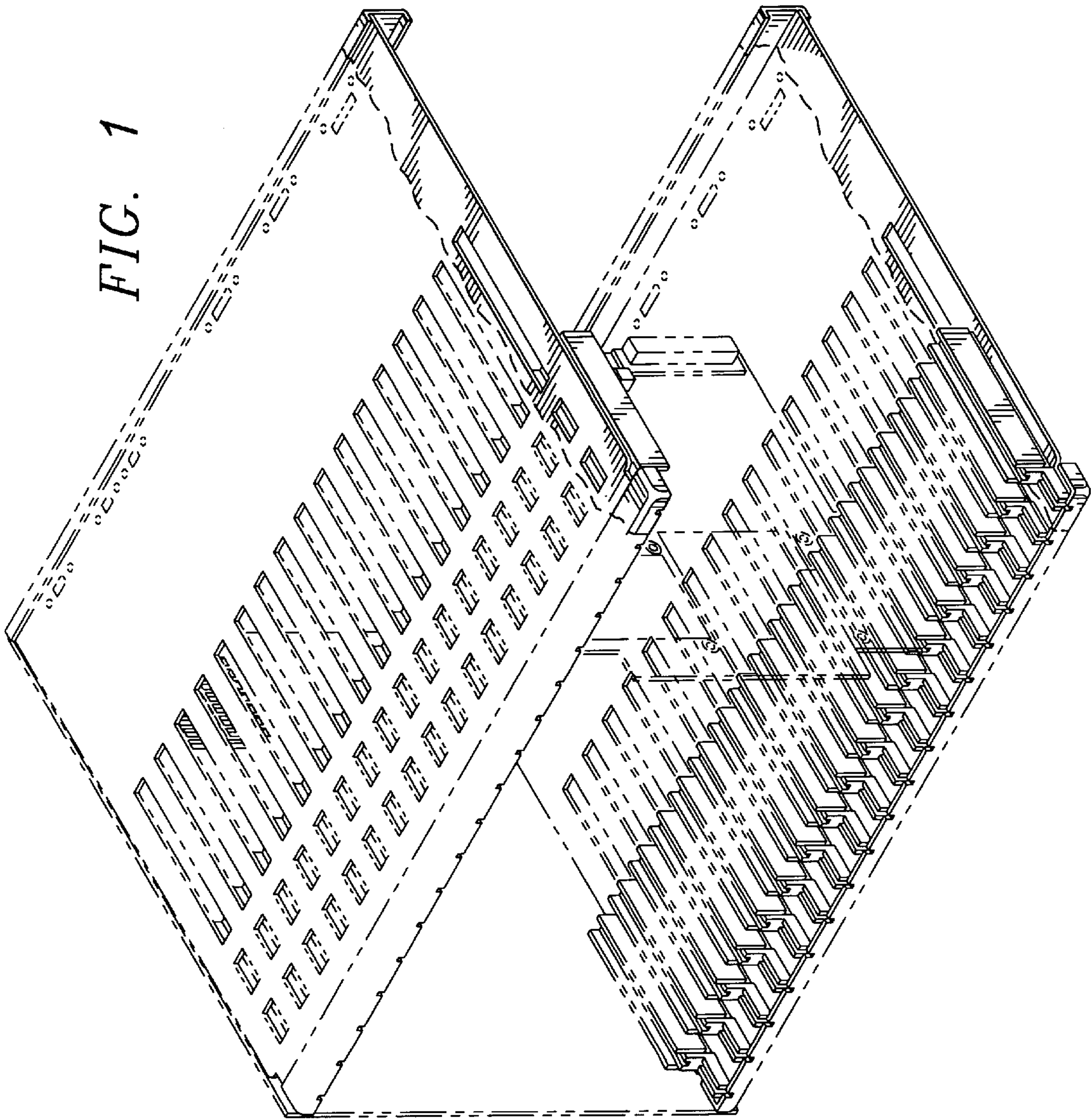
1 Claim, 8 Drawing Sheets



US D439,566 S

Page 2

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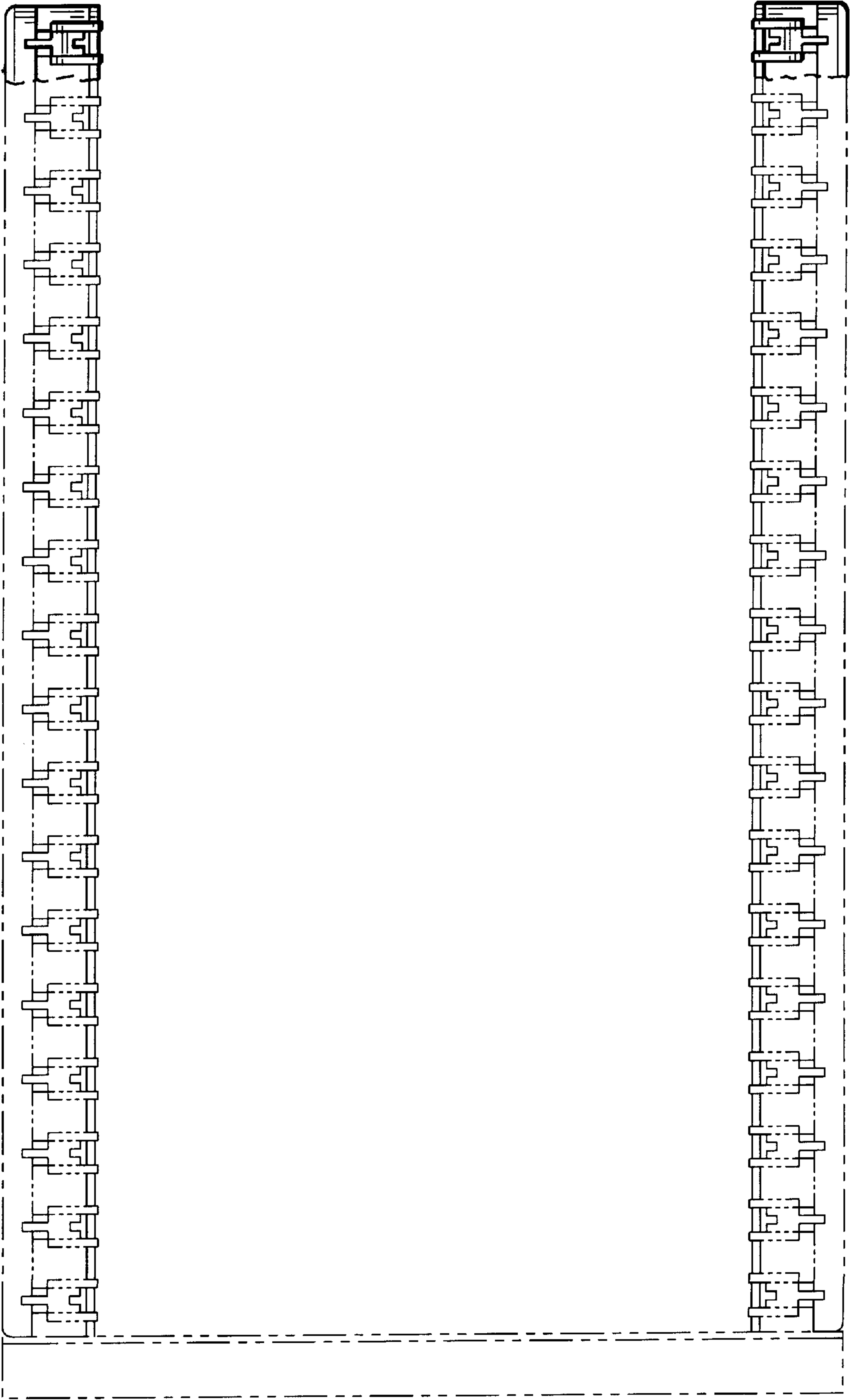


FIG. 2

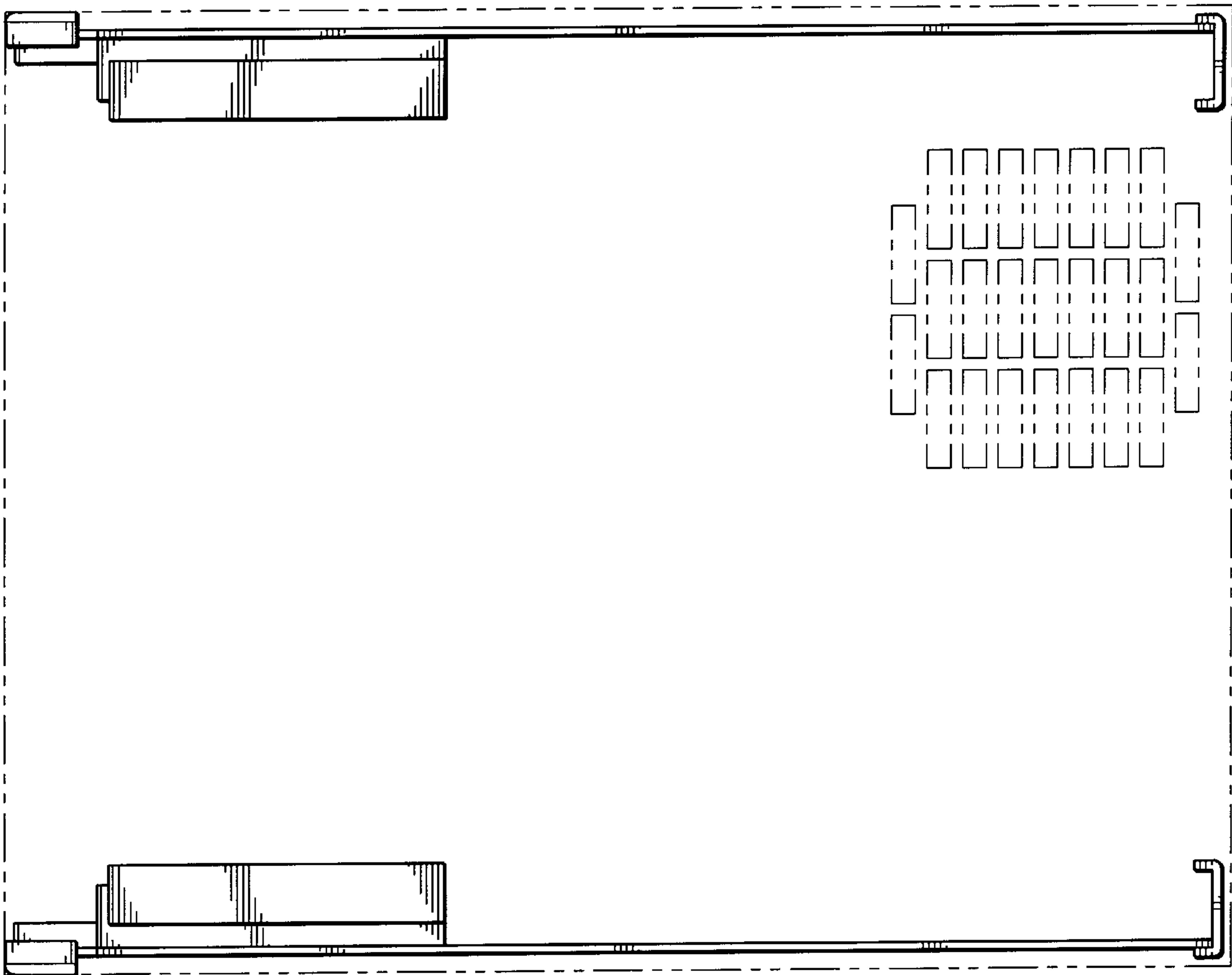


FIG. 3

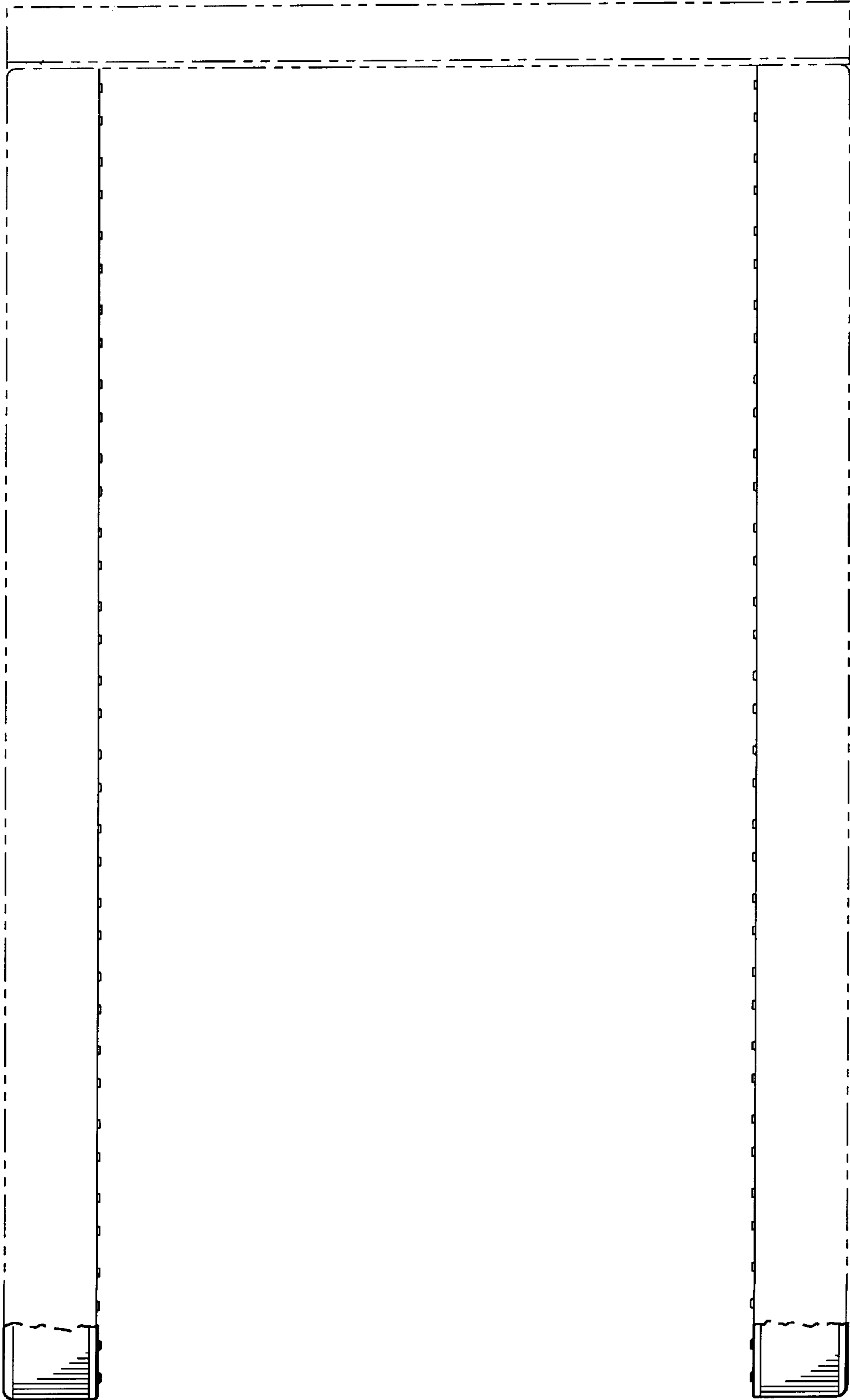


FIG. 4

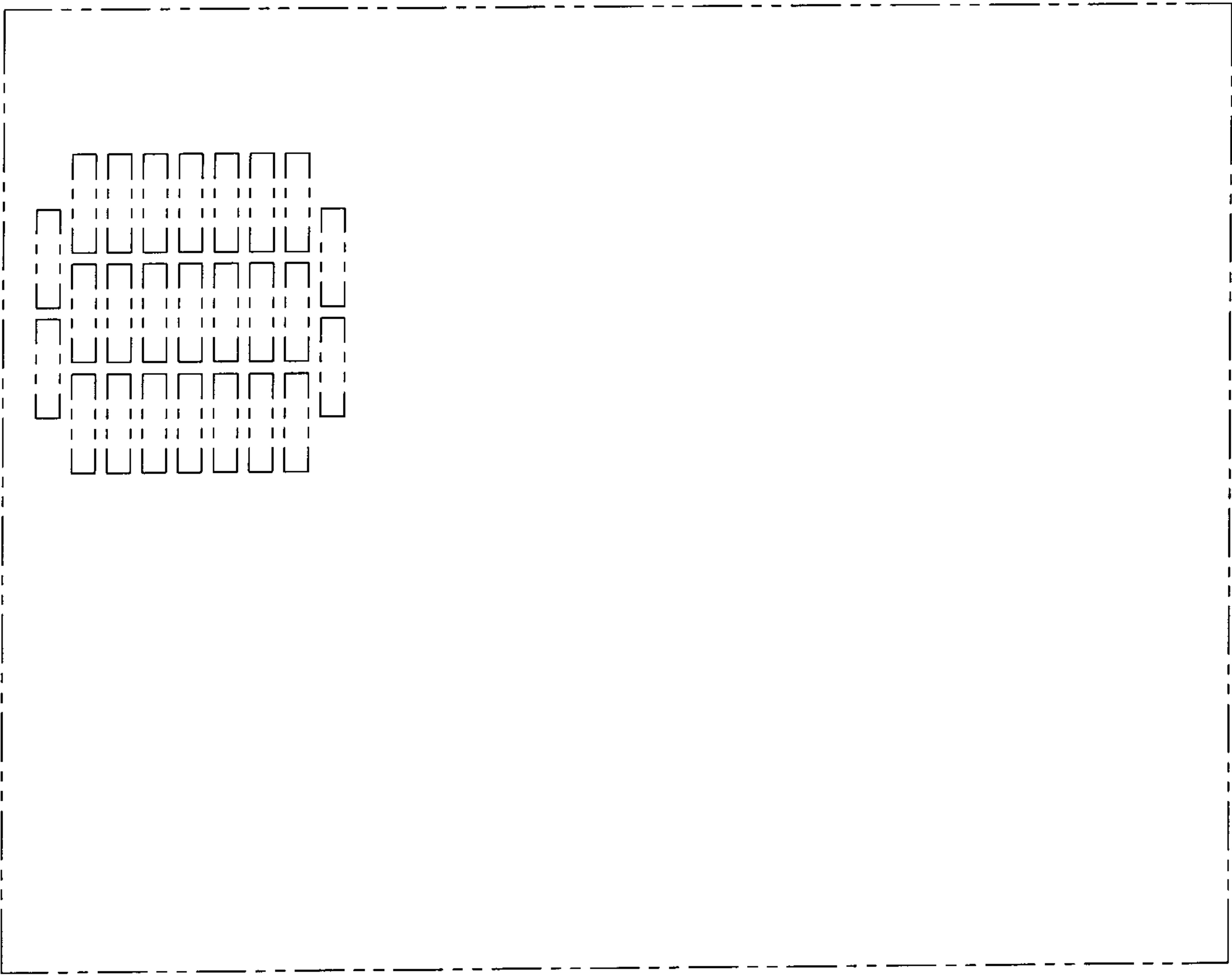


FIG. 5

FIG. 6

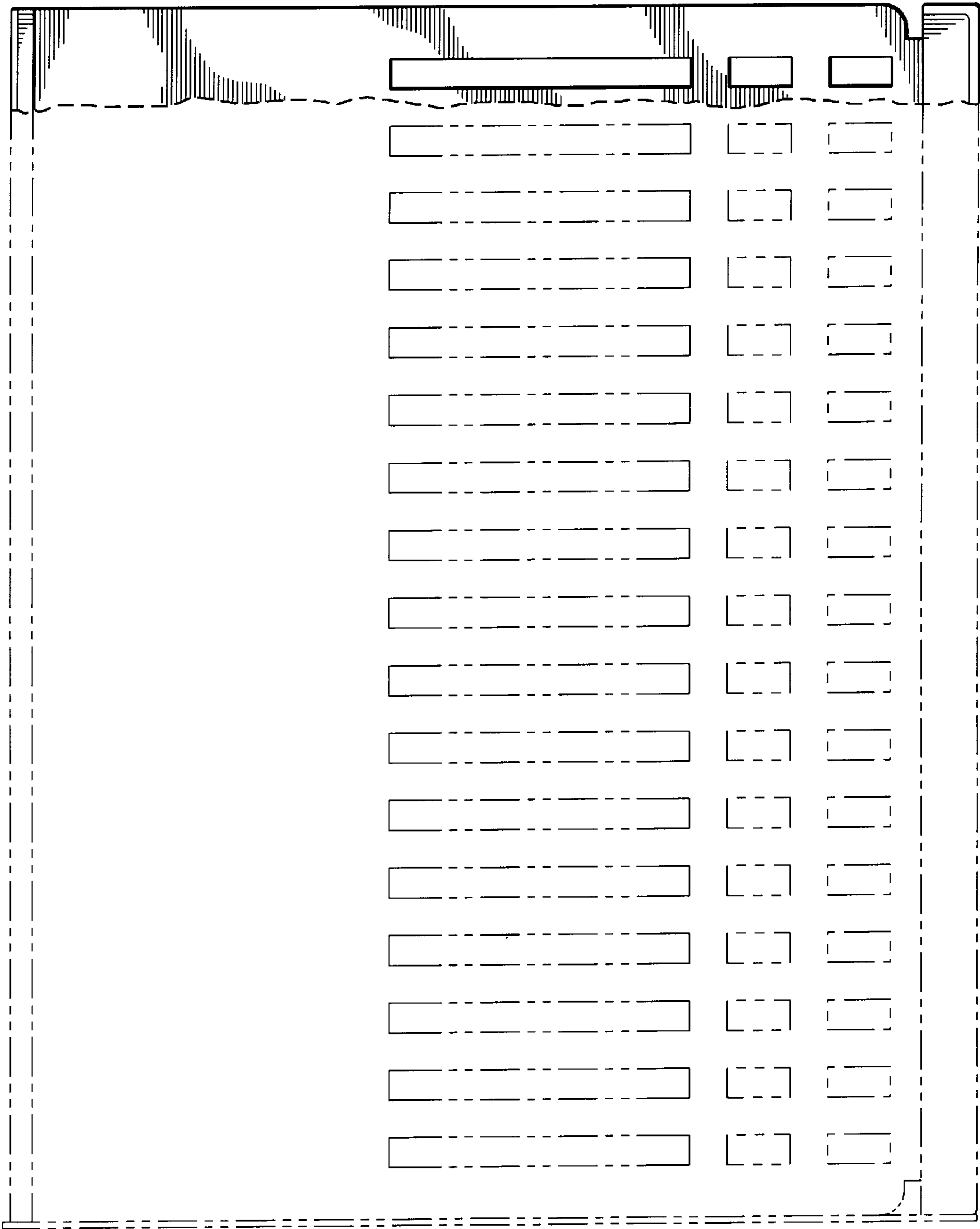


FIG. 7

