



US00D436085S

(12) **United States Design Patent**
Takizawa et al.

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(54) **PIEZOELECTRIC CONVERSION TYPE
SEMICONDUCTOR DEVICE**

5,031,462 * 7/1991 Lam 338/4 X
5,184,107 * 2/1993 Maurer D13/101 X
5,459,351 * 10/1995 Bender 257/417

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* cited by examiner

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(**) Term: **14 Years**

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(21) Appl. No.: **29/106,665**

(22) Filed: **Jun. 18, 1999**

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/182, 101;
73/721; 257/416–418; 338/4, 42

(56) **References Cited**

U.S. PATENT DOCUMENTS

D. 318,646 * 7/1991 Karbassi D13/101
D. 330,263 * 10/1992 Inoi D13/101
D. 390,833 2/1998 Takizawa et al. D13/182

(57) **CLAIM**

The ornamental design for a piezoelectric conversion type semiconductor device, as shown.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a piezoelectric conversion type semiconductor device, showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

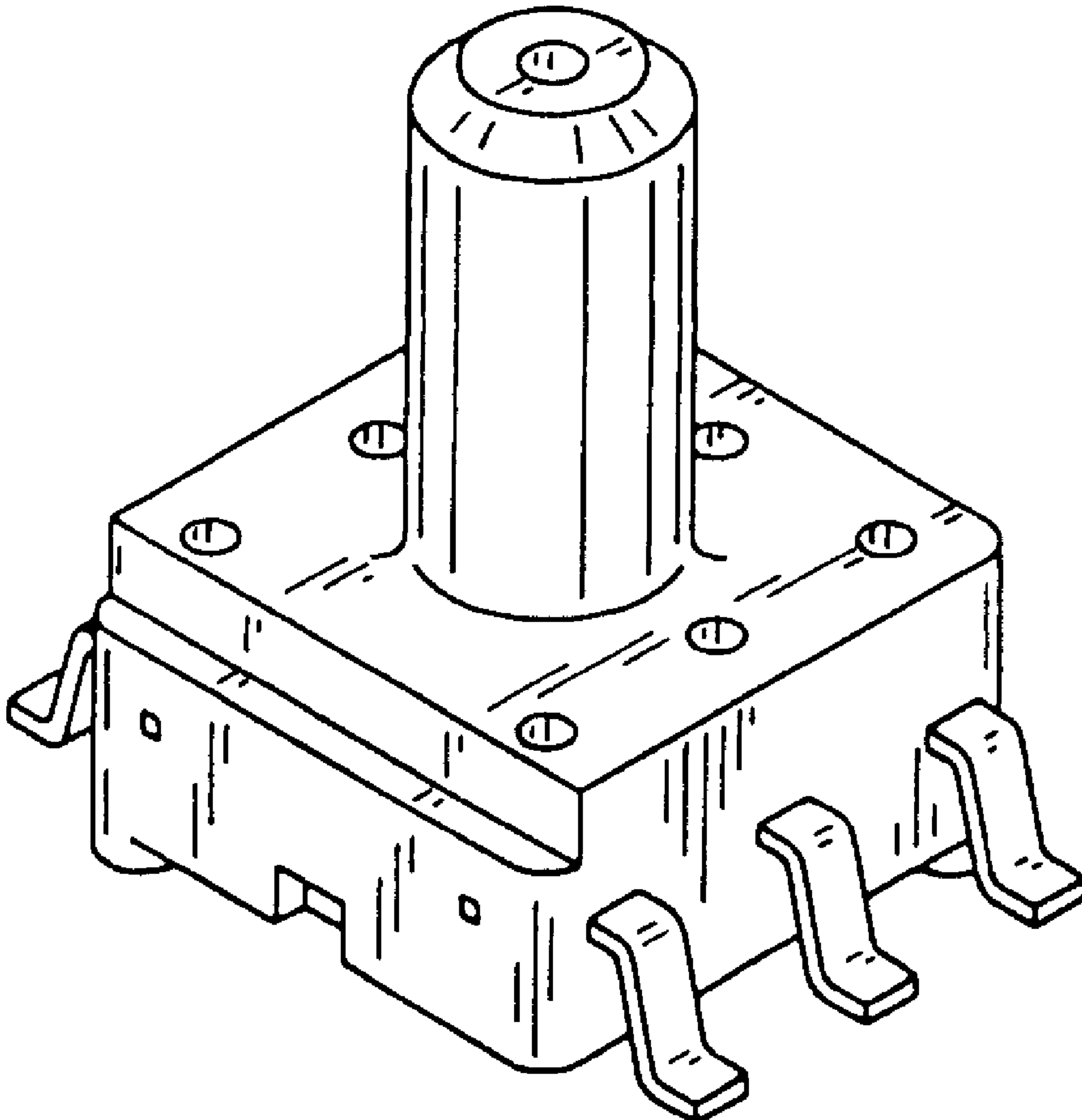
FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a left side elevational view thereof; and,

FIG. 7 is a right side elevational view thereof.

1 Claim, 1 Drawing Sheet



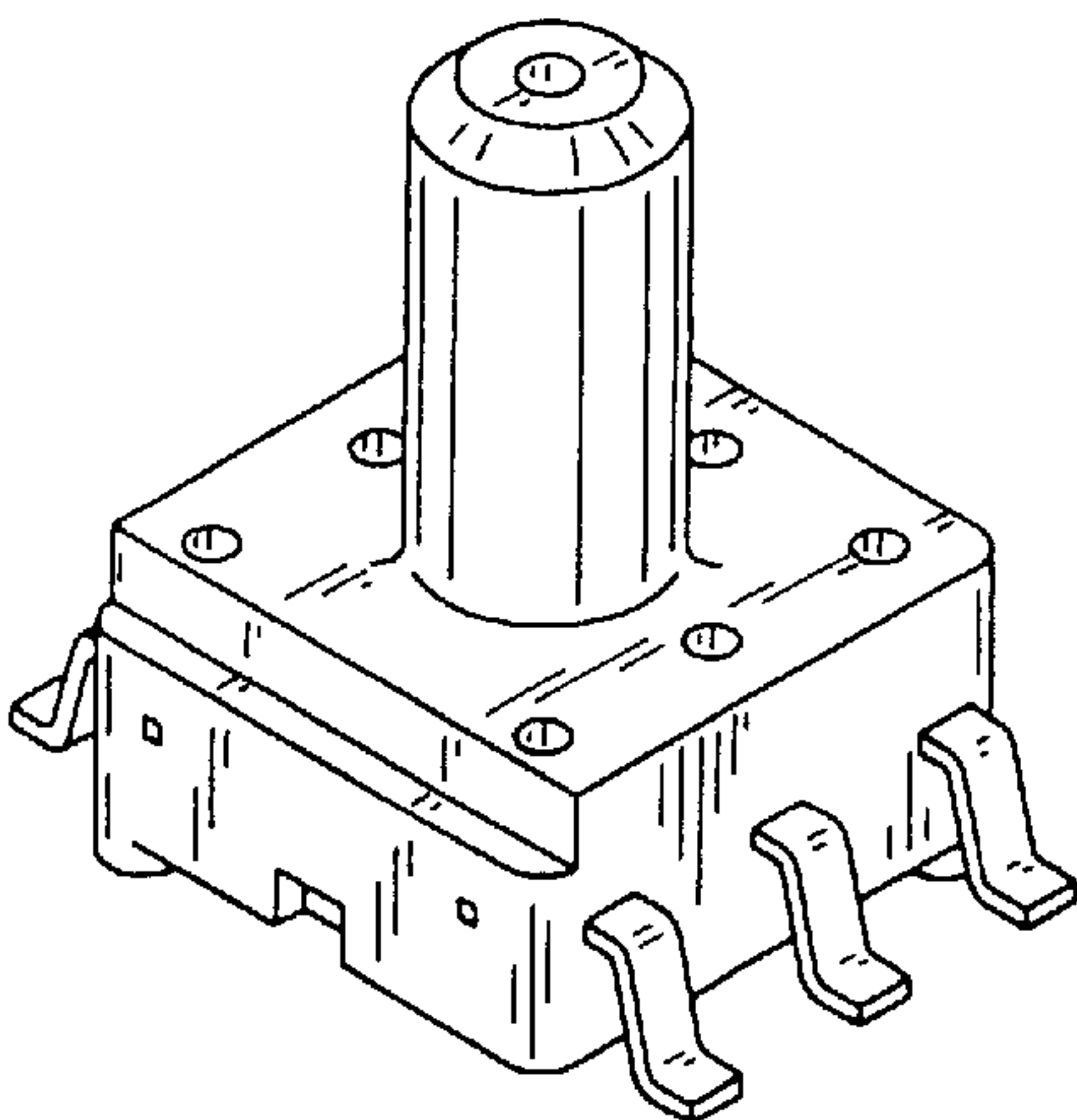


Fig. 1

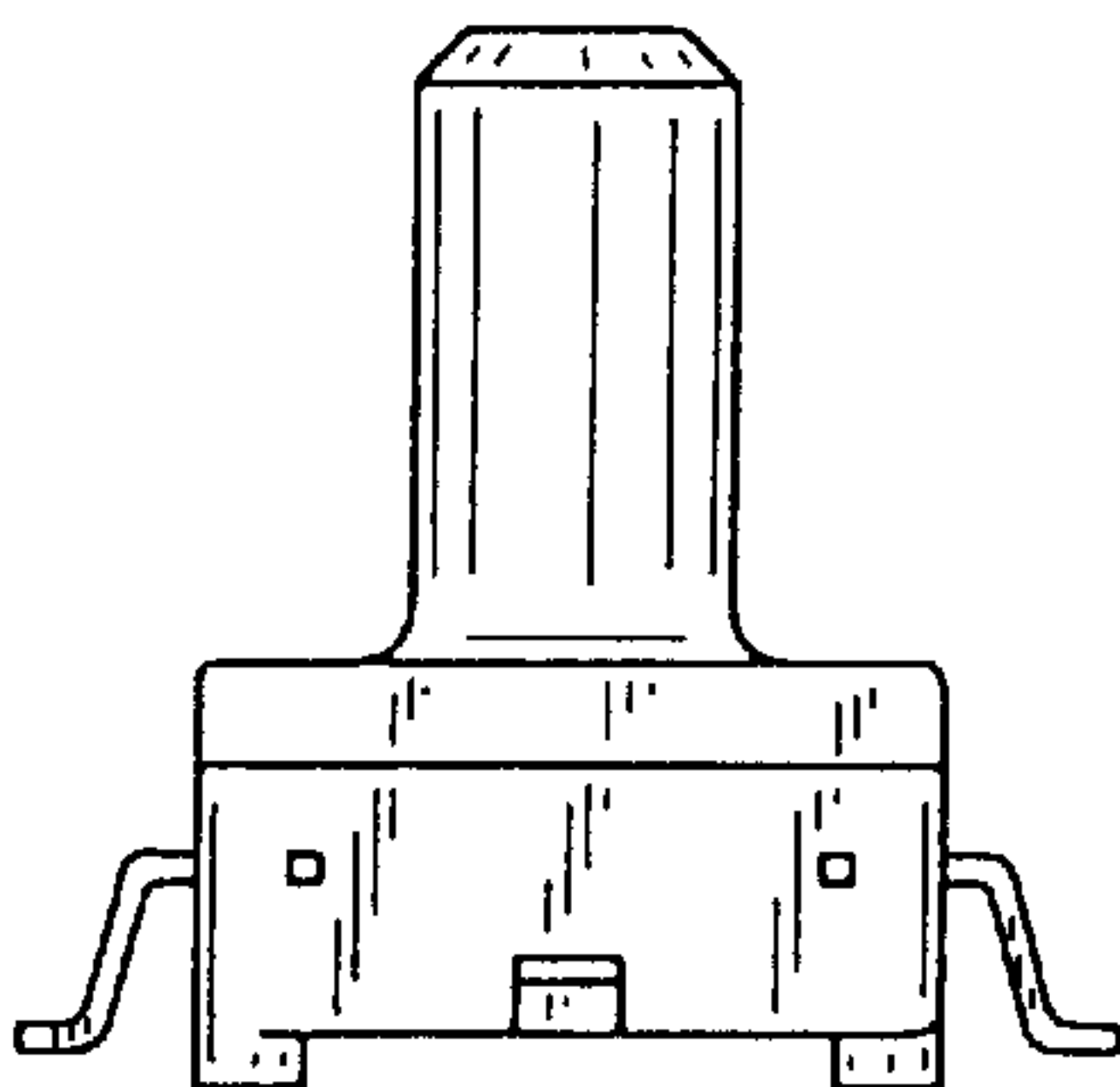


Fig. 2

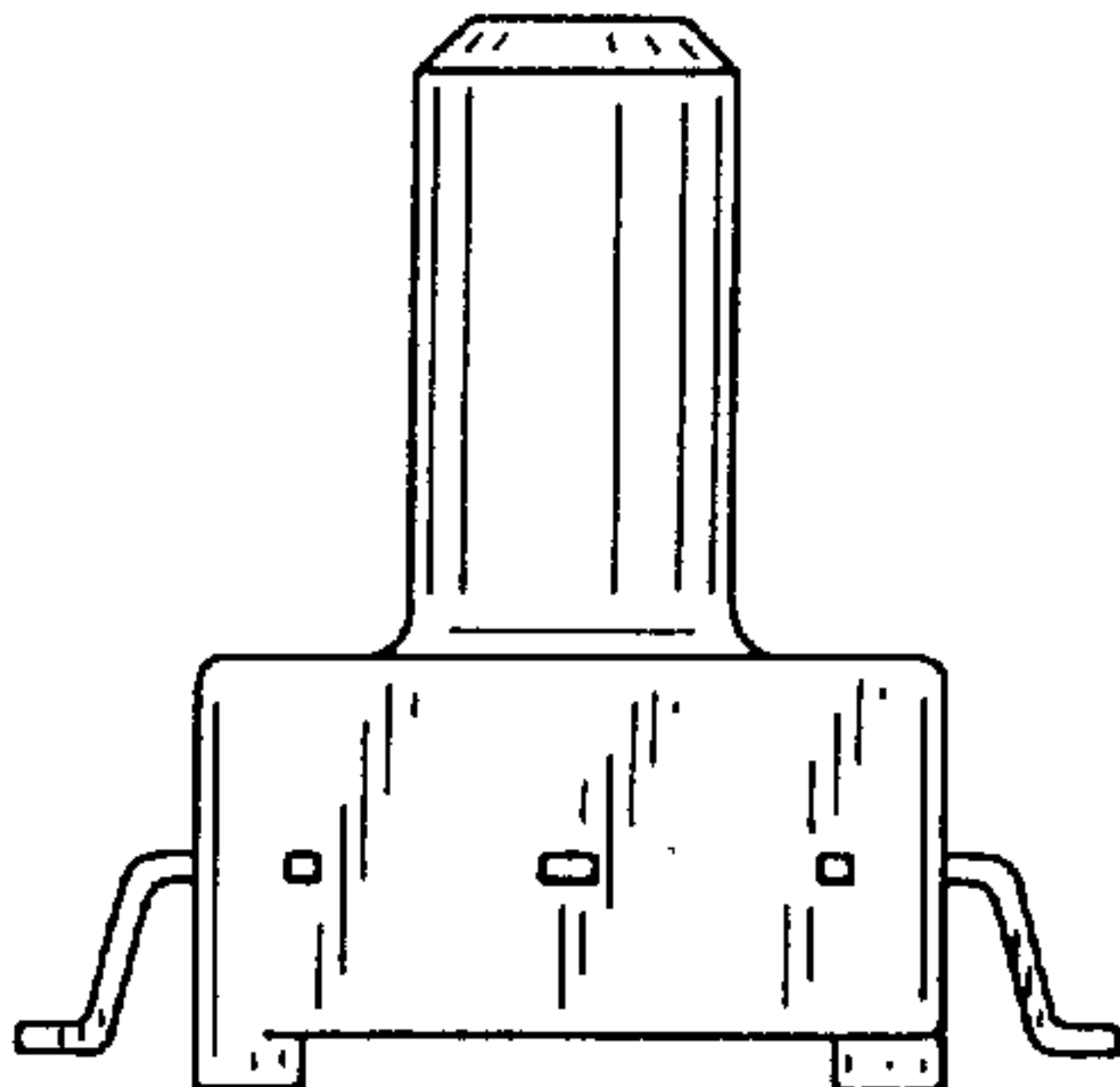


Fig. 3

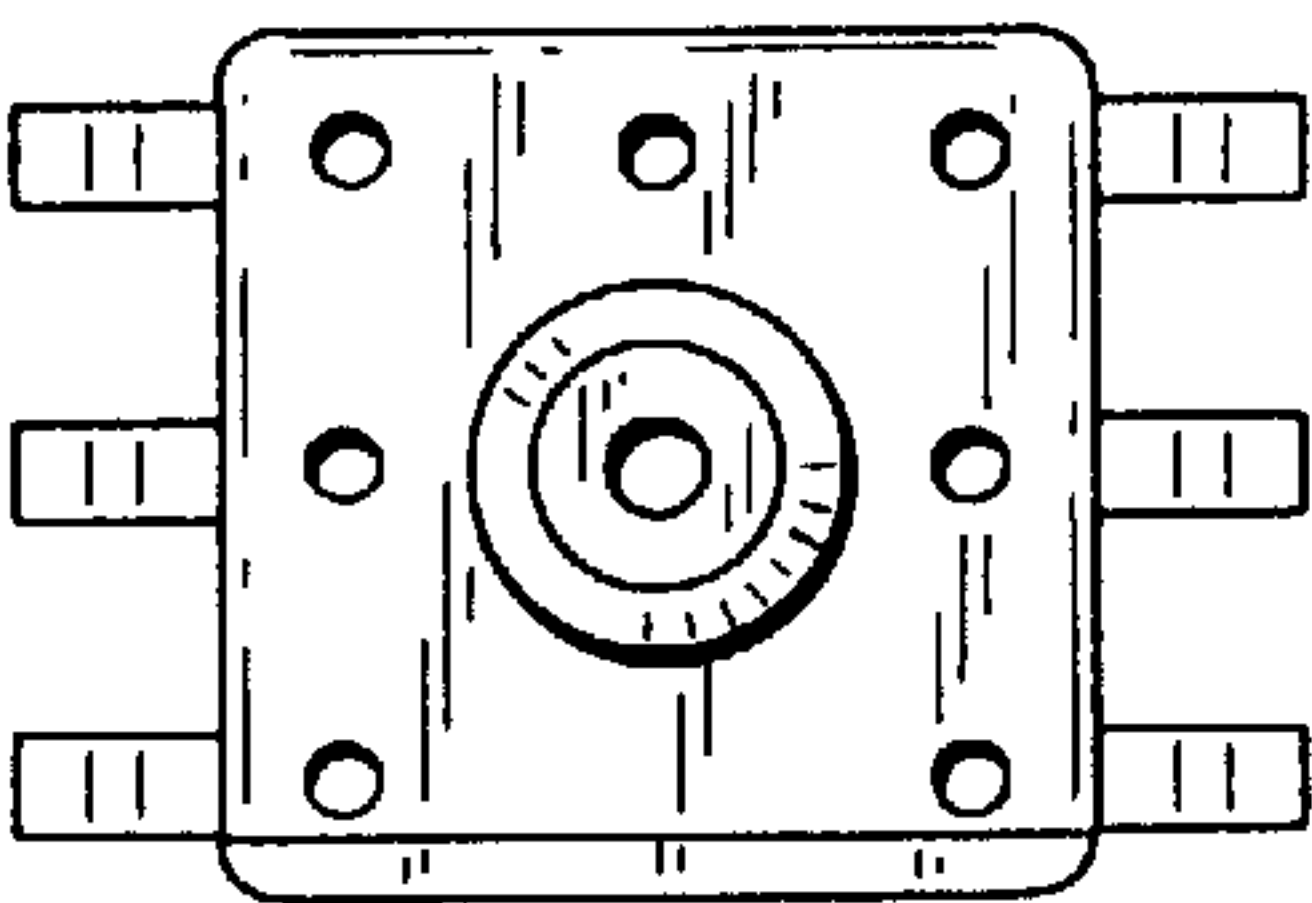


Fig. 4

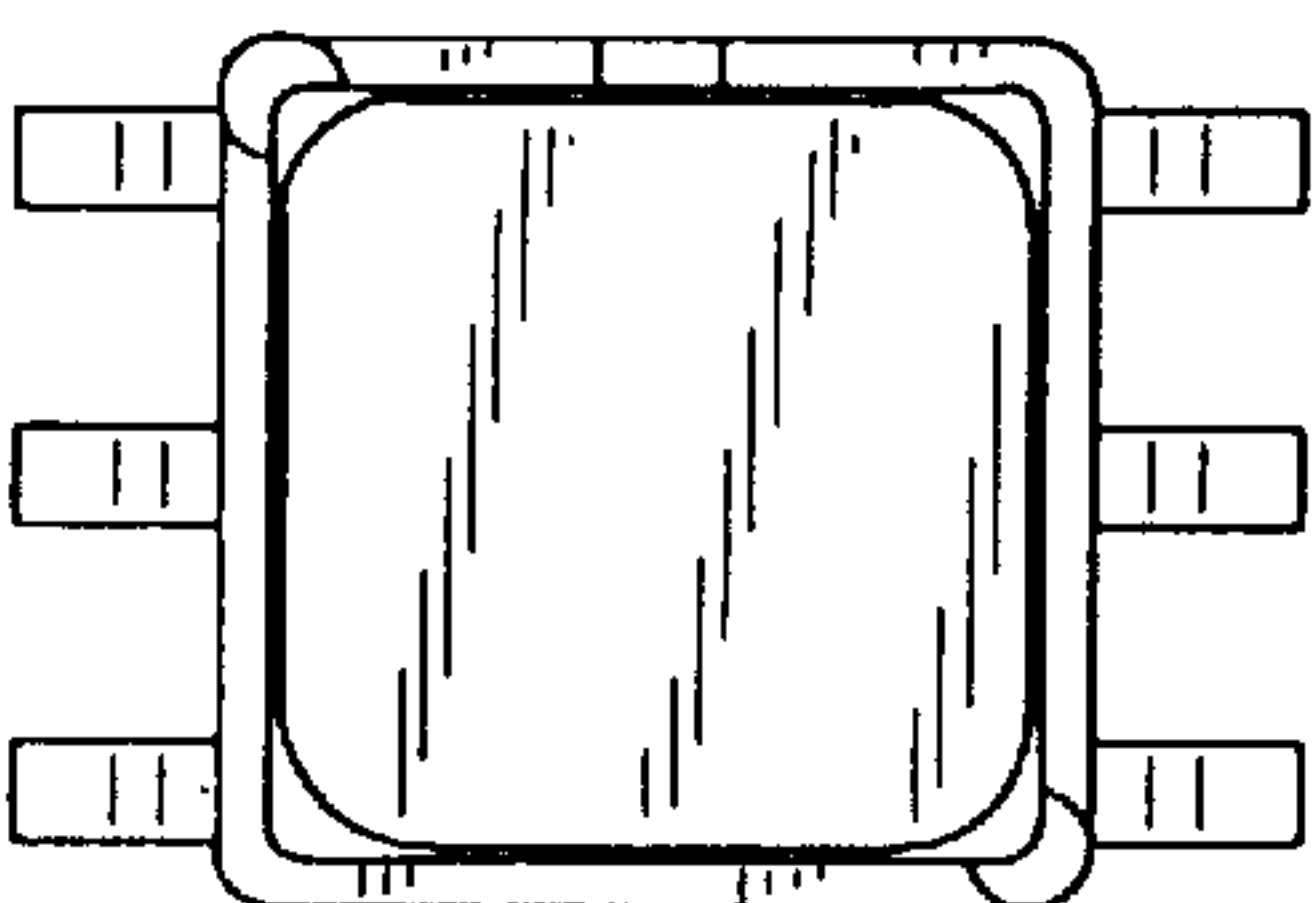


Fig. 5

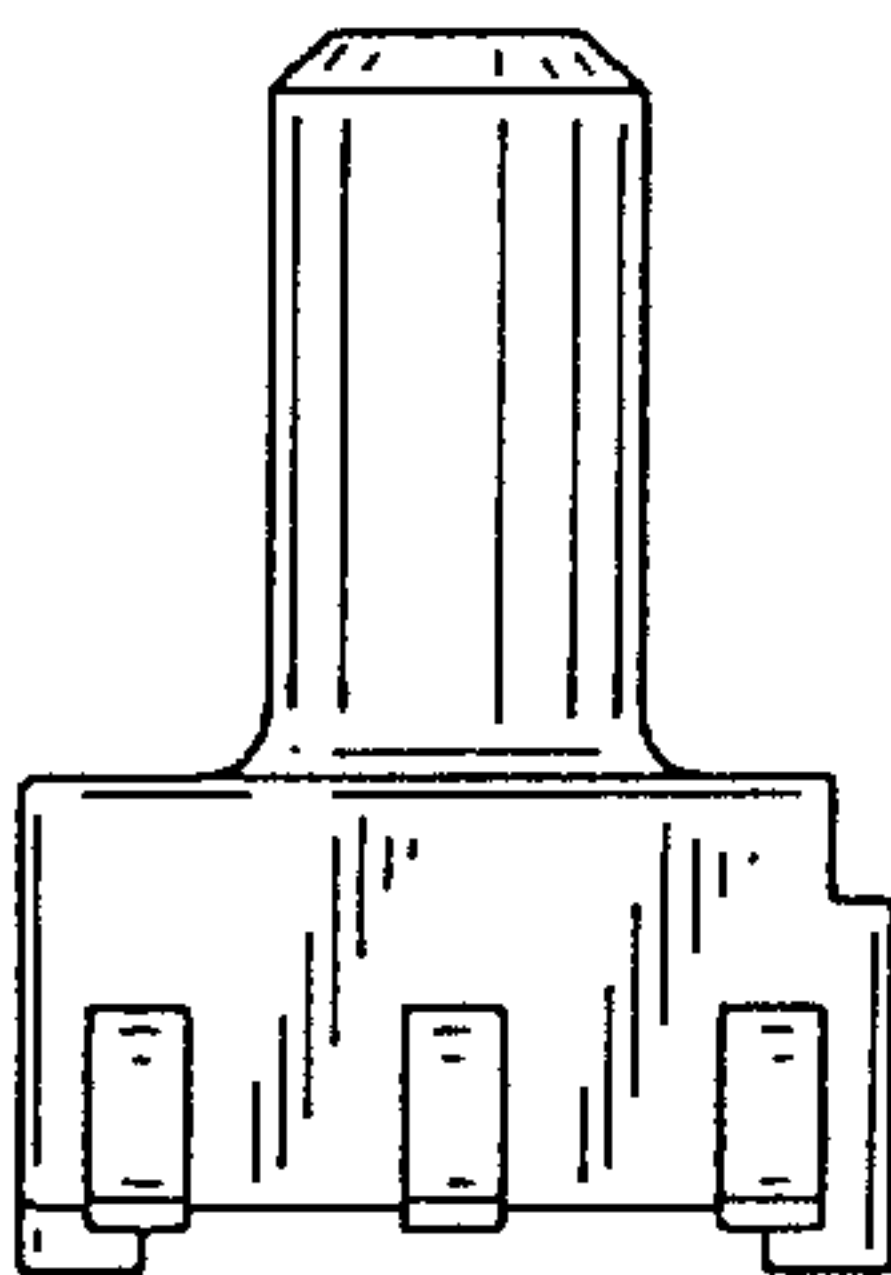


Fig. 6

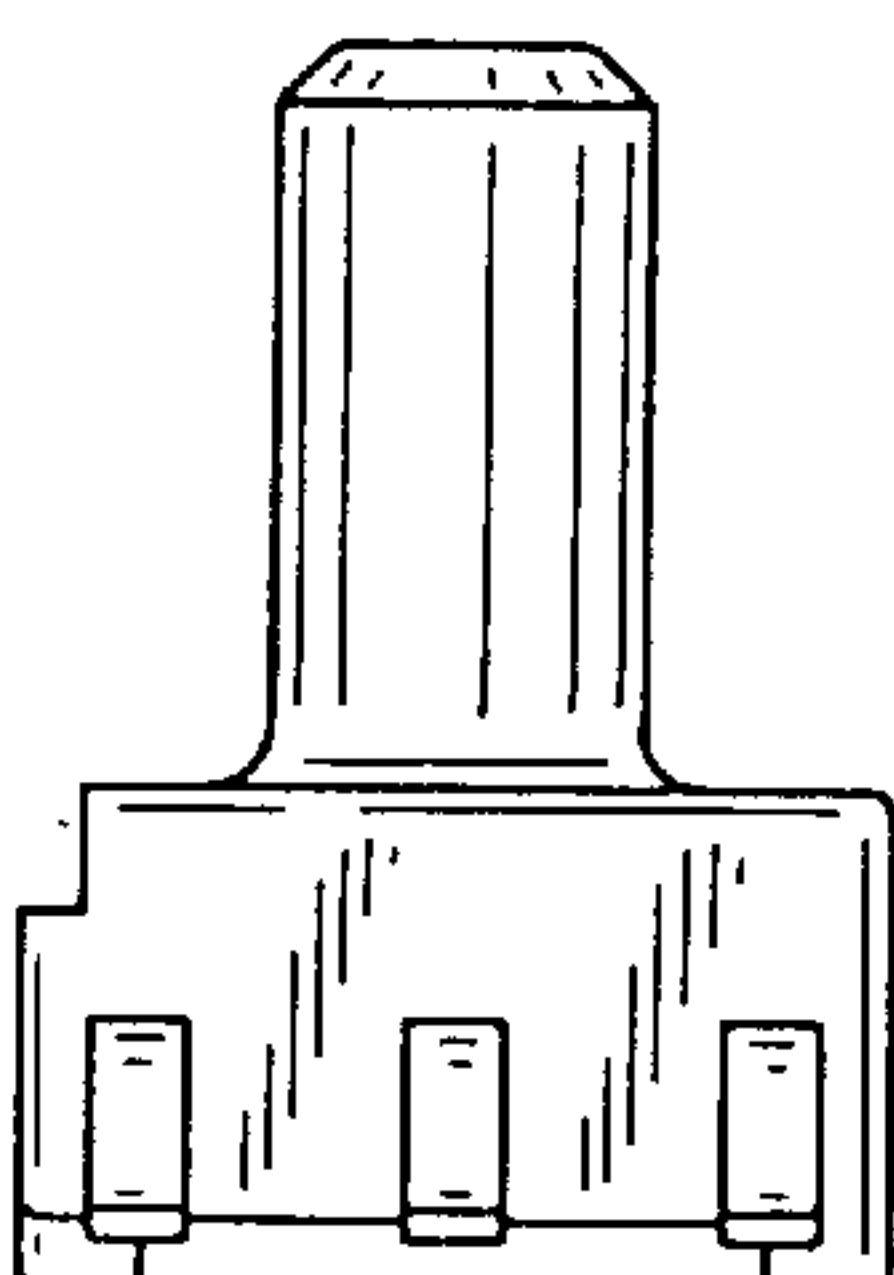


Fig. 7