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**United States Patent** [19]  
**Iwasaki**

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[45] **Date of Patent: \*\* Nov. 28, 2000**

[54] **IC CARD**

[75] **Inventor: Hiroshi Iwasaki, Yokohama, Japan**

[73] **Assignee: Kabushiki Kaisha Toshiba, Kawasaki, Japan**

[\*\*] **Term: 14 Years**

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[30] **Foreign Application Priority Data**

Jan. 22, 1999 [JP] Japan ..... 11-1431

[51] **LOC (7) Cl. .... 14-02**

[52] **U.S. Cl. .... D14/117**

[58] **Field of Search** ..... D14/114, 117;  
D13/182; 235/382.5, 375, 487-94, 441-3;  
361/736-7, 752; 174/52.1; 283/75-77, 48.1,  
107, 901; D19/9-10

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**OTHER PUBLICATIONS**

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Toshiba, pp. 1-32, "Toshiba MOS Digital Integrated Circuit Silicon Gate CMOS 64Mbit (8Mx8bit) CMOS NAND E<sup>2</sup>PROM (8M Byte SmartMedia™)", Nov. 6, 1998.

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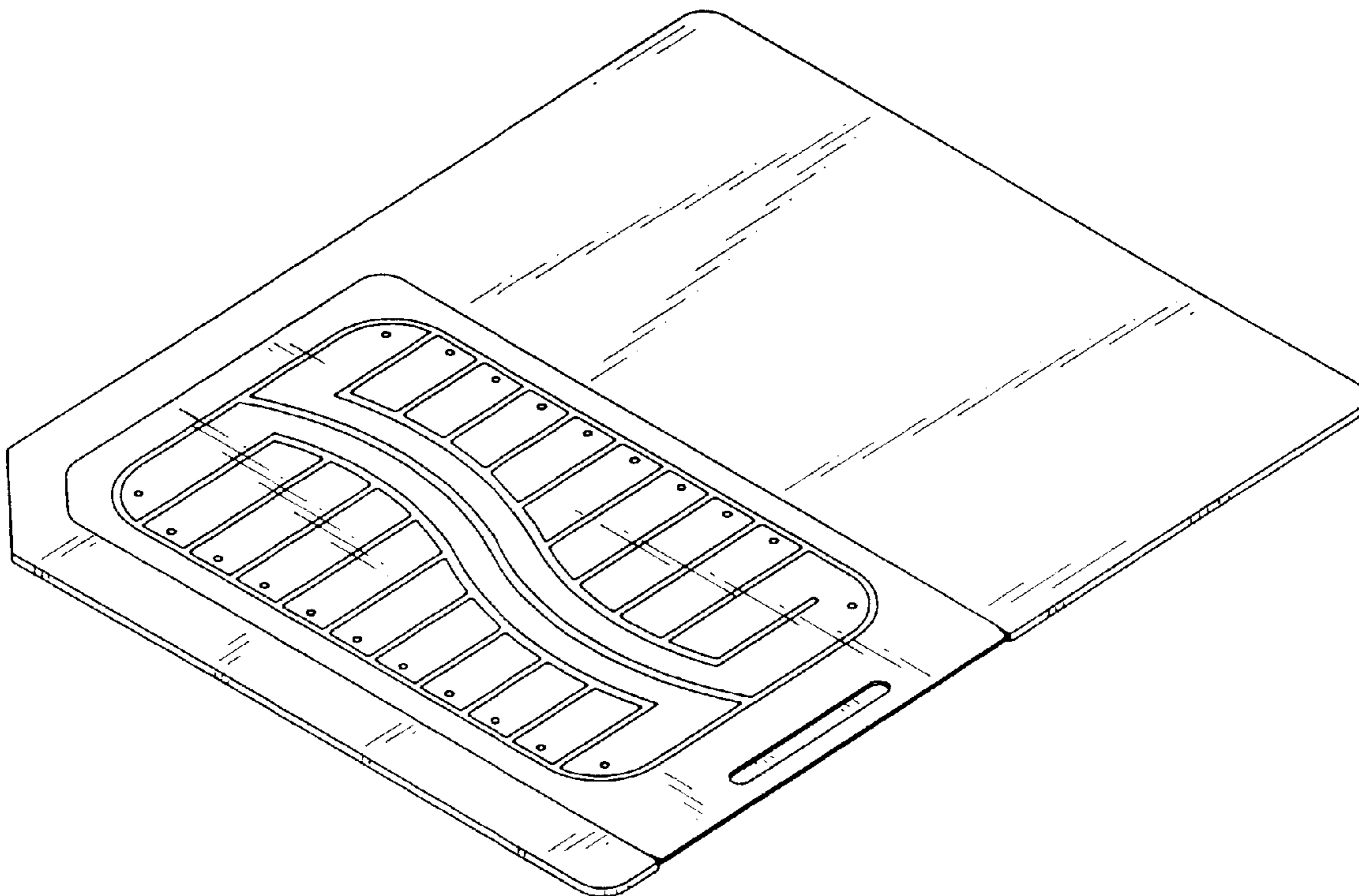
[57] **CLAIM**

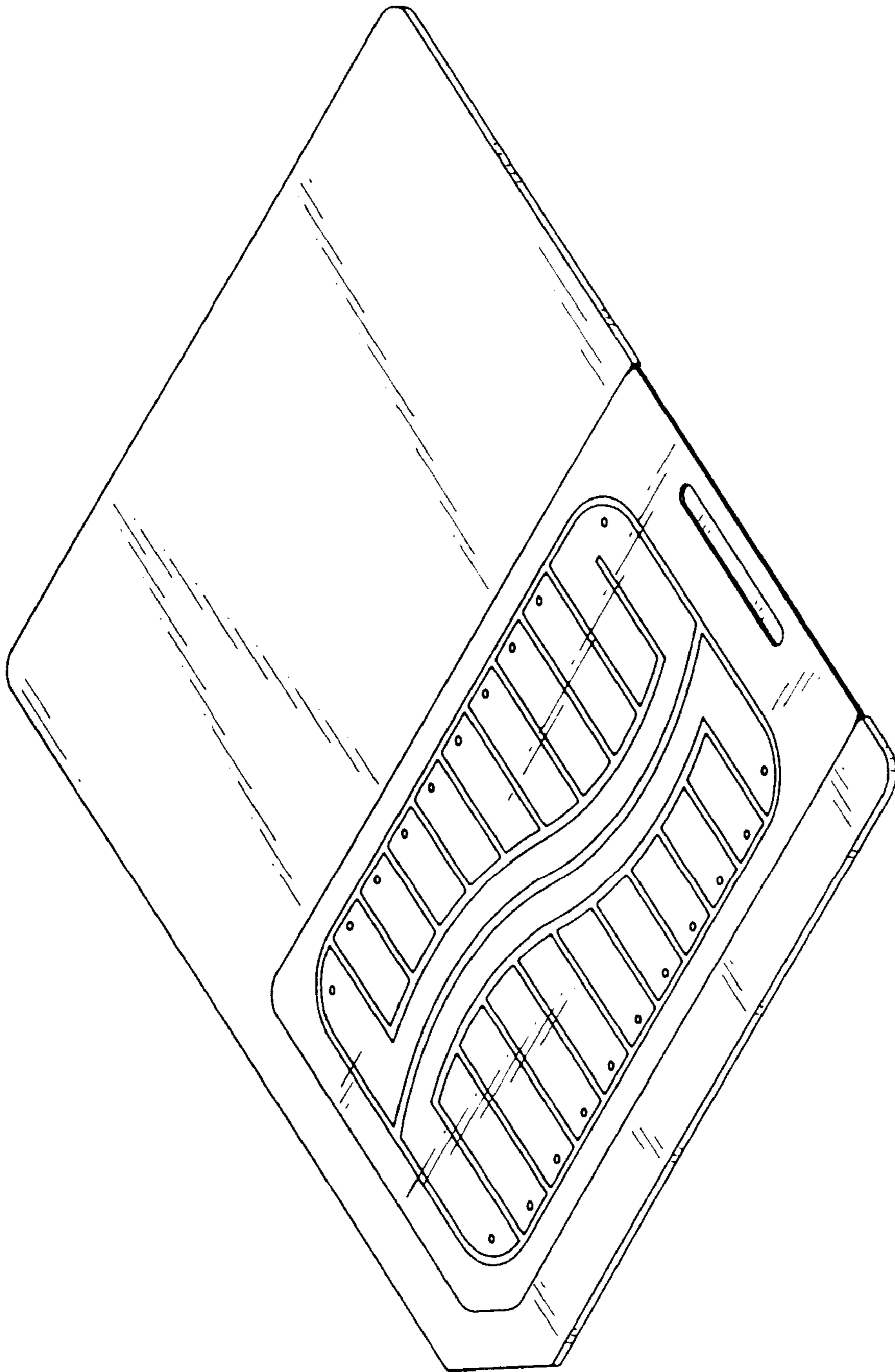
The ornamental design for an IC card, as shown.

**DESCRIPTION**

FIG. 1 is a front, bottom and right side perspective view of an IC card, showing my new design;  
FIG. 2 is a top plan view thereof;  
FIG. 3 is a right side elevational view thereof;  
FIG. 4 is a front elevational view thereof;  
FIG. 5 is a bottom plan view thereof;  
FIG. 6 is a left side elevational view thereof; and,  
FIG. 7 is a rear elevational view thereof.

**1 Claim, 2 Drawing Sheets**

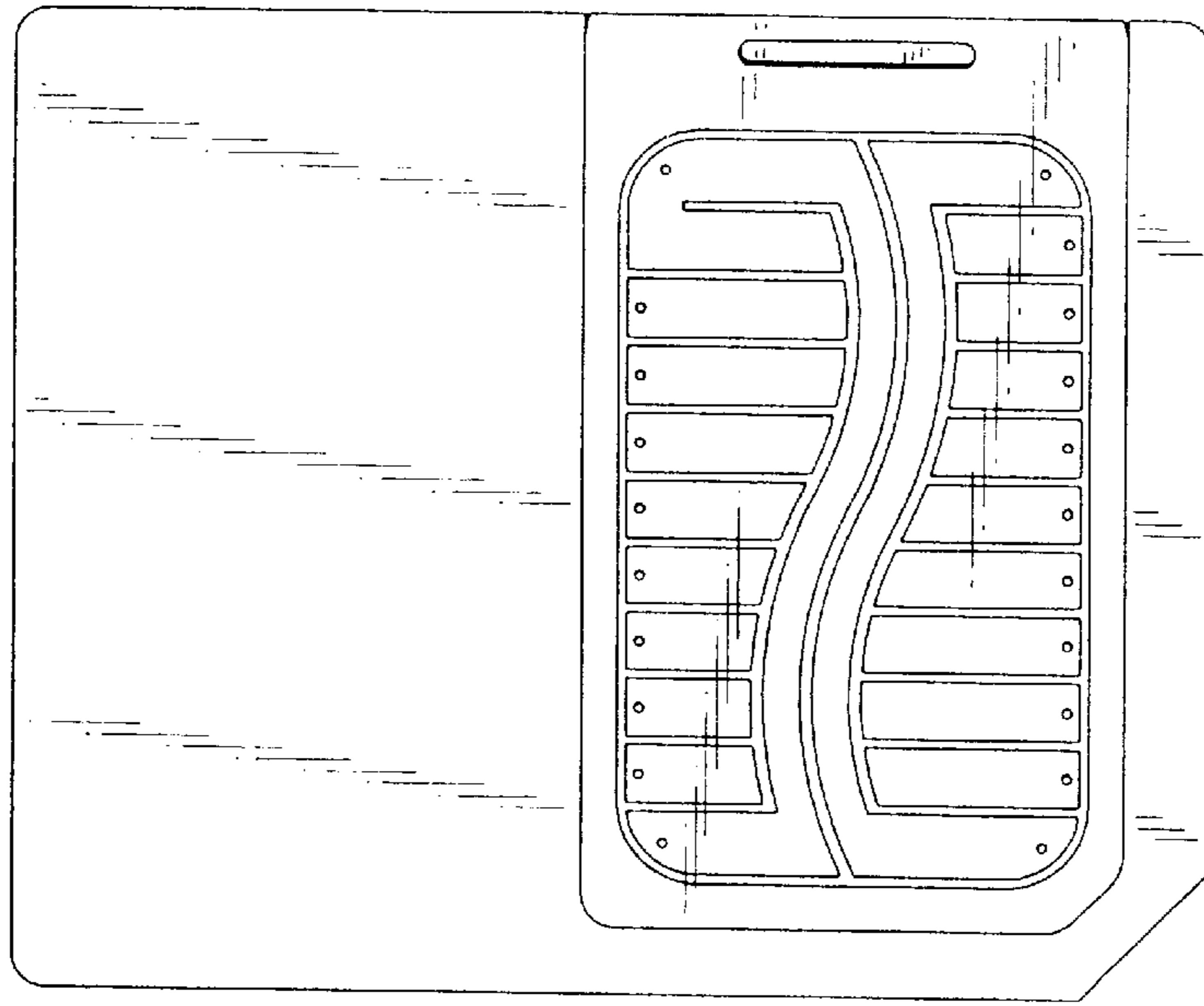




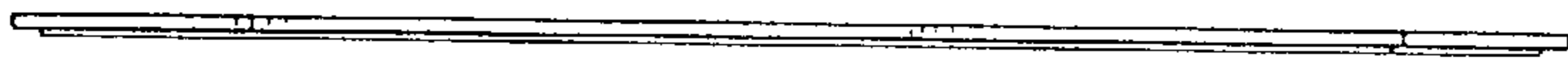
*Fig. 1*



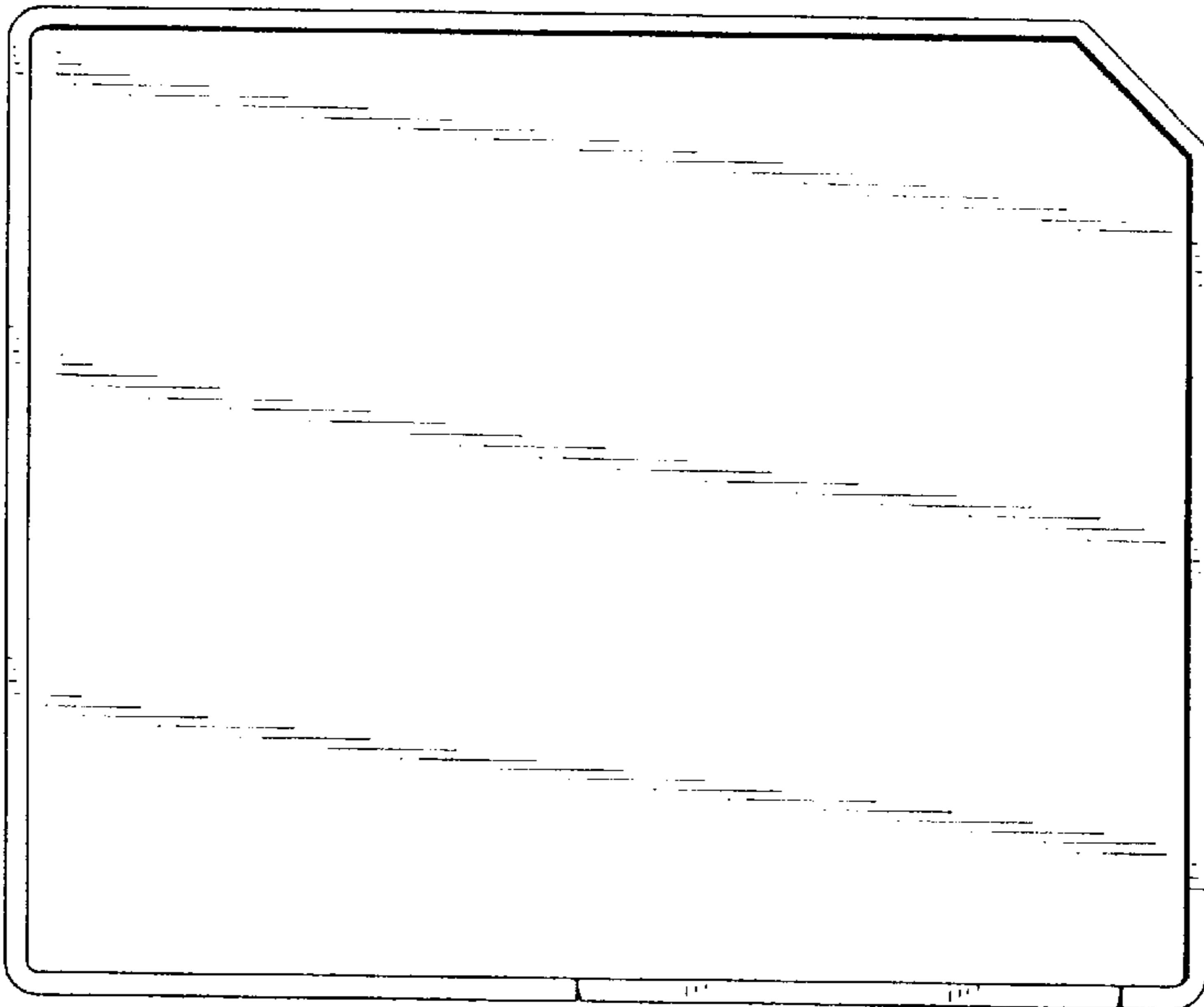
*Fig. 2*



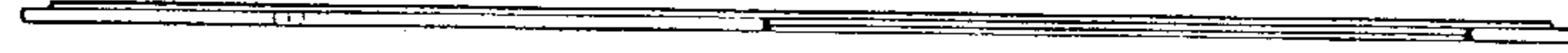
*Fig. 4*



*Fig. 6*



*Fig. 7*



*Fig. 3*



*Fig. 5*