



US00D432498S

United States Patent [19]

[11] **Patent Number: Des. 432,498**

Kuroda

[45] **Date of Patent: ** Oct. 24, 2000**

[54] **CONNECTOR FOR PRINTED CIRCUIT BOARDS**

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[73] Assignee: **Japan Solderless Terminal Mfg. Co., Ltd.**, Osaka, Japan

[**] Term: **14 Years**

[21] Appl. No.: **29/108,142**

[22] Filed: **Jul. 22, 1999**

[30] **Foreign Application Priority Data**

Jan. 28, 1999 [JP] Japan 11-2177

[51] **LOC (7) Cl.** **13-03**

[52] **U.S. Cl.** **D13/133**

[58] **Field of Search** D13/133; 439/329, 439/578, 596, 682, 686, 691, 695, 752, 876, 879

[56] **References Cited**

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Primary Examiner—Joel Sincavage
Attorney, Agent, or Firm—Antonelli, Terry, Stout & Kraus, LLP

[57] **CLAIM**

The ornamental design for a connector for printed circuit boards, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of a connector for printed circuit boards showing my claimed design;
 FIG. 2 is a bottom plan view thereof;
 FIG. 3 is a front elevation thereof;
 FIG. 4 is a rear elevation thereof;
 FIGS. 5 and 6 are right-hand and left-hand elevations, respectively;
 FIGS. 7, 8 and 9 are cross sections taken along lines 7—7, 8—8 and 9—9 in FIG. 1, respectively;
 FIGS. 10 and 11 are cross sections taken along the lines 10—10 and 11—11 in FIG. 5, respectively;
 FIGS. 12 and 13 are perspective views seen from the front and rear faces, respectively; and,
 FIG. 14 is a cross section corresponding to FIG. 7 but including the adjacent parts of relevant members that may be involved when using the connector.

The broken lines showing various adjacent parts are included for the purpose of illustrating environmental structure only and forms no part of the claimed design.

1 Claim, 3 Drawing Sheets

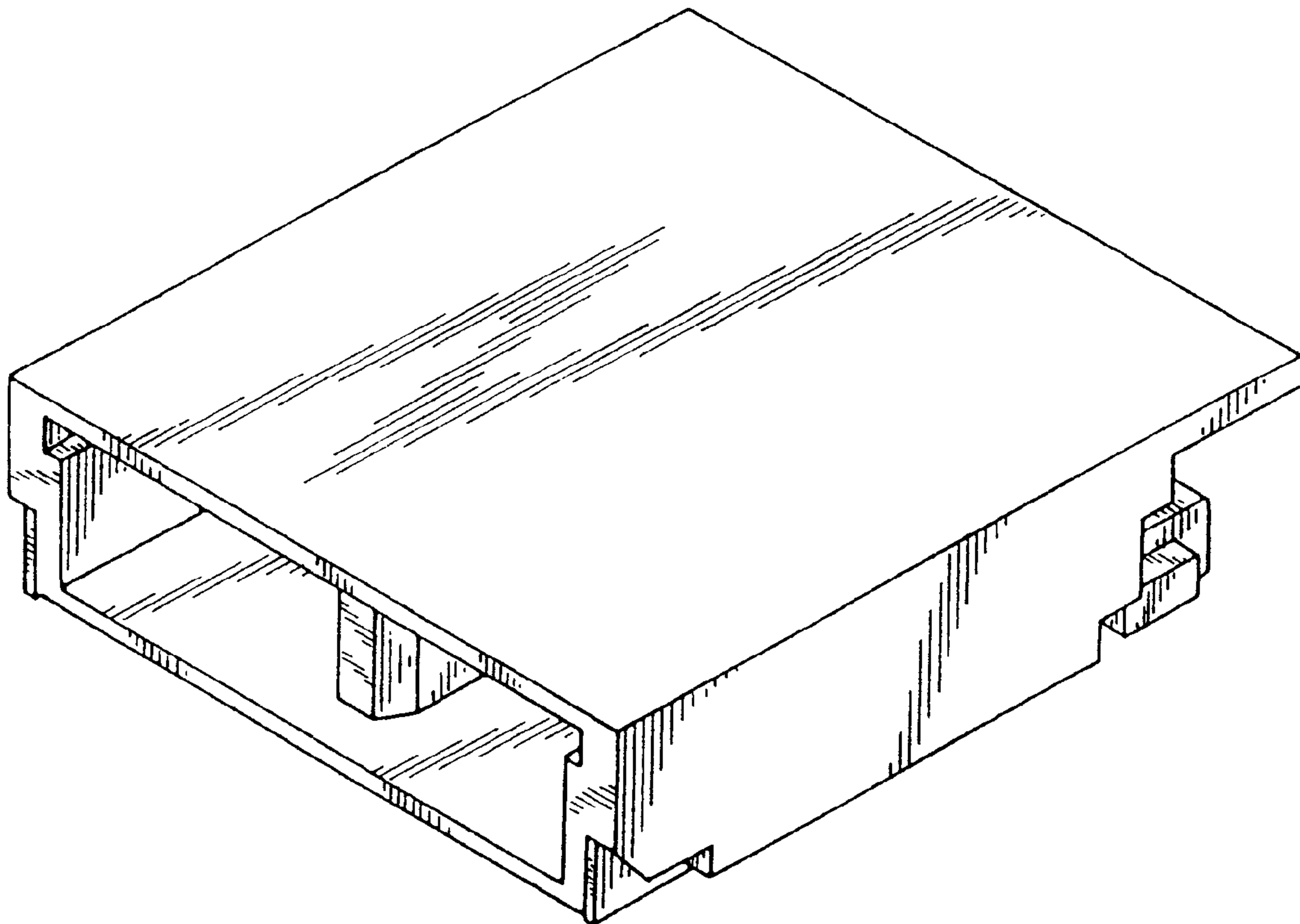


FIG. 1

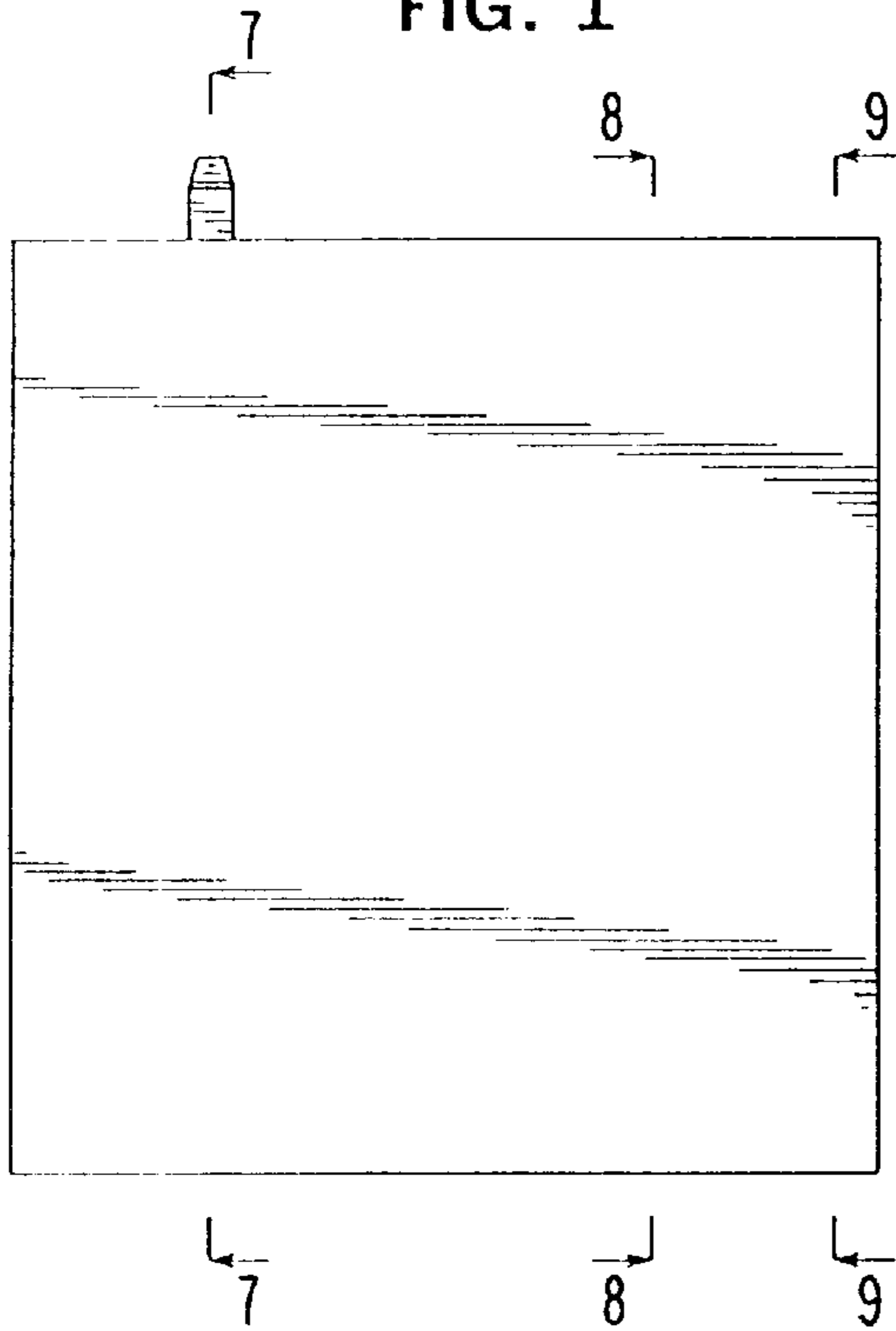


FIG. 2

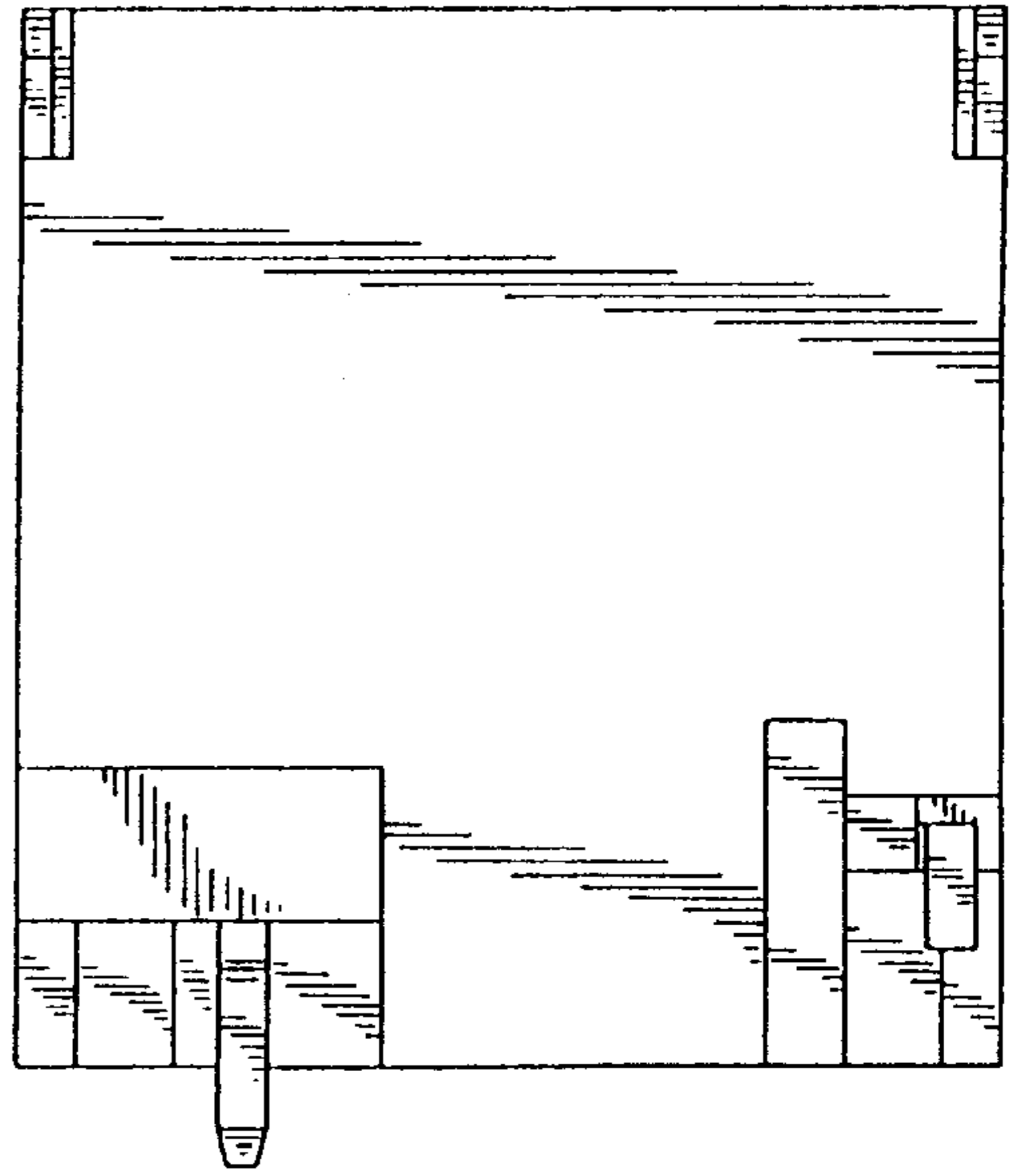


FIG. 3

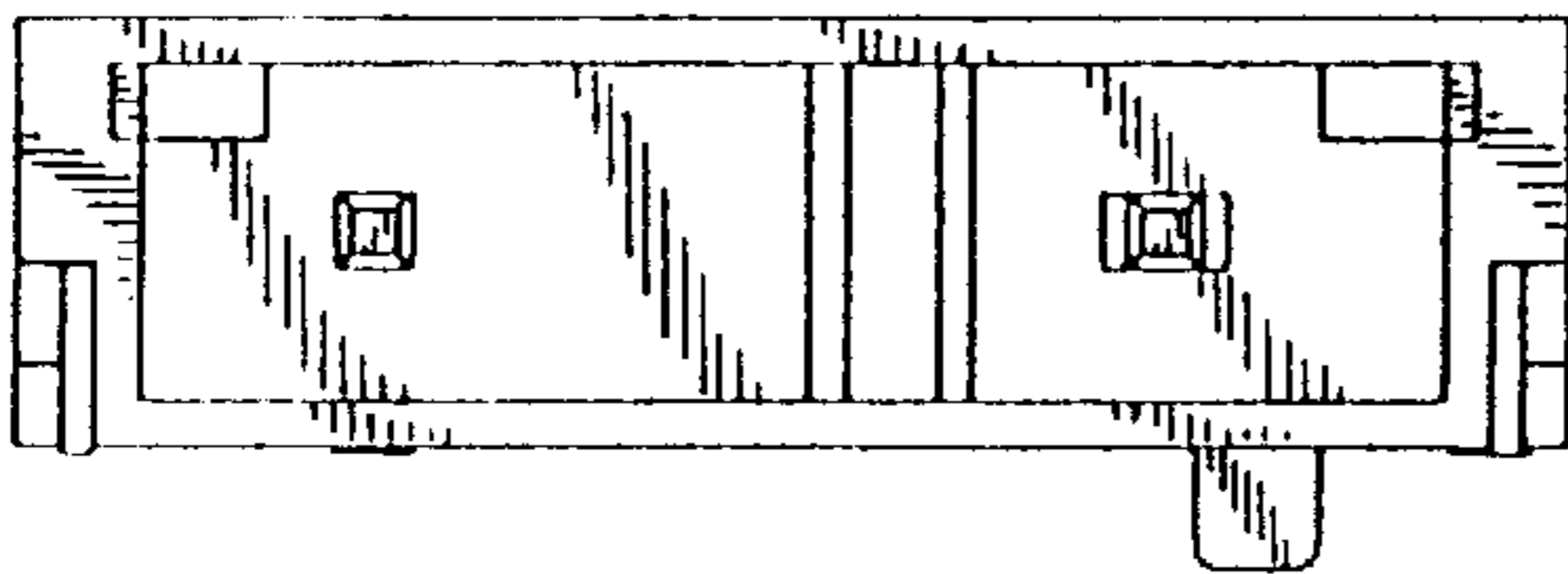


FIG. 4

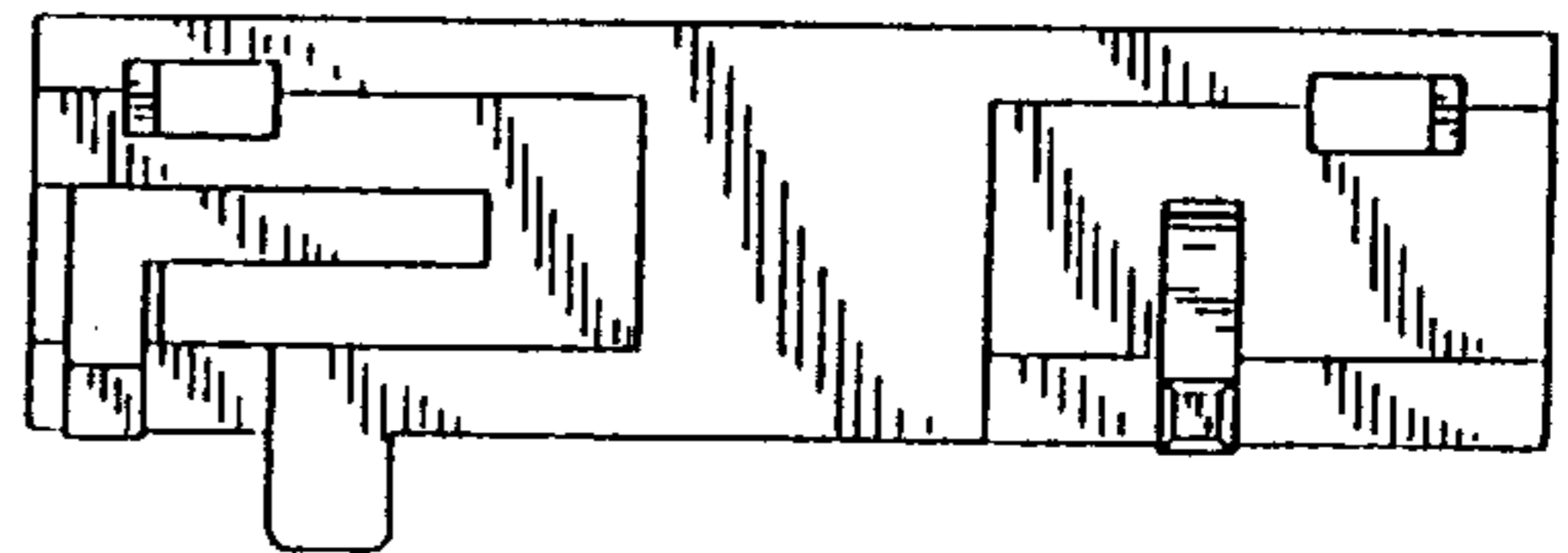


FIG. 5

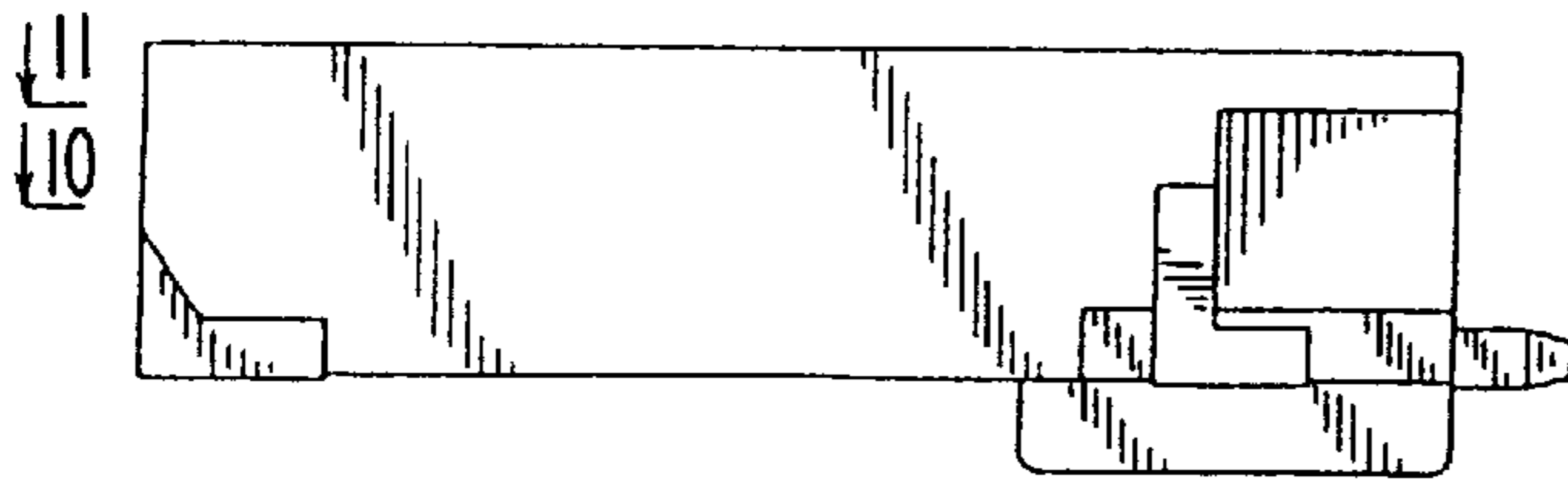


FIG. 6

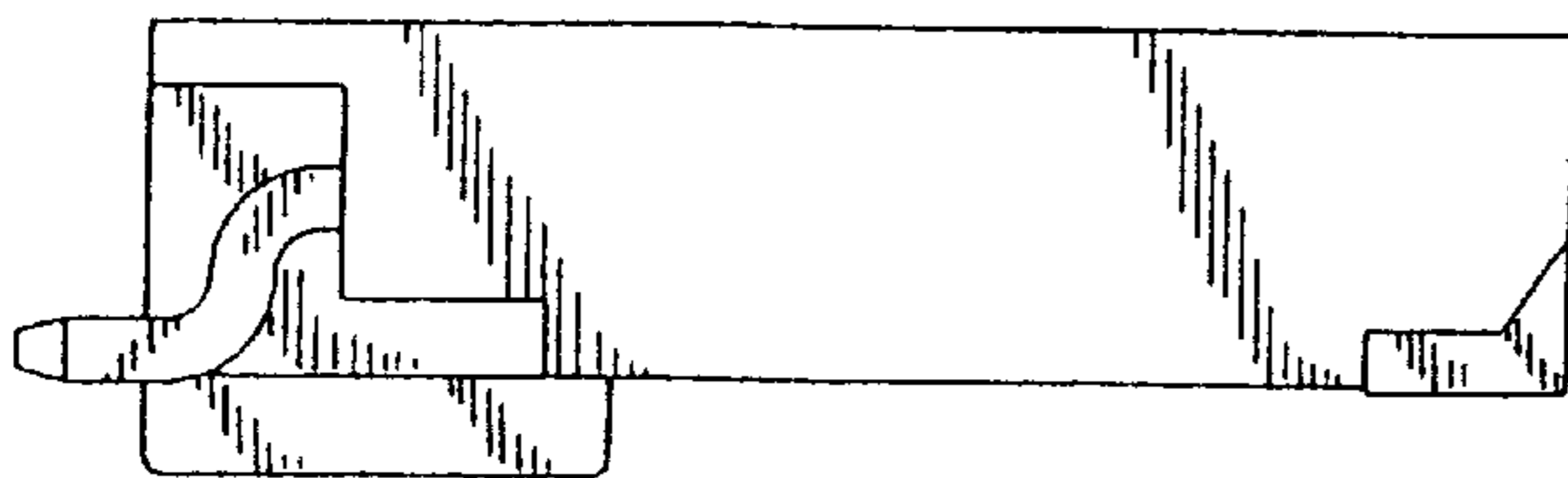


FIG. 7

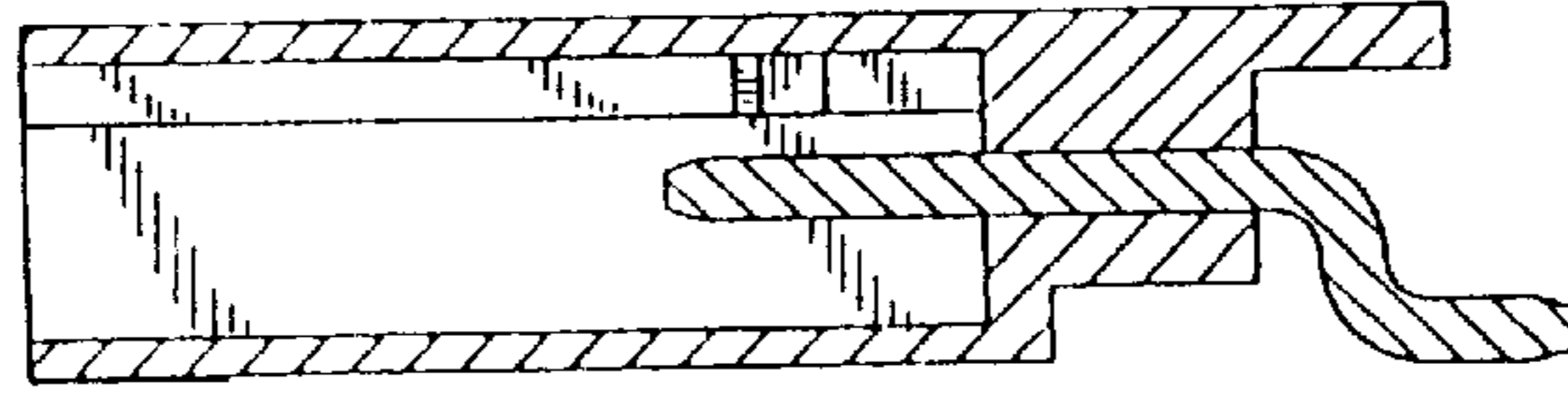


FIG. 8

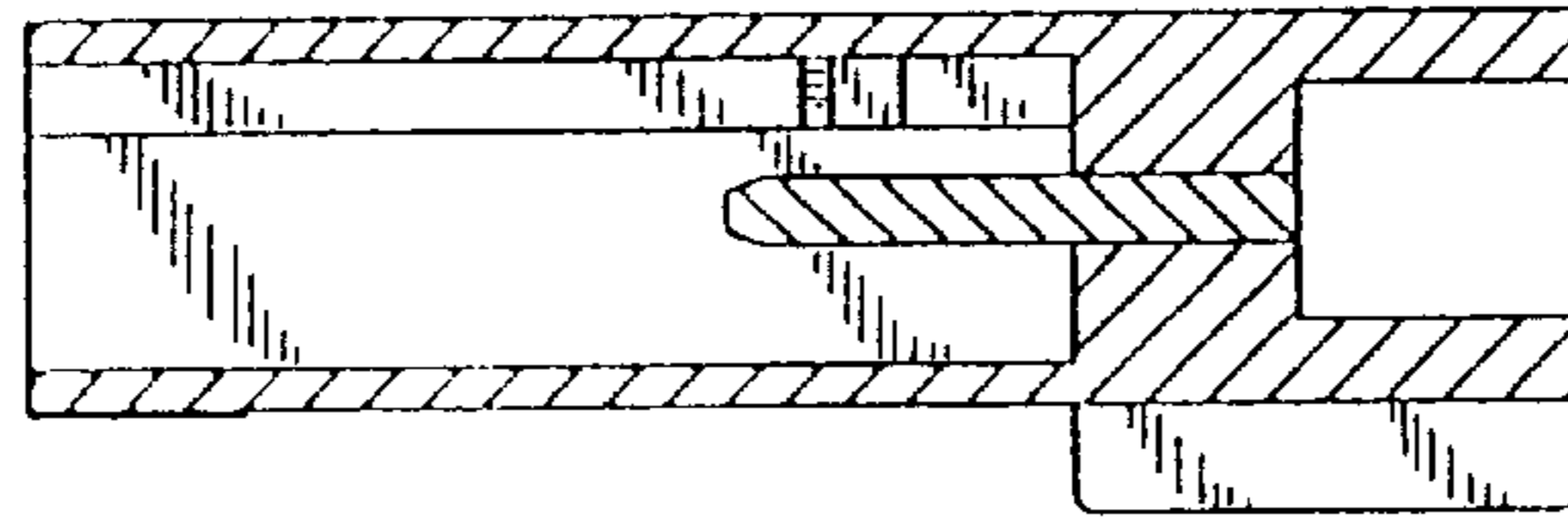


FIG. 9

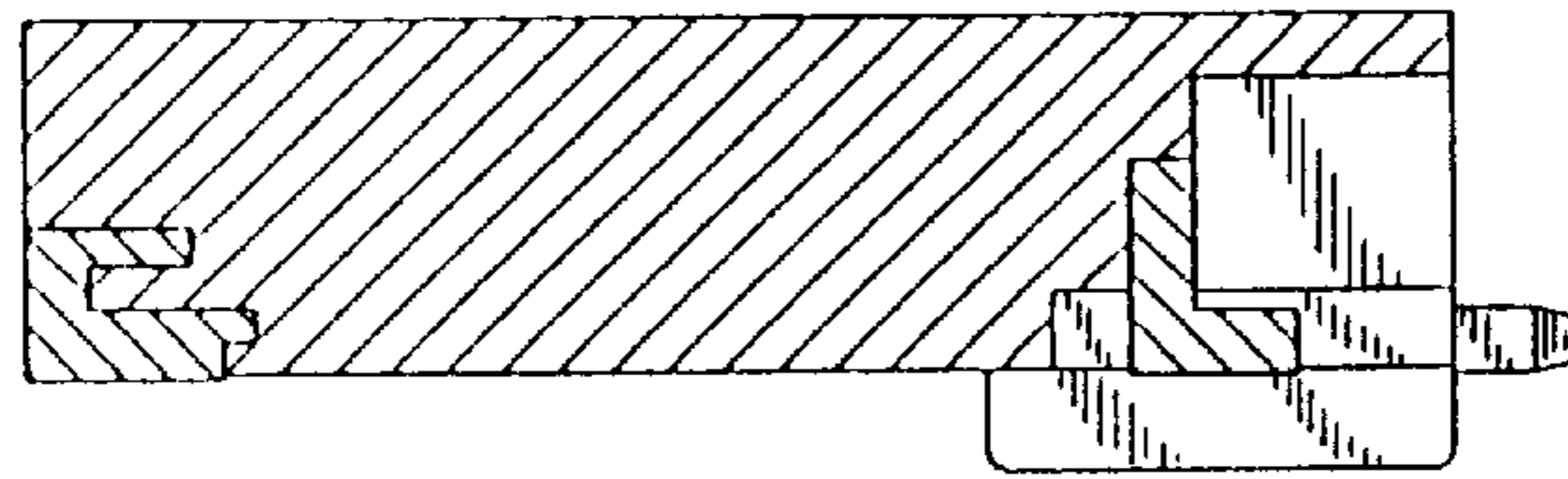


FIG. 10

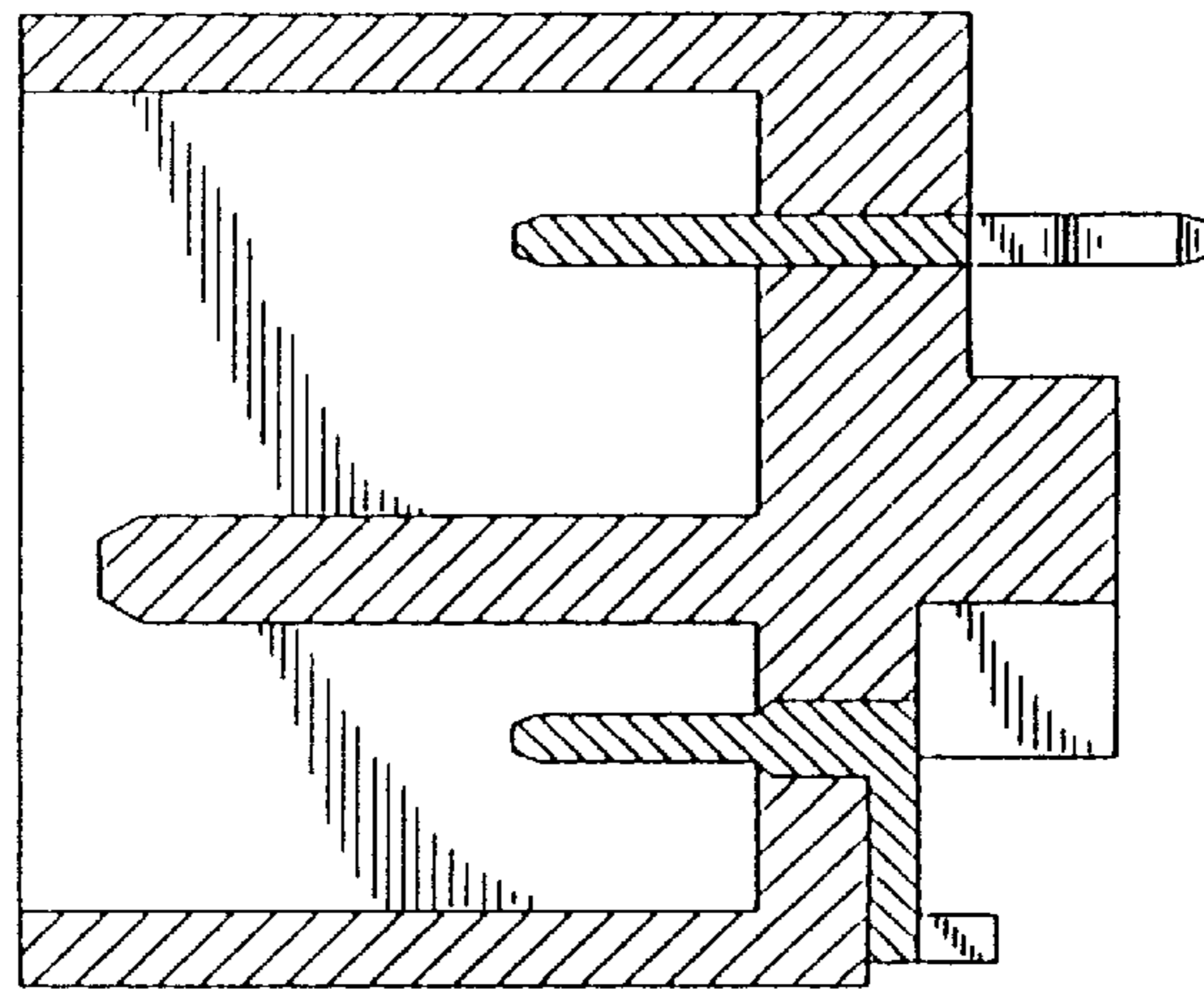


FIG. 11

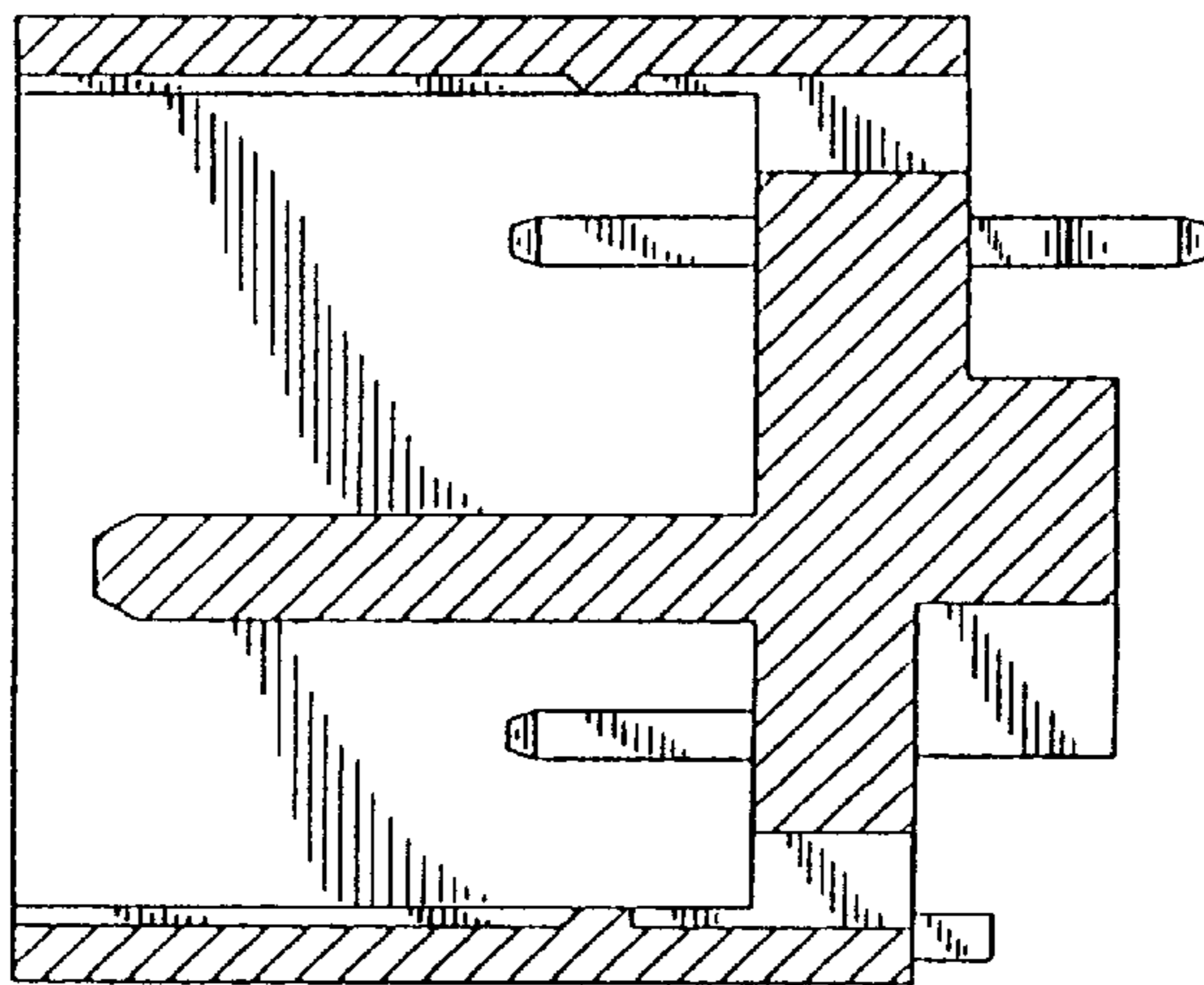


FIG. 14

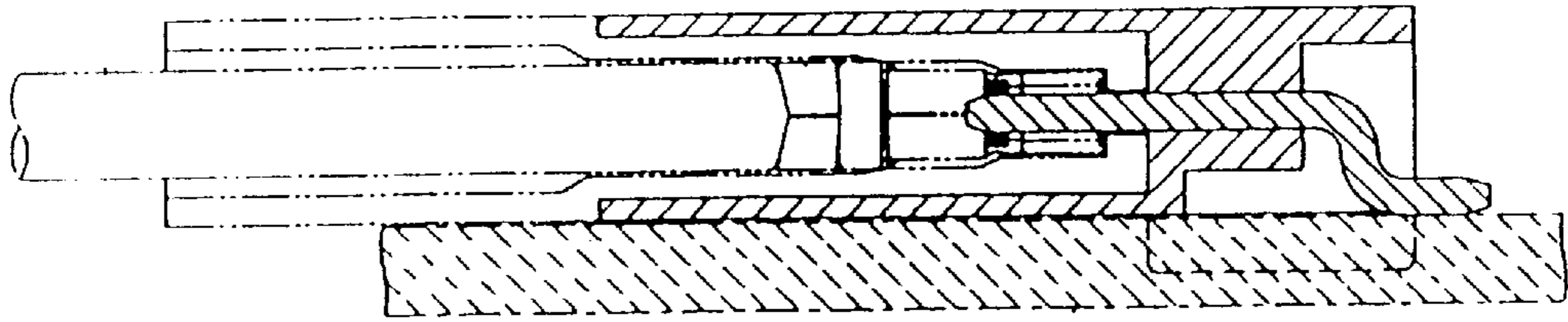


FIG. 12

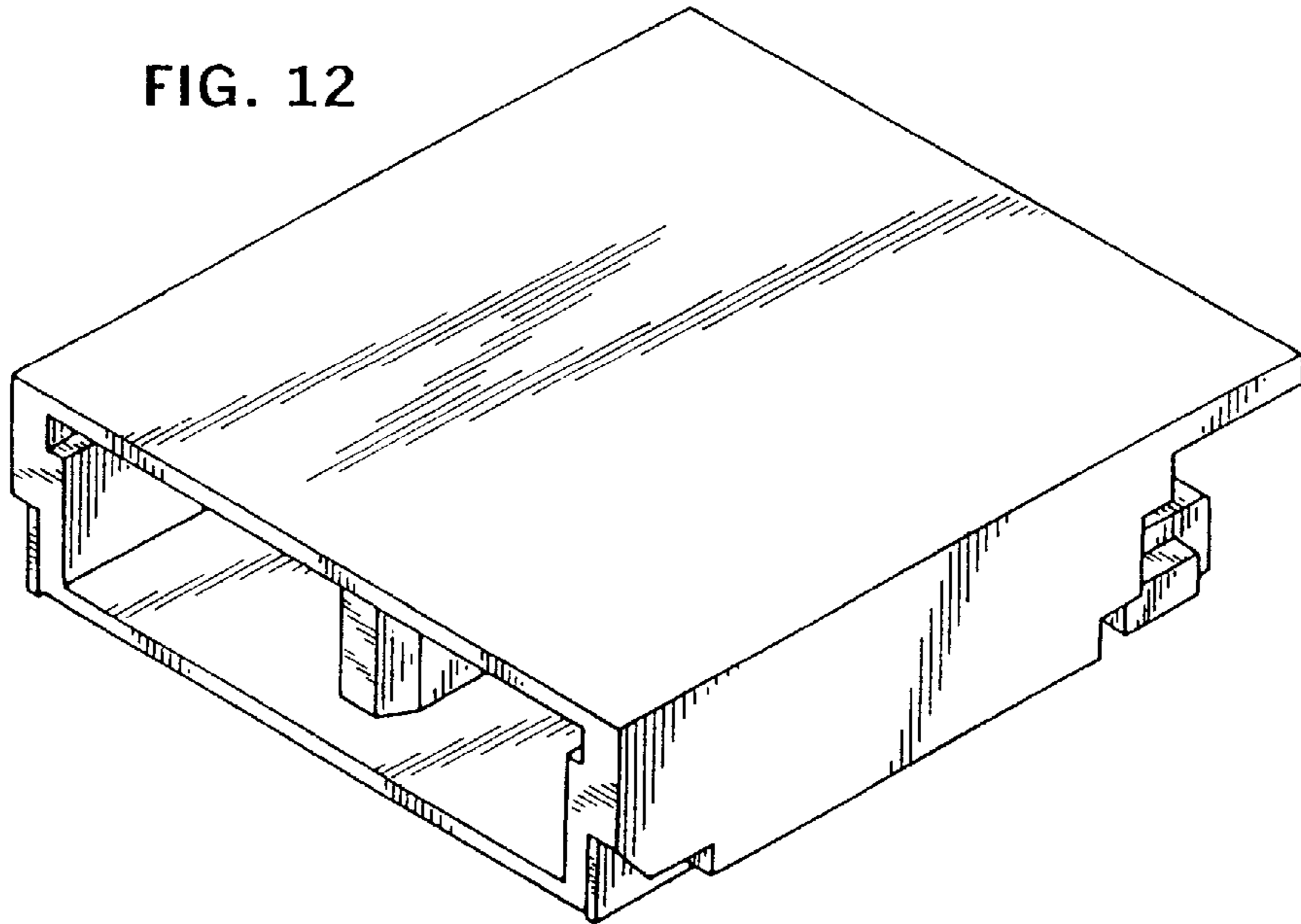


FIG. 13

