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United States Patent [19]

Ito et al.

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[45] **Date of Patent: ** Oct. 17, 2000**

[54] **DIGITAL-ANALOG CONVERTER**

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[73] Assignee: **TEAC Corporation**, Tokyo, Japan

[**] Term: **14 Years**

[21] Appl. No.: **29/101,712**

[22] Filed: **Mar. 10, 1999**

[30] **Foreign Application Priority Data**

Oct. 7, 1998 [JP] Japan 10-28840

[51] **LOC (7) Cl.** **13-03**

[52] **U.S. Cl.** **D13/123**

[58] **Field of Search** D13/123; D14/433, D14/434, 356, 357, 358; 341/144–172

[56] **References Cited**

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Primary Examiner—Joel Sincavage
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[57] **CLAIM**

The ornamental design for a digital-analog converter, as shown and described.

DESCRIPTION

FIG. **1** is a perspective view of a digital-analog converter showing our new design;
FIG. **2** is a front view thereof;
FIG. **3** is a rear view thereof;
FIG. **4** is a top view thereof;
FIG. **5** is a bottom view thereof; and,
FIG. **6** is a right side view thereof, the left side view being a mirror image of the right side.

1 Claim, 2 Drawing Sheets

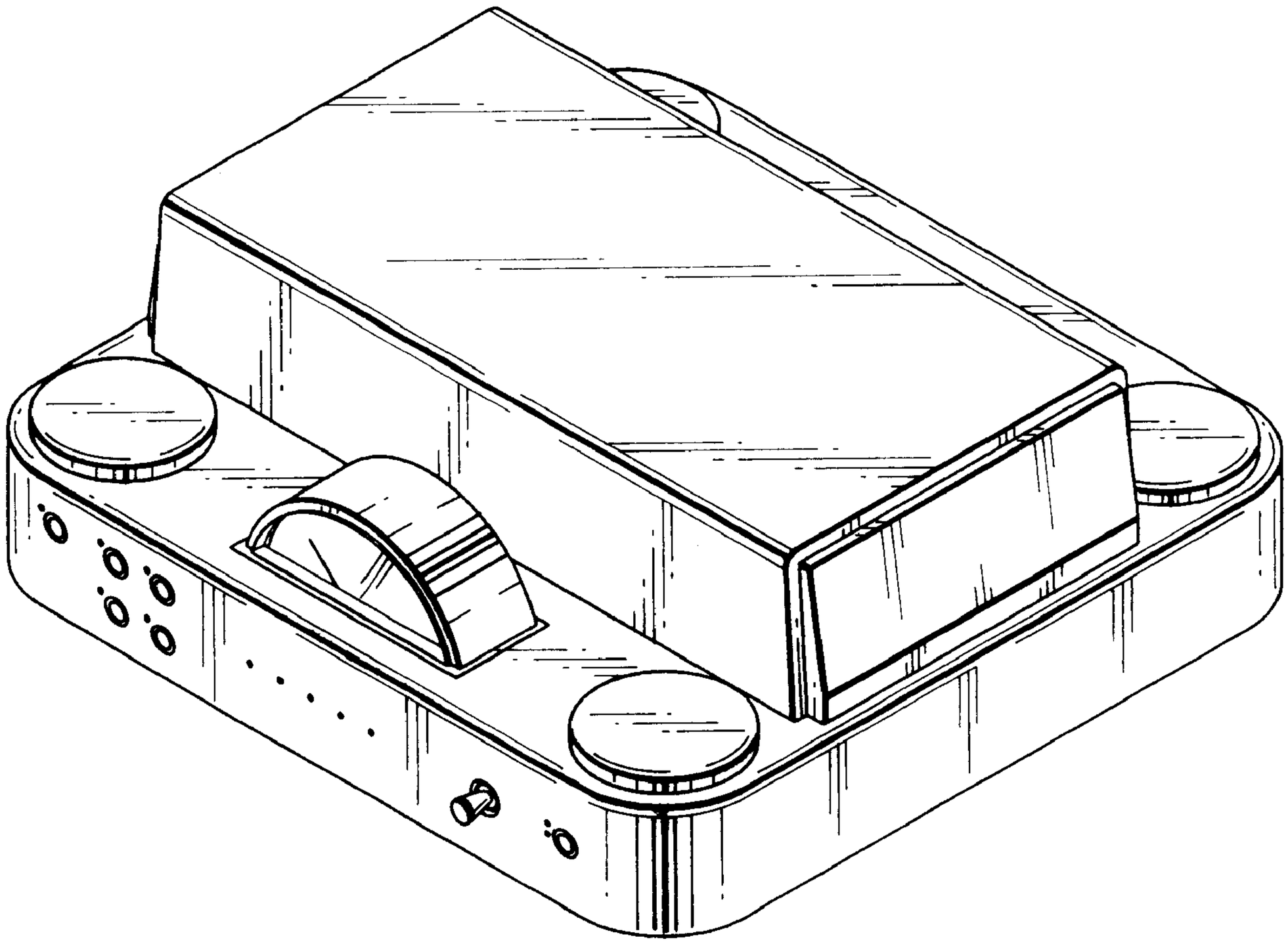

FIG.1

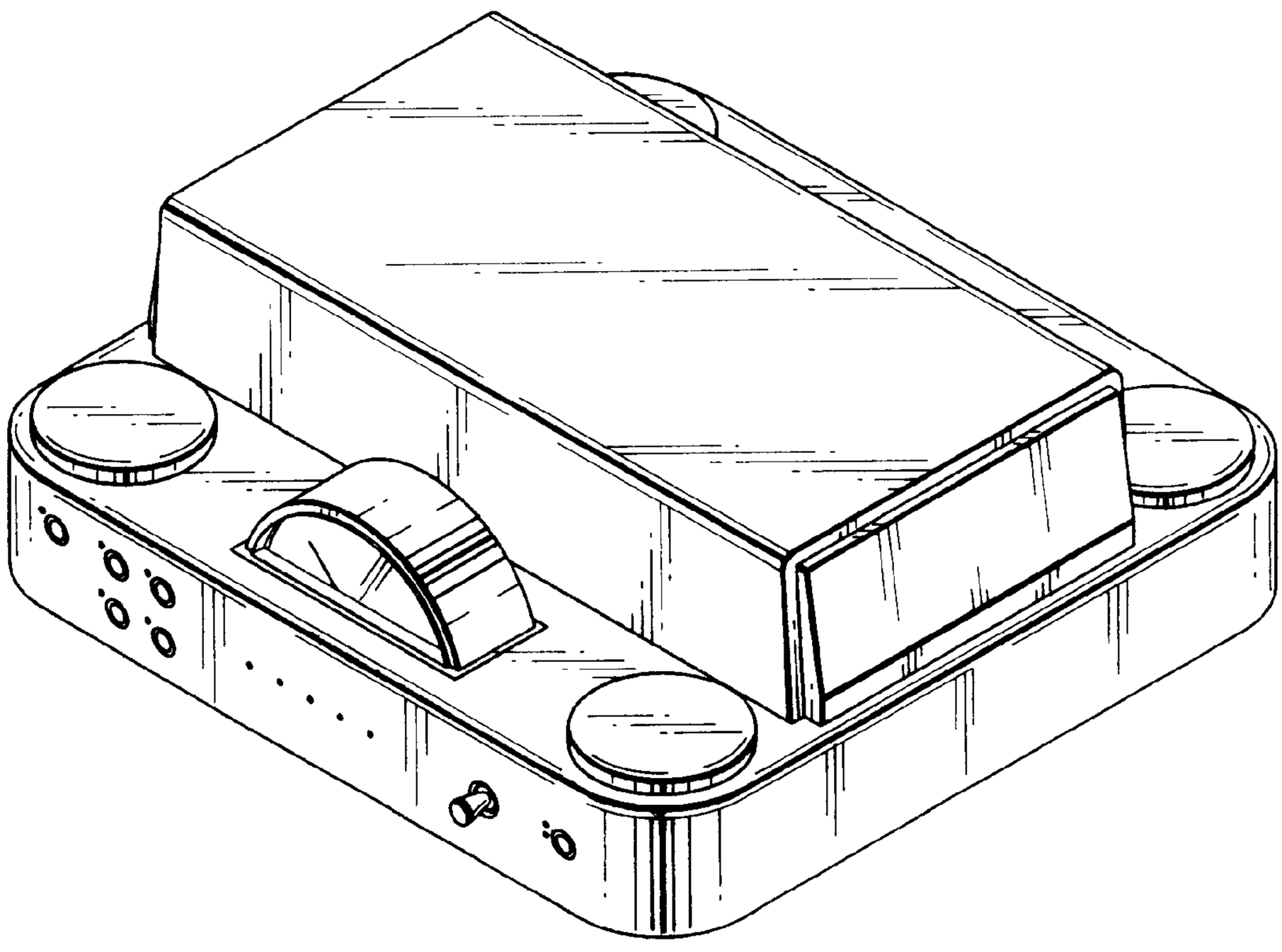

FIG.2

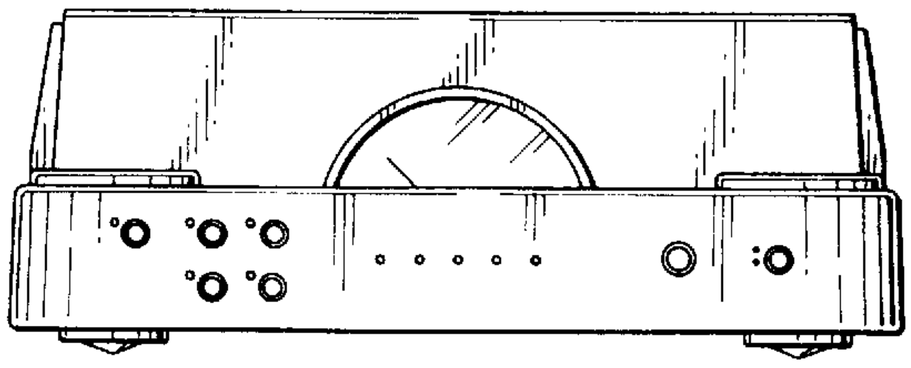

FIG.3

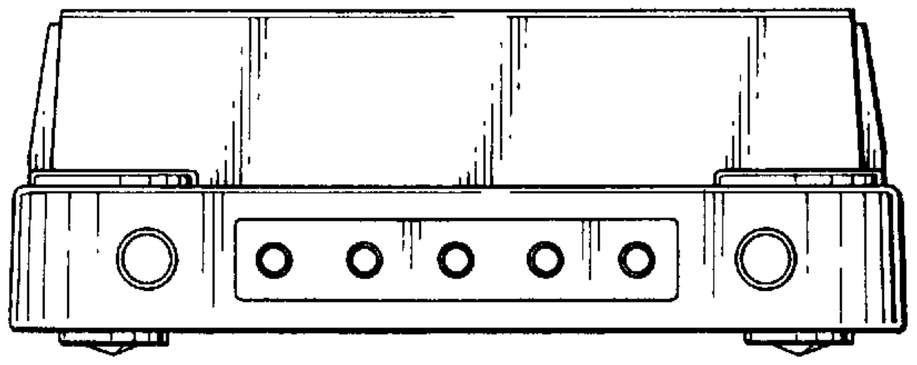

FIG.4

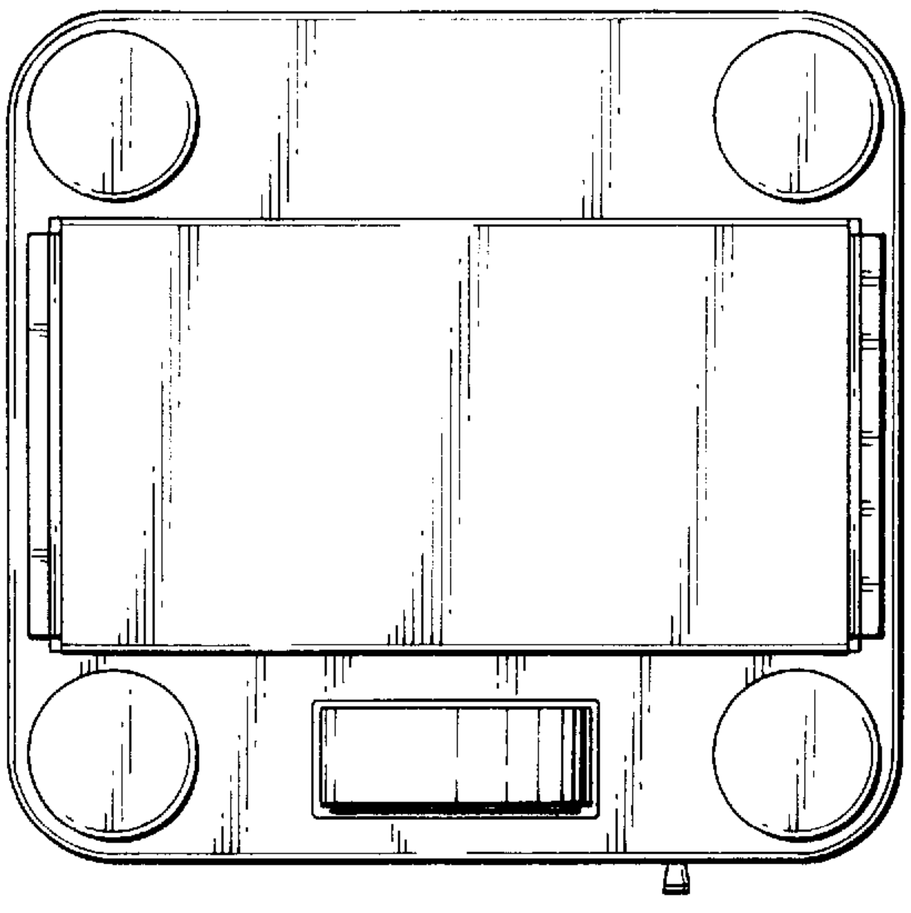

FIG.5

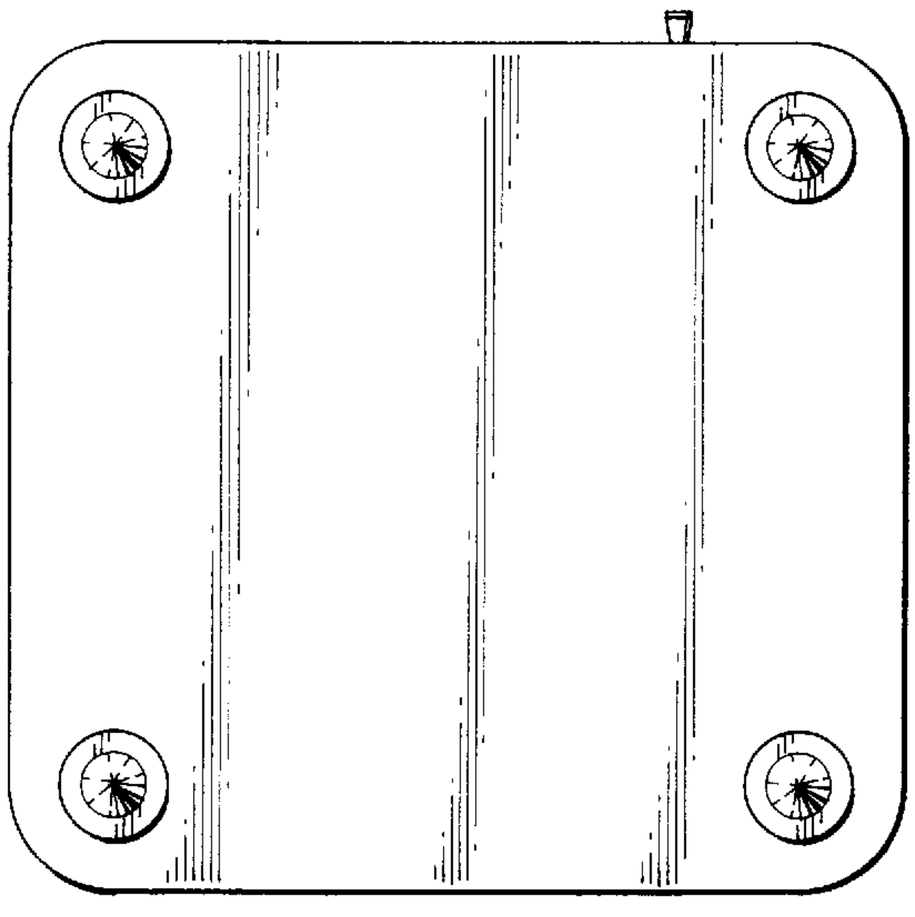

FIG.6