



US00D431790S

**United States Patent** [19]  
**Chiang**

[11] **Patent Number: Des. 431,790**

[45] **Date of Patent: \*\* Oct. 10, 2000**

[54] **CIRCUIT ANALYZER**

D. 396,013 7/1998 Luebke ..... D10/78

[76] Inventor: **Joan-Ang Chiang**, 3274 Ettie St.,  
Oakland, Calif. 94608

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[\*\*] Term: **14 Years**

[57] **CLAIM**

The ornamental design for a circuit analyzer, as shown.

[21] Appl. No.: **29/113,745**

**DESCRIPTION**

[22] Filed: **Nov. 10, 1999**

[51] **LOC (7) Cl.** ..... **10-04**

[52] **U.S. Cl.** ..... **D10/78**

[58] **Field of Search** ..... D10/78; 324/133,  
324/508–511; 361/42–50, 86, 87

FIG. 1 is a perspective view of a circuit analyzer showing  
my new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a top view thereof; and,

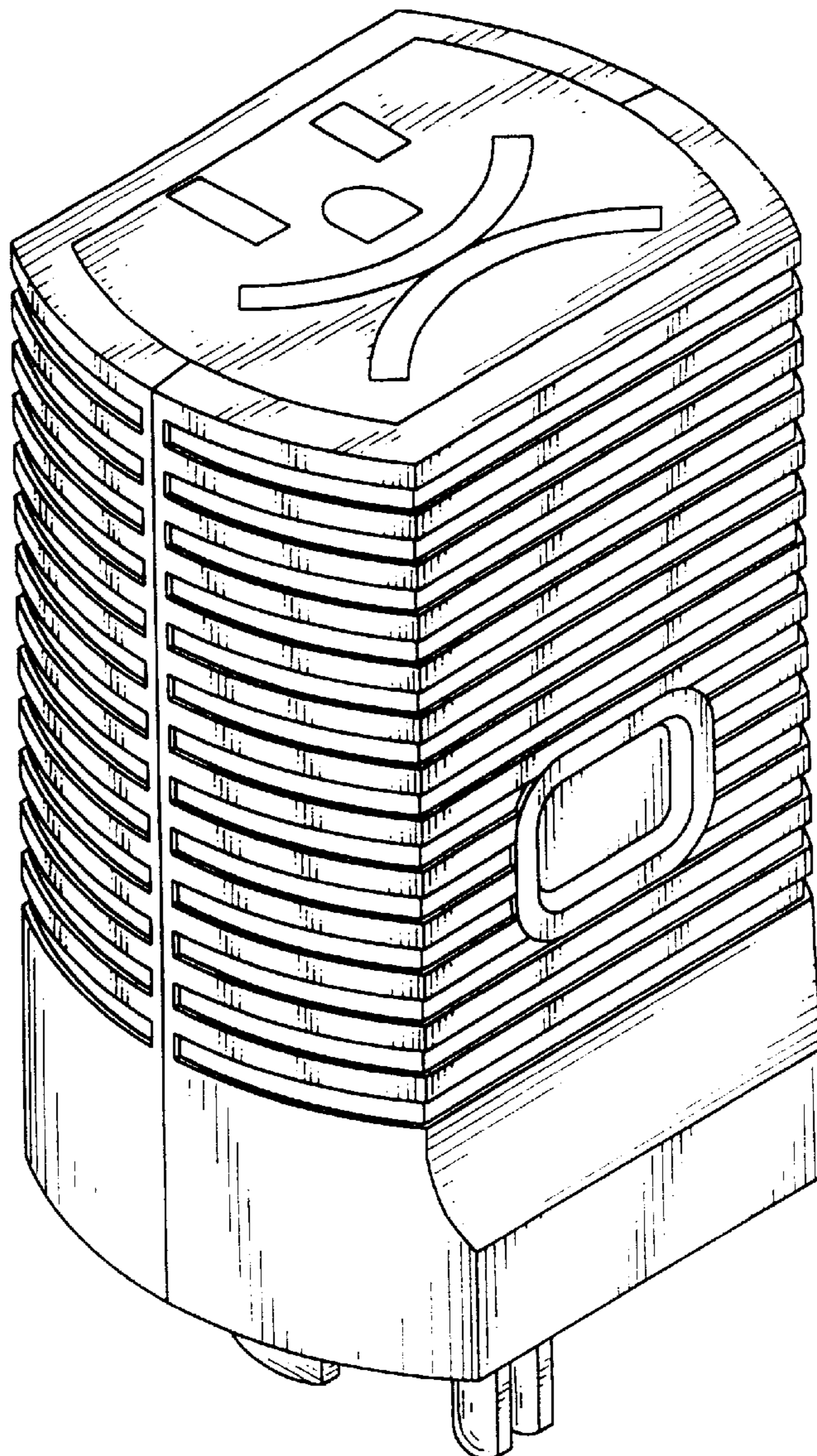
FIG. 7 is a bottom view thereof.

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

D. 300,809 4/1989 Schwartz ..... D10/78

**1 Claim, 4 Drawing Sheets**



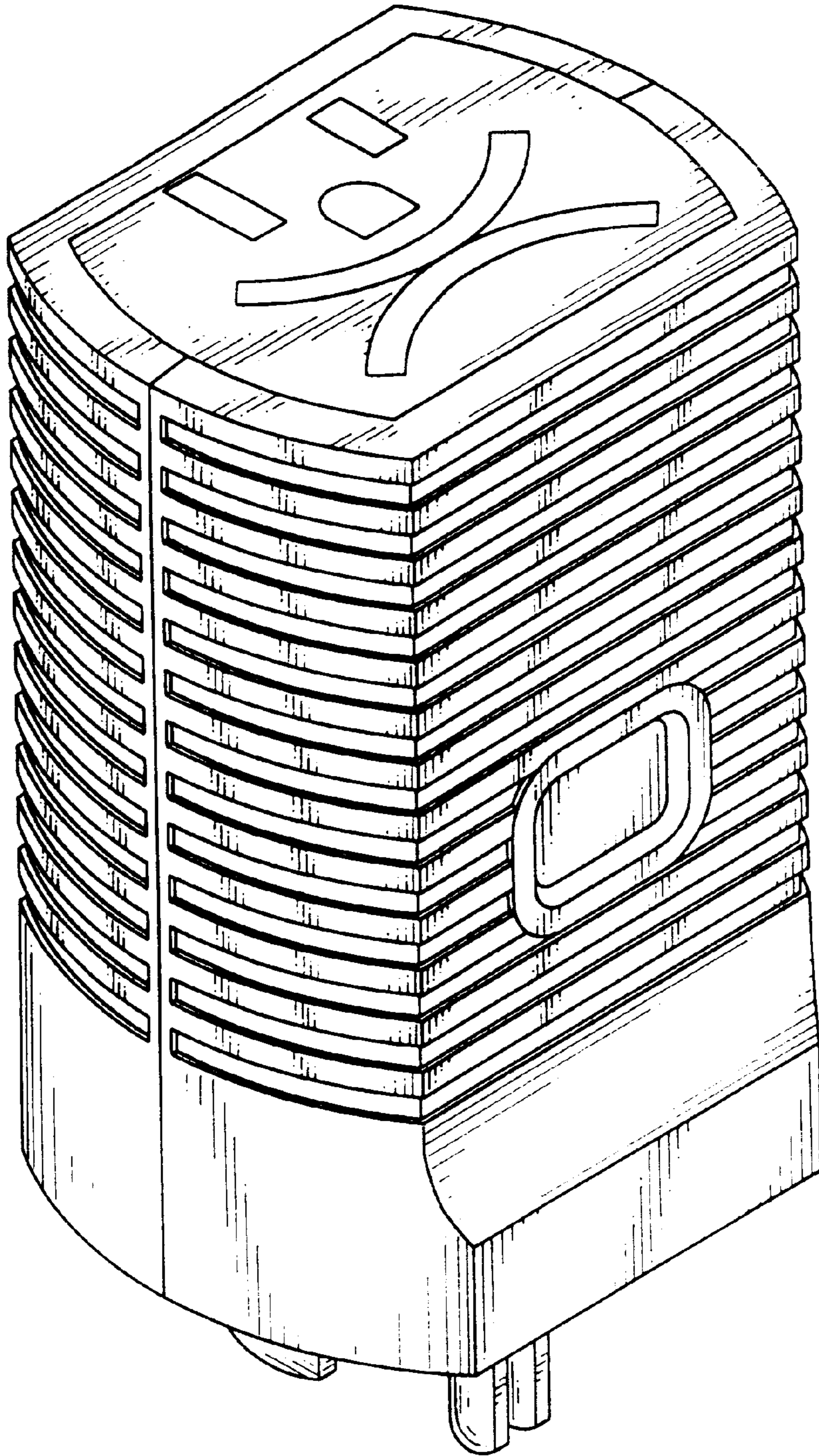


FIG. 1

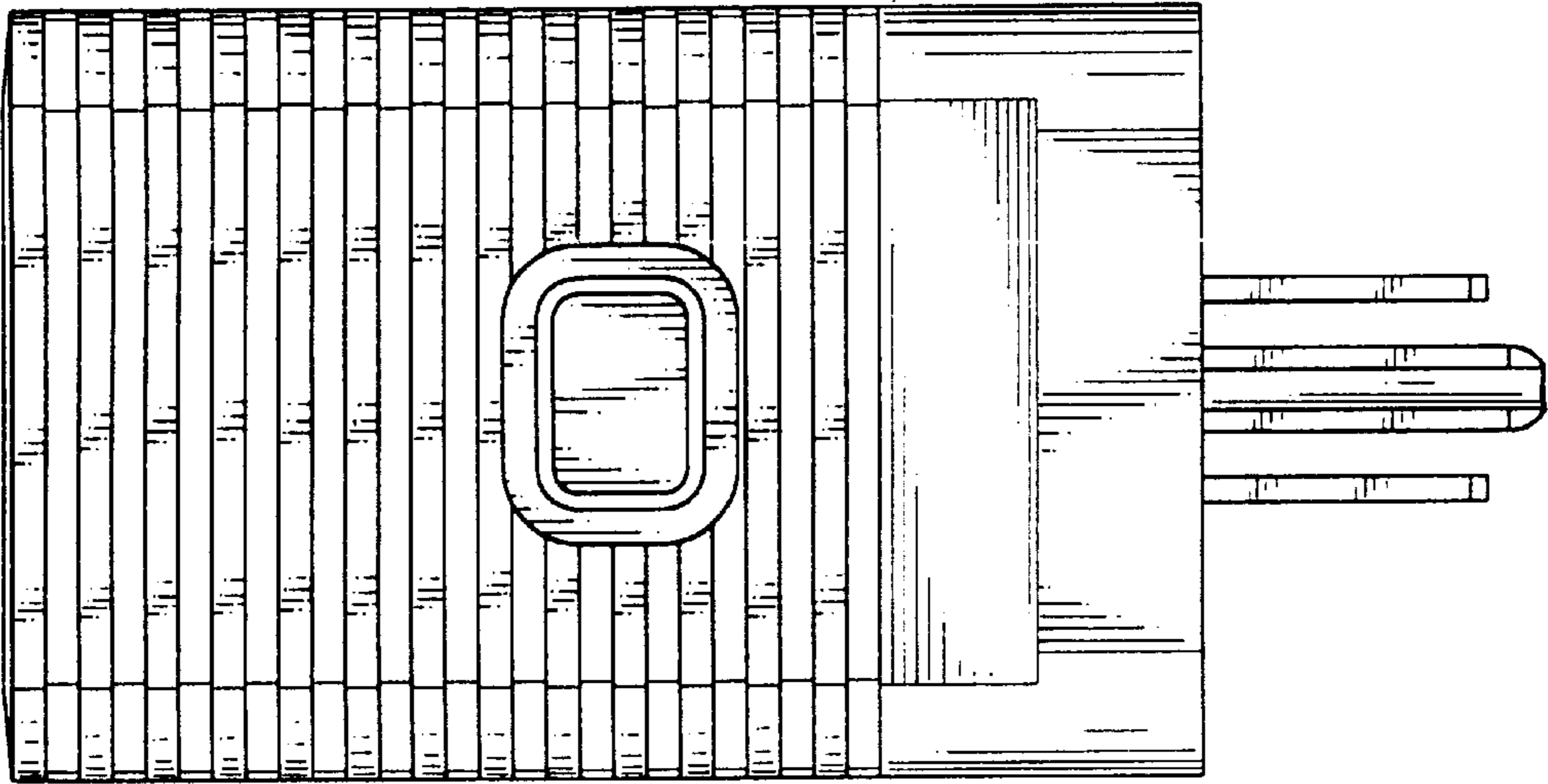


FIG. 2

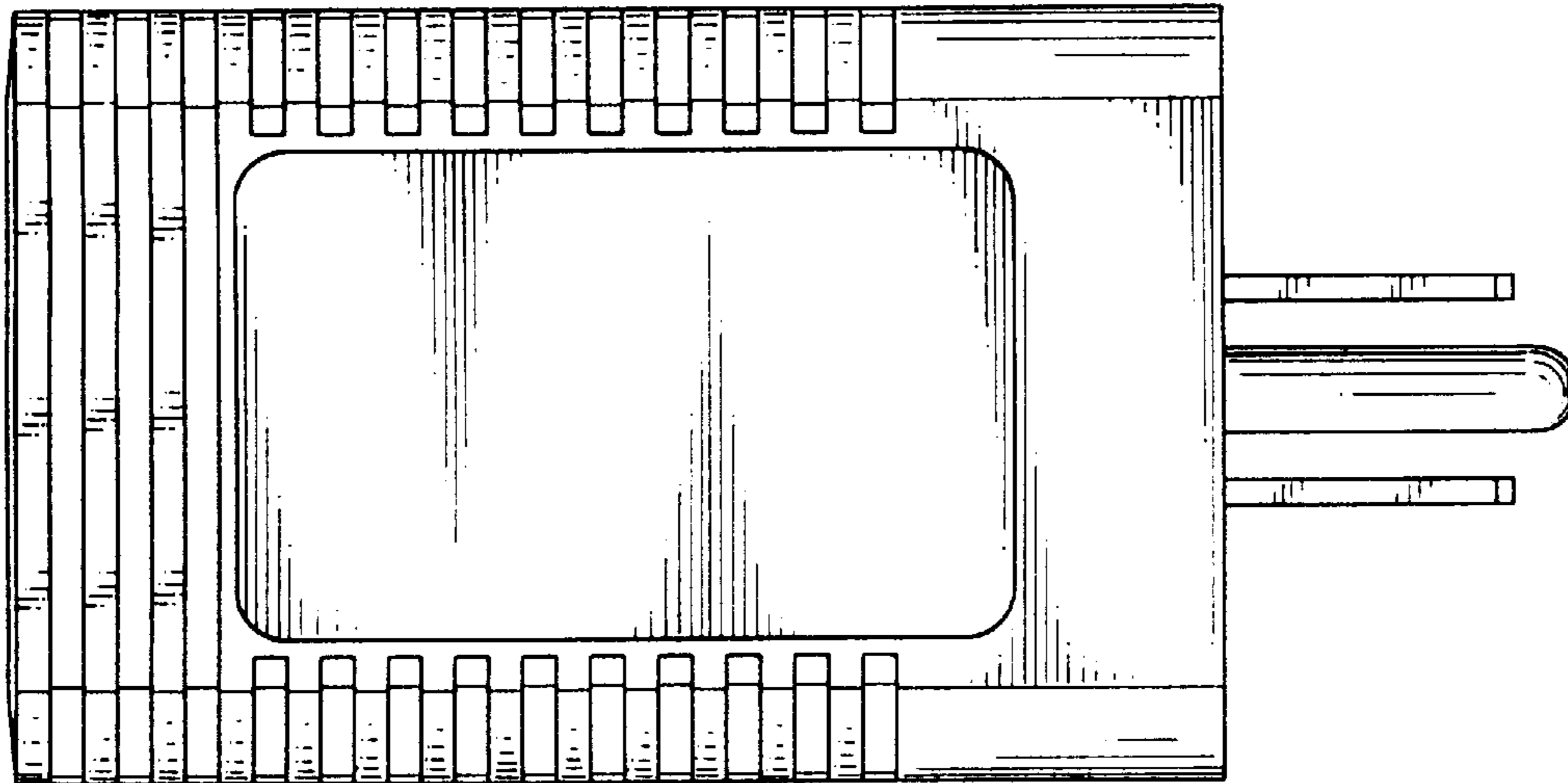


FIG. 3

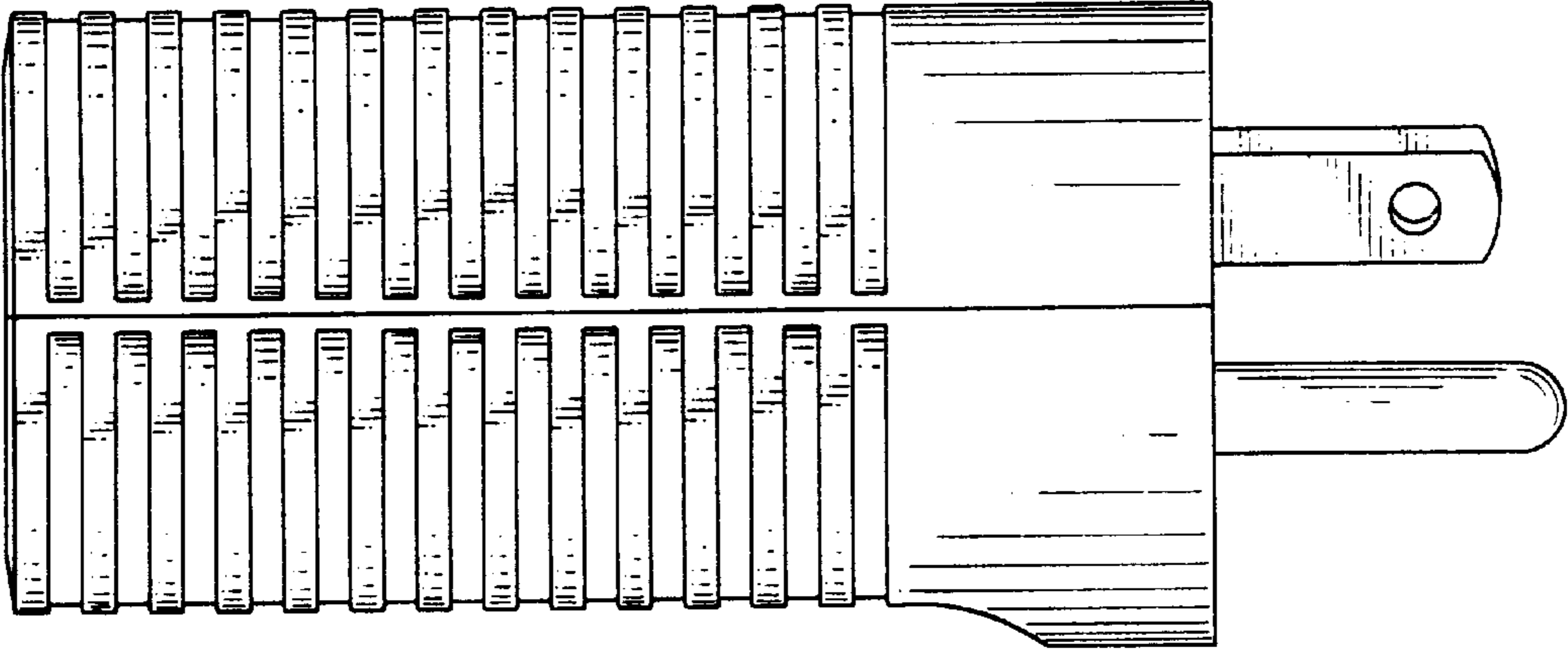


FIG. 5

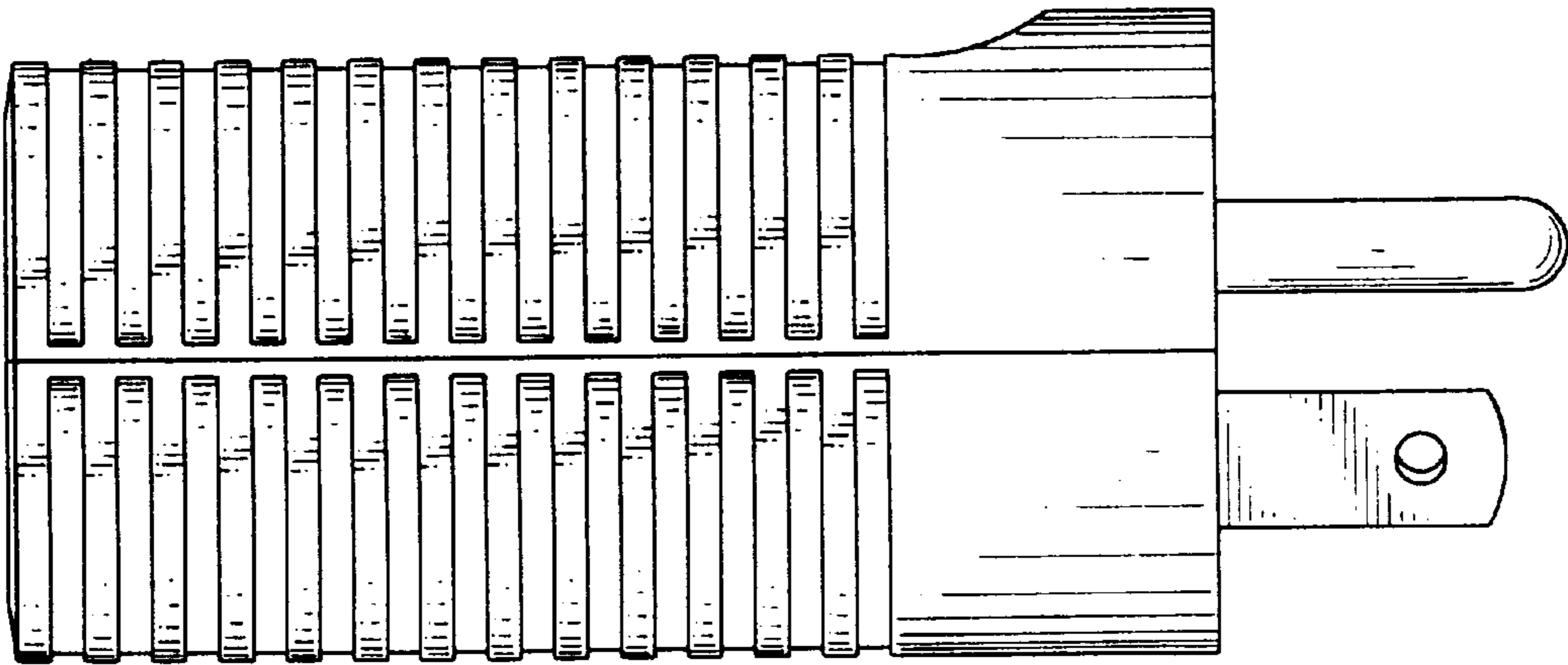


FIG. 4

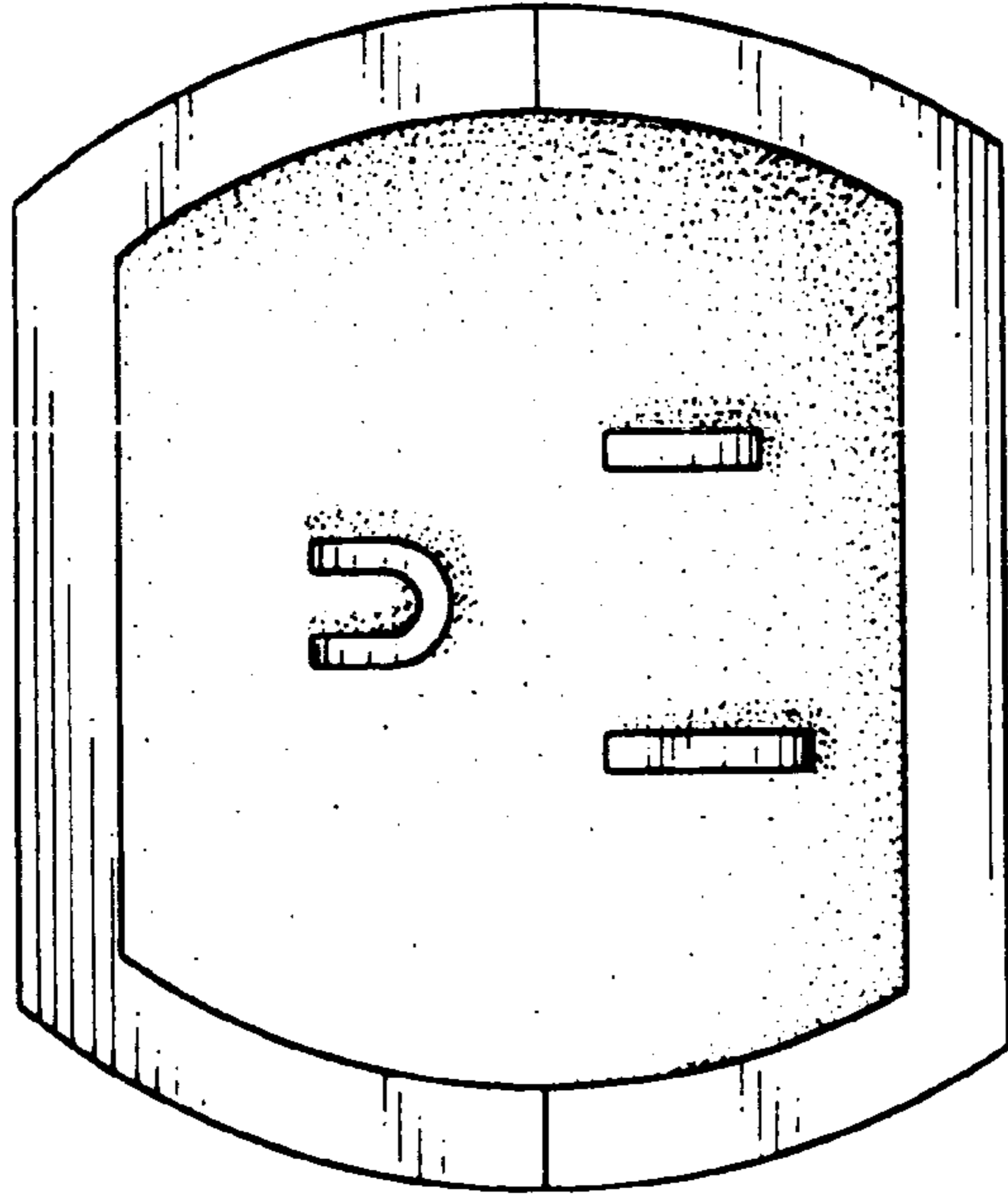


FIG. 6

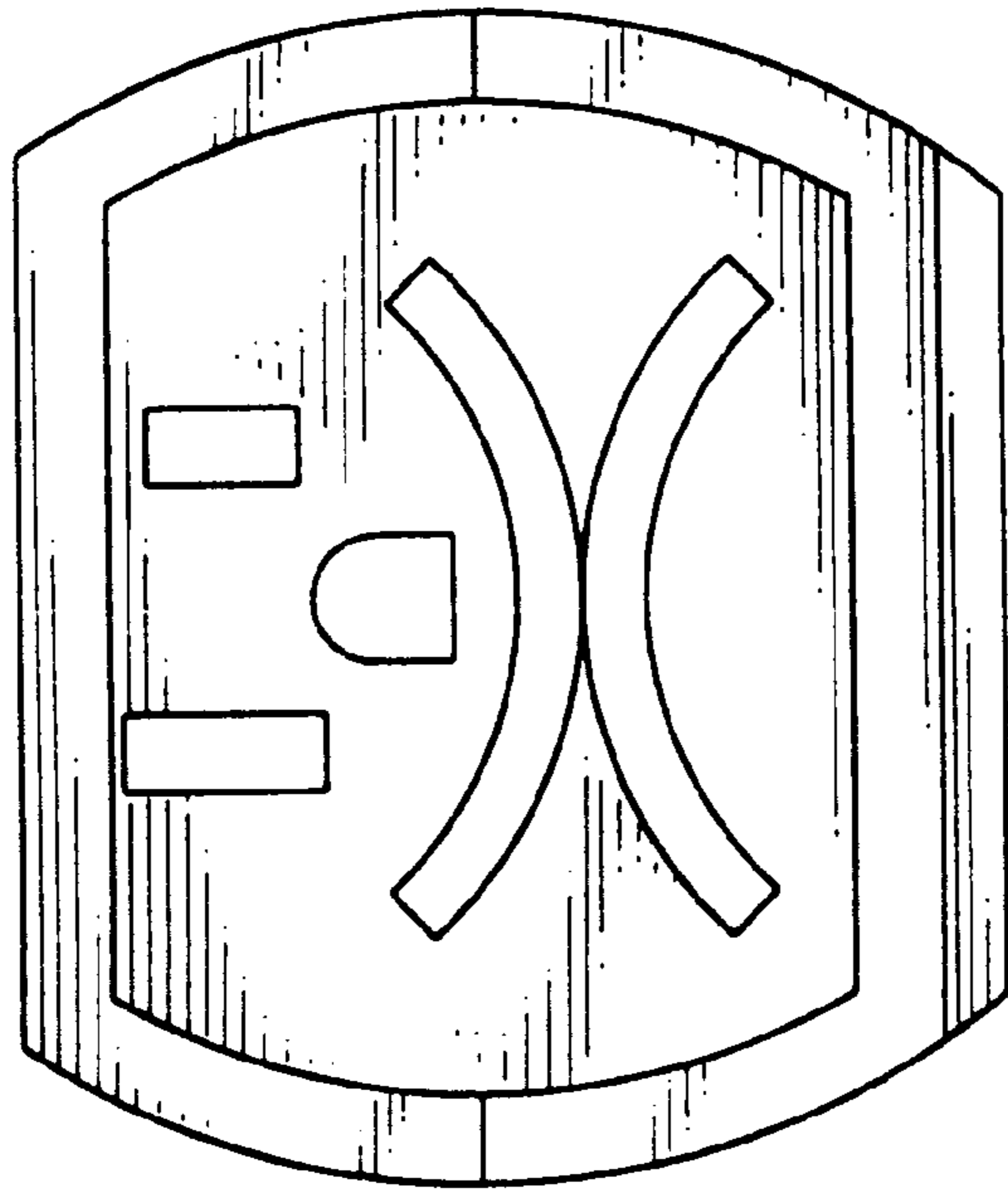


FIG. 7

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : Des. 431,790  
DATED : October 10, 2000  
INVENTOR(S) : Chiang

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

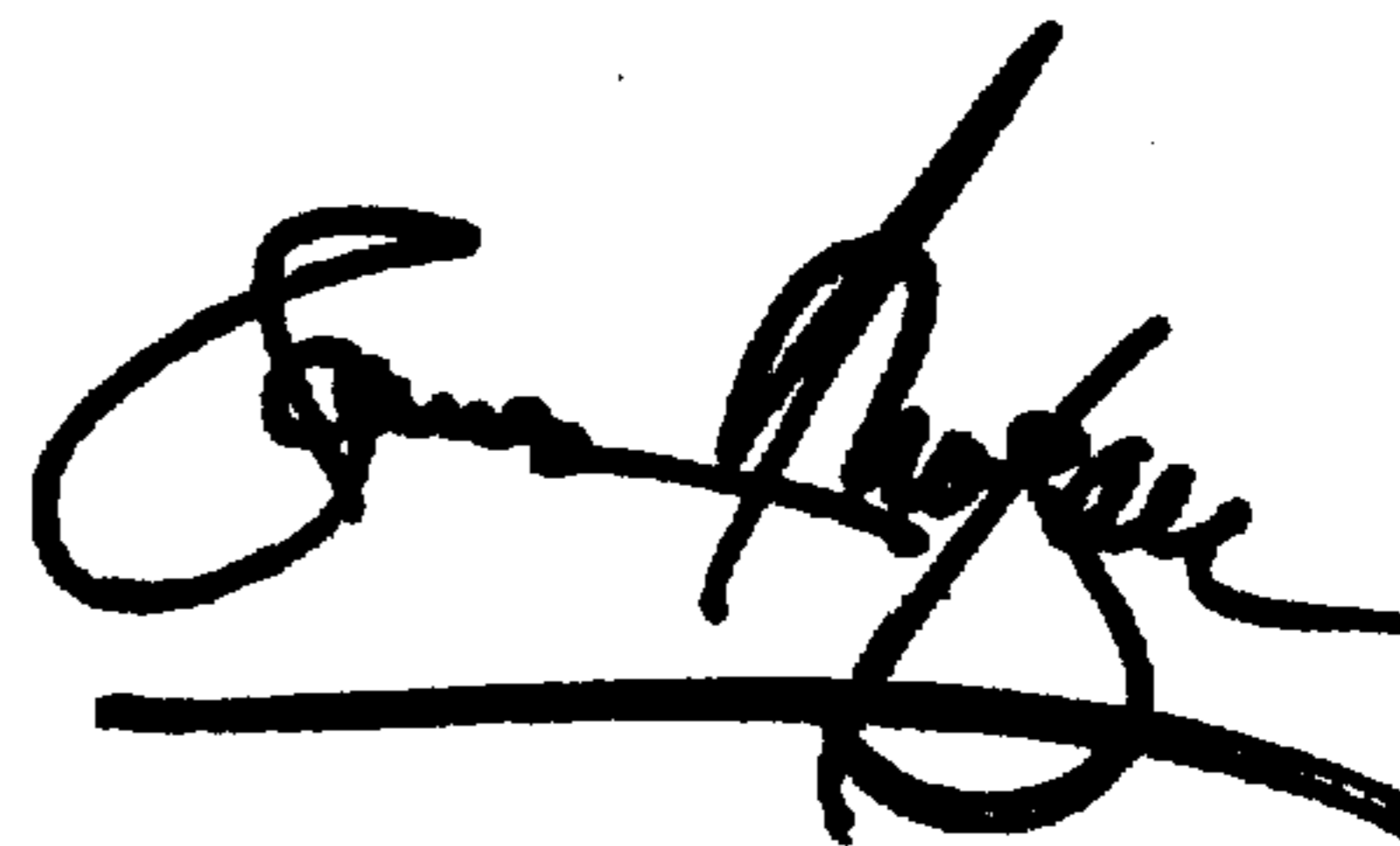
Title page,

Inventor's name should be:  
John-Ang Chiang

Signed and Sealed this

Eighteenth Day of December, 2001

Attest:



Attesting Officer

JAMES E. ROGAN  
Director of the United States Patent and Trademark Office