



US00D431580S

United States Patent [19]

[11] Patent Number: Des. 431,580

Ito et al.

[45] Date of Patent: ** Oct. 3, 2000

[54] IC TRAY HOLDER

[75] Inventors: Akihiko Ito; Yoshihito Kobayashi, both of Tokyo, Japan

[73] Assignee: Advantest Corporation, Tokyo, Japan

[**] Term: 14 Years

[21] Appl. No.: 29/101,478

[22] Filed: Mar. 4, 1999

[30] Foreign Application Priority Data

Oct. 1, 1998	[JP]	Japan	10-28227
Oct. 1, 1998	[JP]	Japan	10-28228
Oct. 1, 1998	[JP]	Japan	10-28229
Oct. 1, 1998	[JP]	Japan	10-28268

[51] LOC (7) Cl. 15-09

[52] U.S. Cl. D15/140

[58] Field of Search D15/140; 324/158.1, 324/755; 439/61, 65, 73, 331

[56] References Cited

U.S. PATENT DOCUMENTS

5,978,218	11/1999	Fujimoto et al.	324/158.1
5,994,894	11/1999	Fujita	324/158.1
6,018,248	11/1999	Kelley et al.	324/158.1

Primary Examiner—Antoine Duval Davis
Attorney, Agent, or Firm—Birch, Stewart, Kolasch & Birch, LLP

[57] CLAIM

The ornamental design for an IC tray holder, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of an IC tray holder according to a first embodiment of the present design.

FIG. 2 is a front view of the IC tray holder shown in FIG. 1. A rear view of the IC tray holder is omitted from the drawing since the rear view is symmetrical with the front view.

FIG. 3 is a bottom plan view of the IC tray holder shown in FIG. 1.

FIG. 4 is a left side view of the IC tray holder shown in FIG. 1.

FIG. 5 is a right side view of the IC tray holder shown in FIG. 1.

FIG. 6 is a perspective view of the IC tray holder shown in FIG. 1.

FIG. 7 is a perspective view of the IC tray holder shown in FIG. 1, for illustrating the use of the IC tray holder.

FIG. 8 is a top plan view of an IC tray holder according to a second embodiment of the present design.

FIG. 9 is a front view of the IC tray holder shown in FIG. 8. A rear view of the IC tray holder is omitted from the drawing since the rear view is symmetrical with the front view.

FIG. 10 is a bottom plan view of the IC tray holder shown in FIG. 8.

FIG. 11 is a right side view of the IC tray holder shown in FIG. 8. A left side view of the IC tray holder is omitted from the drawing since the left side view is symmetrical with the right side view.

FIG. 12 is a perspective view of the IC tray holder shown in FIG. 8.

FIG. 13 is a perspective view of the IC tray holder shown in FIG. 8, for illustrating the use of the IC tray holder.

FIG. 14 is a top plan view of an IC tray holder according to a third embodiment of the present design.

FIG. 15 is a front view of the IC tray holder shown in FIG. 14. A rear view of the IC tray holder is omitted from the drawing since the rear view is symmetrical with the front view.

FIG. 16 is a bottom plan view of the IC tray holder shown in FIG. 14.

FIG. 17 is a left side view of the IC tray holder shown in FIG. 14.

FIG. 18 is a right side view of the IC tray holder shown in FIG. 14.

FIG. 19 is a perspective view of the IC tray holder shown in FIG. 14.

FIG. 20 is a perspective view of the IC tray holder shown in FIG. 14, for illustrating the use of the IC tray holder.

FIG. 21 is a top plan view of an IC tray holder according to a fourth embodiment of the present design.

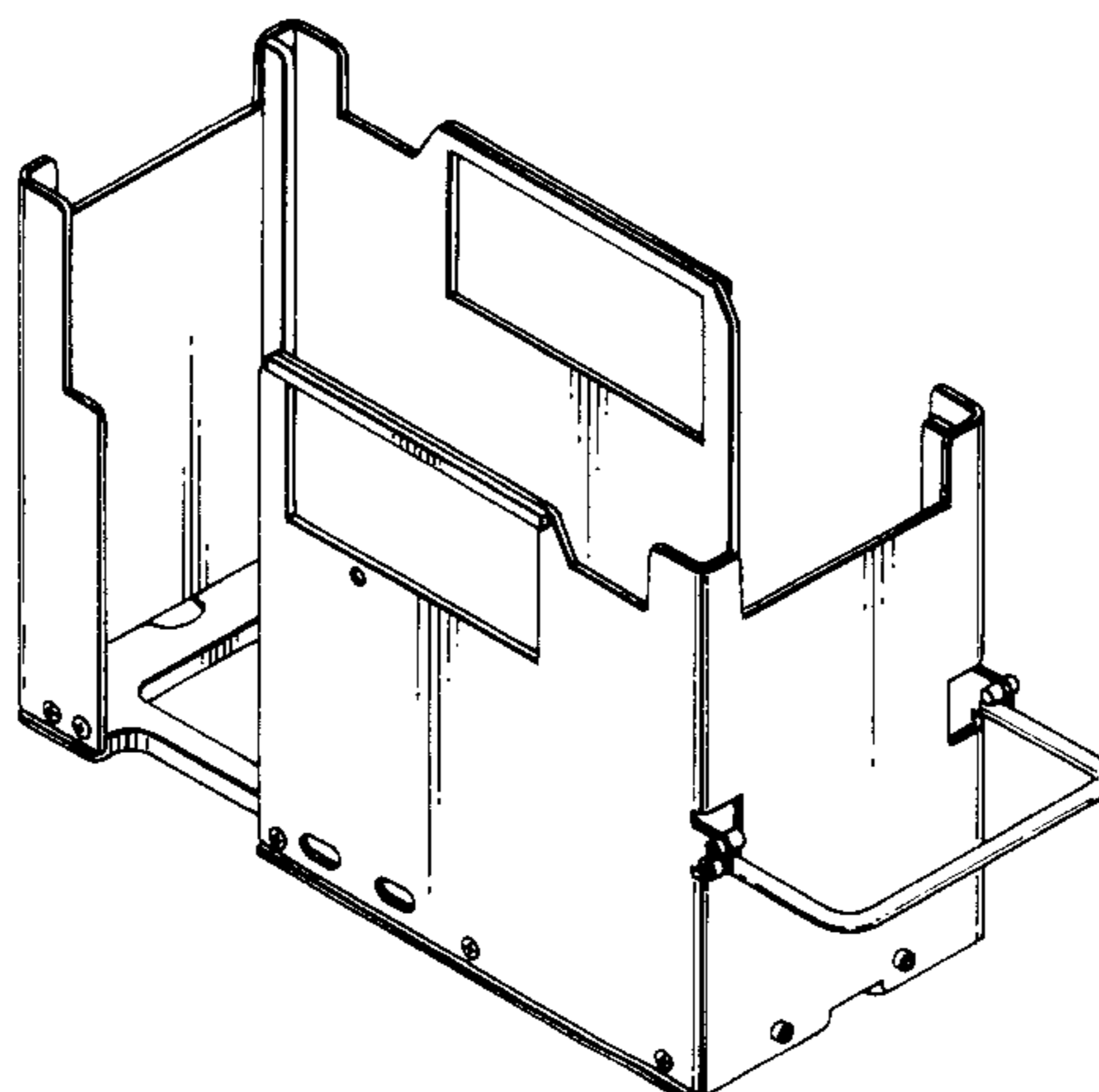


FIG. 22 is a front view of the IC tray holder shown in FIG. 21. A rear view of the IC tray holder is omitted from the drawing since the rear view is symmetrical with the front view.

FIG. 23 is a bottom plan view of the IC tray holder shown in FIG. 21.

FIG. 24 is a left side view of the IC tray holder shown in FIG. 21.

FIG. 25 is a right side view of the IC tray holder shown in FIG. 21.

FIG. 26 is a perspective view of the IC tray holder shown in FIG. 21; and,

FIG. 27 is a perspective view of the IC tray holder shown in FIG. 21, for illustrating the use of the IC tray holder.

The present design is embodied in an IC tray holder for putting IC-trays into as shown in FIGS. 7, 13, 20 and 27. The IC-trays loaded with IC devices respectively are piled up and they are put into the IC tray holder.

The IC tray holder is provided for an IC testing and handling apparatus which handles IC devices and judges the quality of each IC device.

The broken line showing of an IC-tray is for illustrative purposes only and forms no part of the claimed design.

1 Claim, 12 Drawing Sheets

FIG. 1

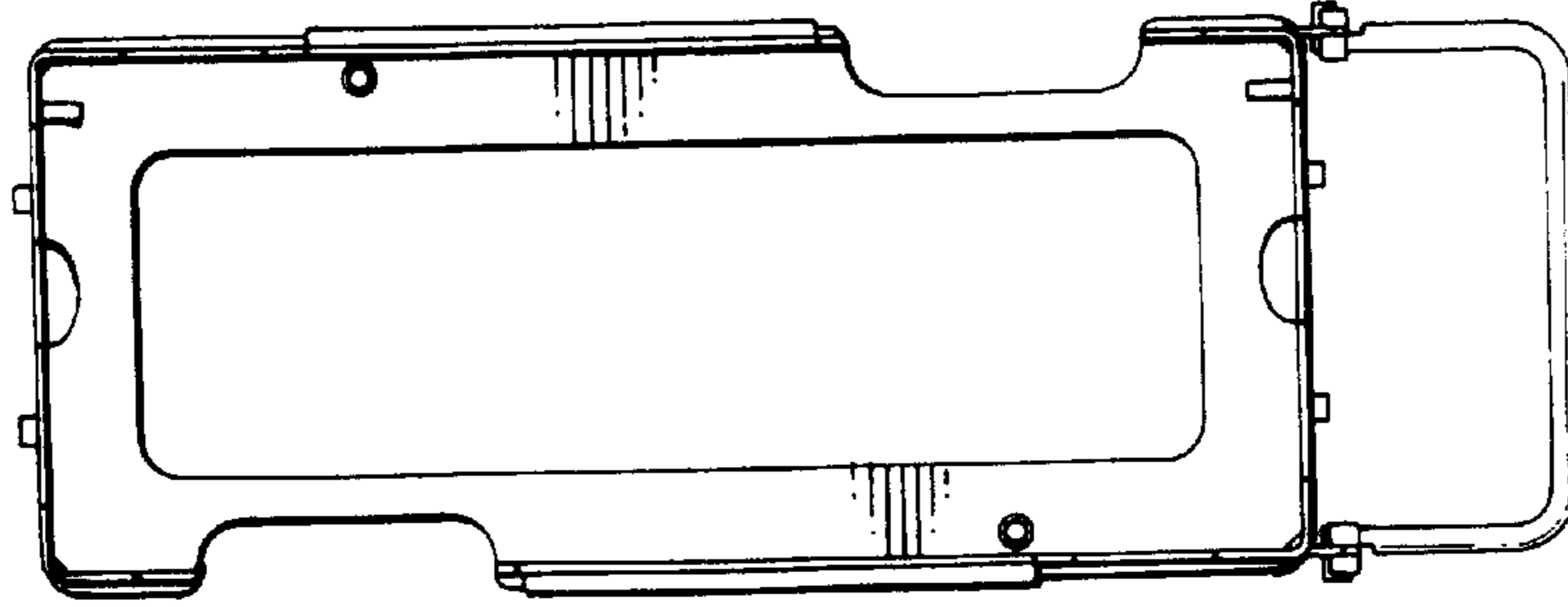


FIG. 2

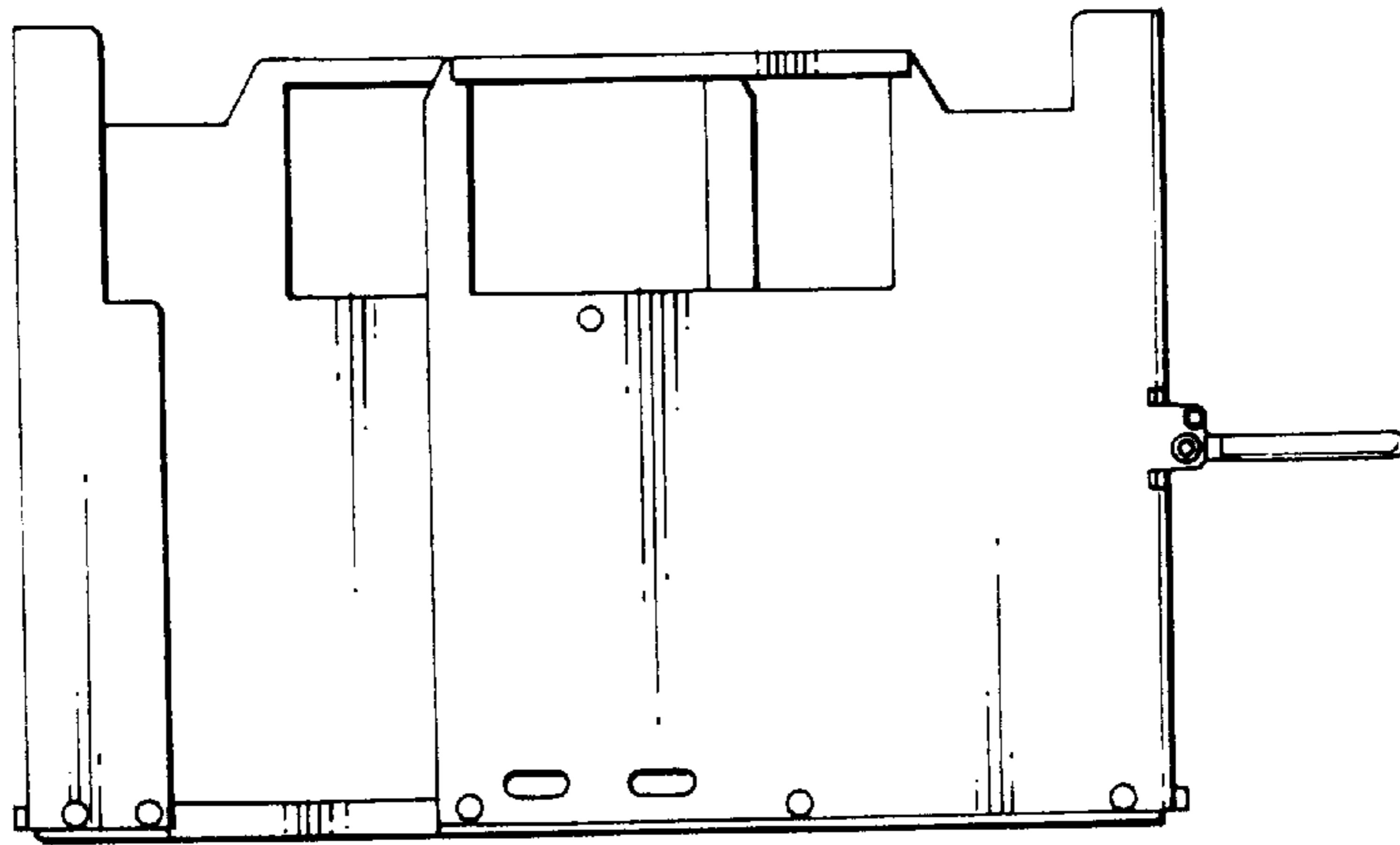


FIG. 3

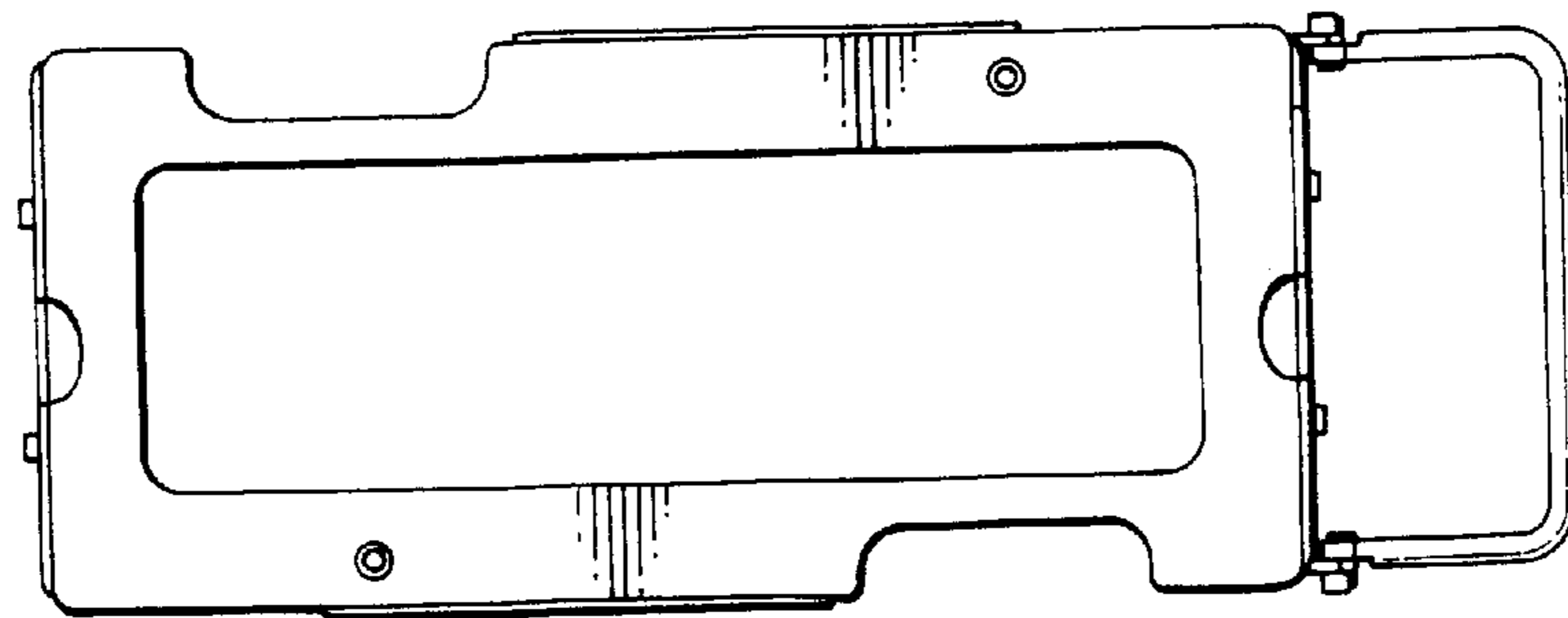


FIG. 4

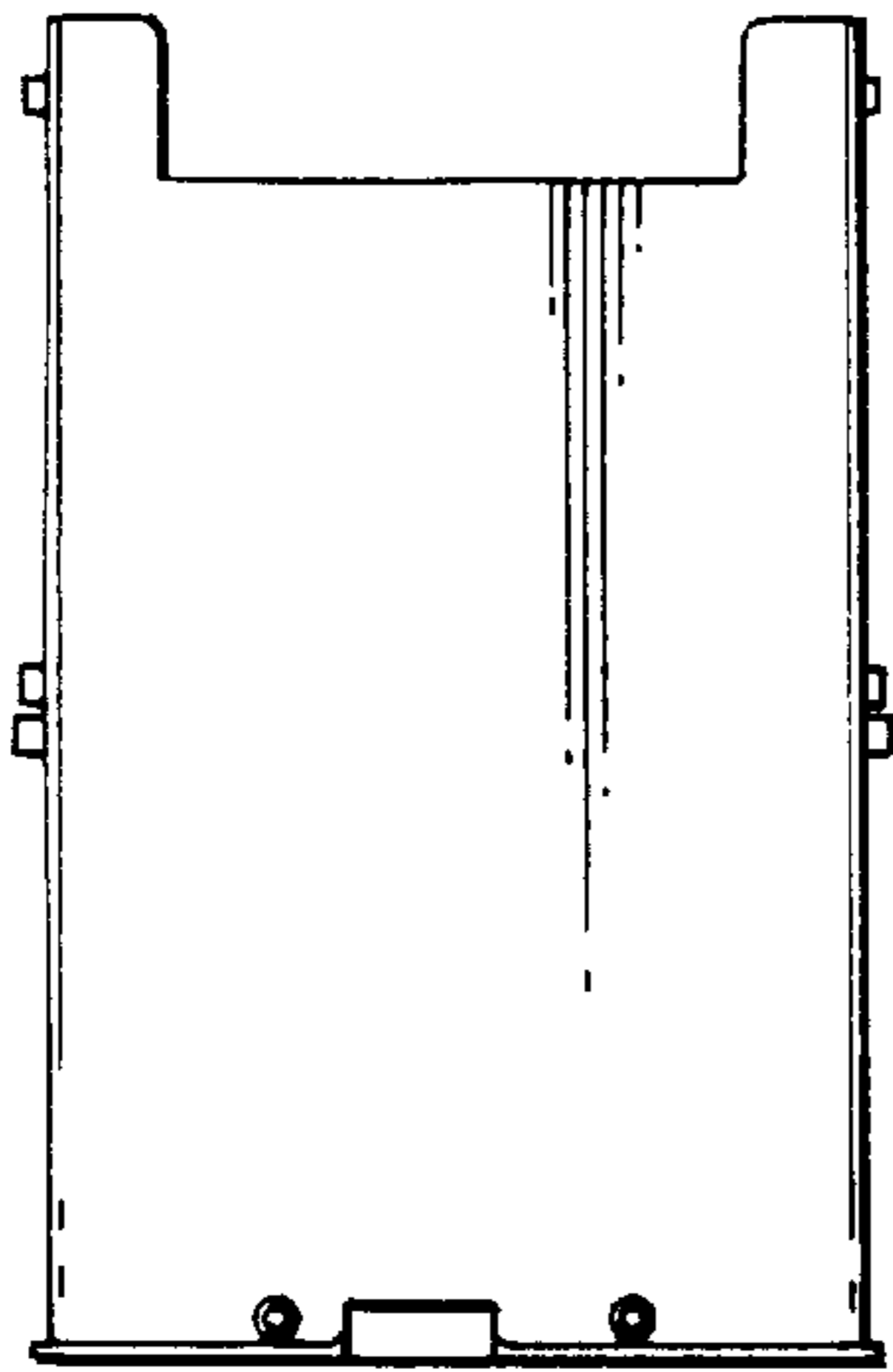


FIG. 5

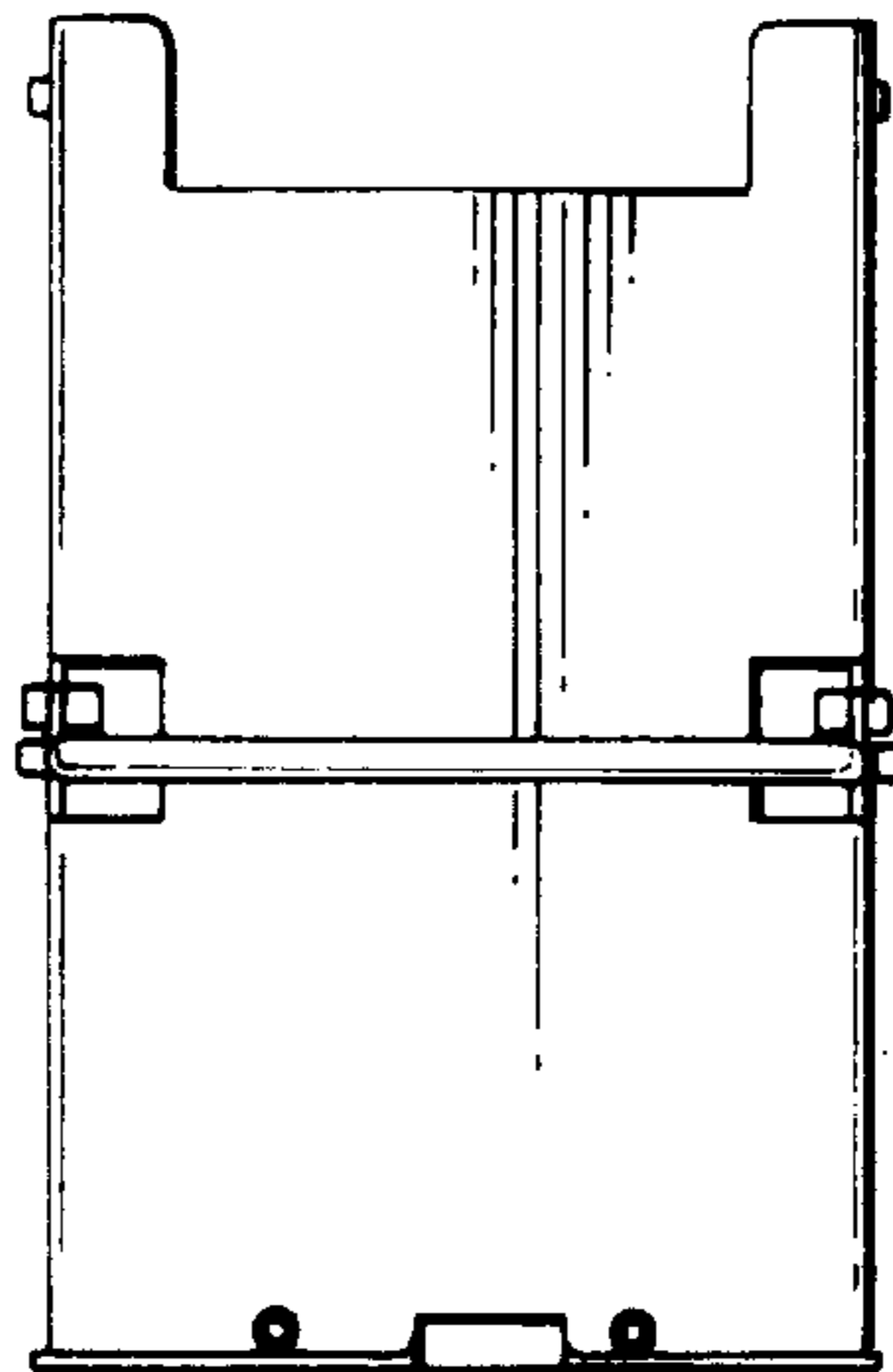


FIG. 6

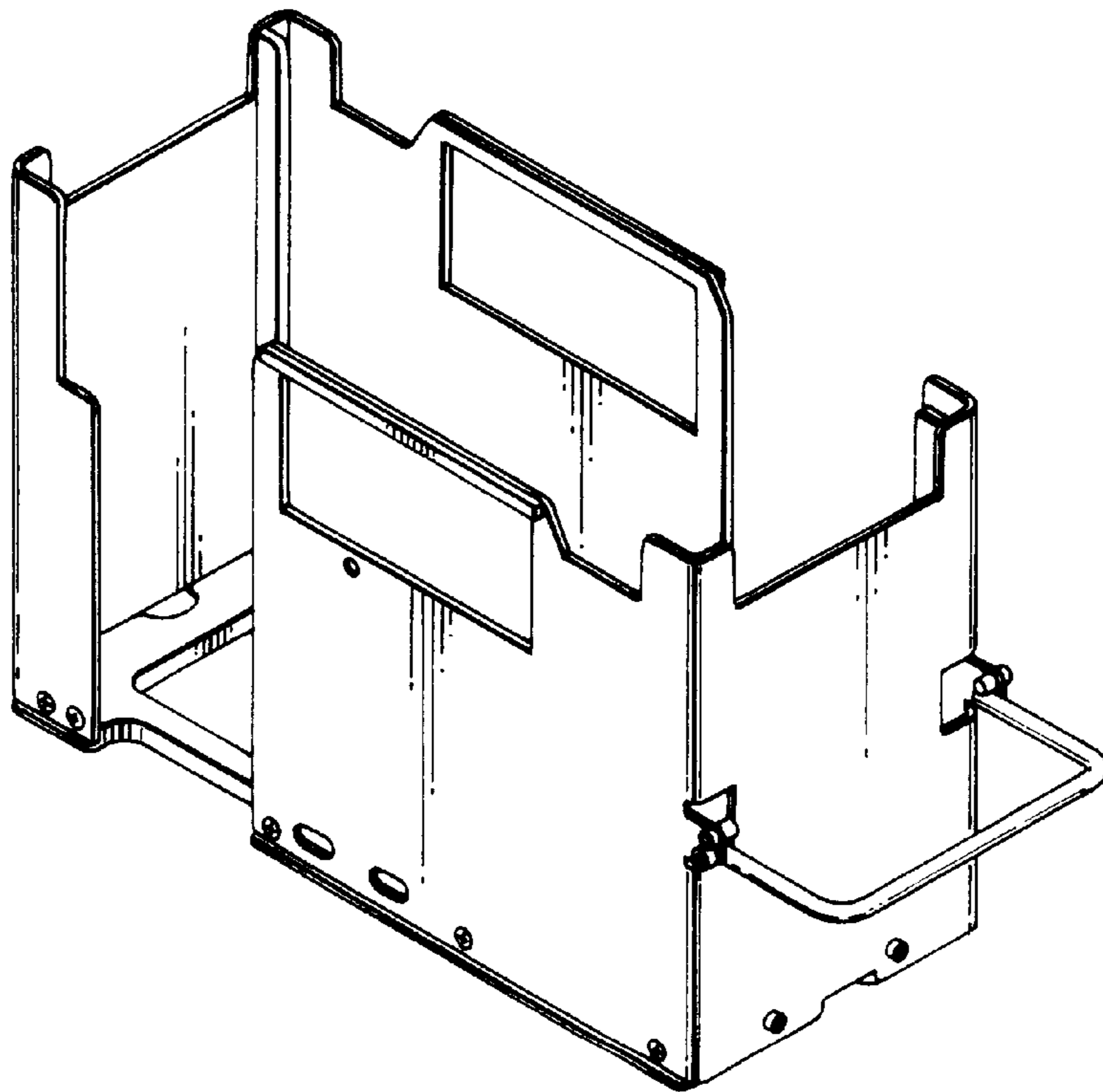


FIG. 7

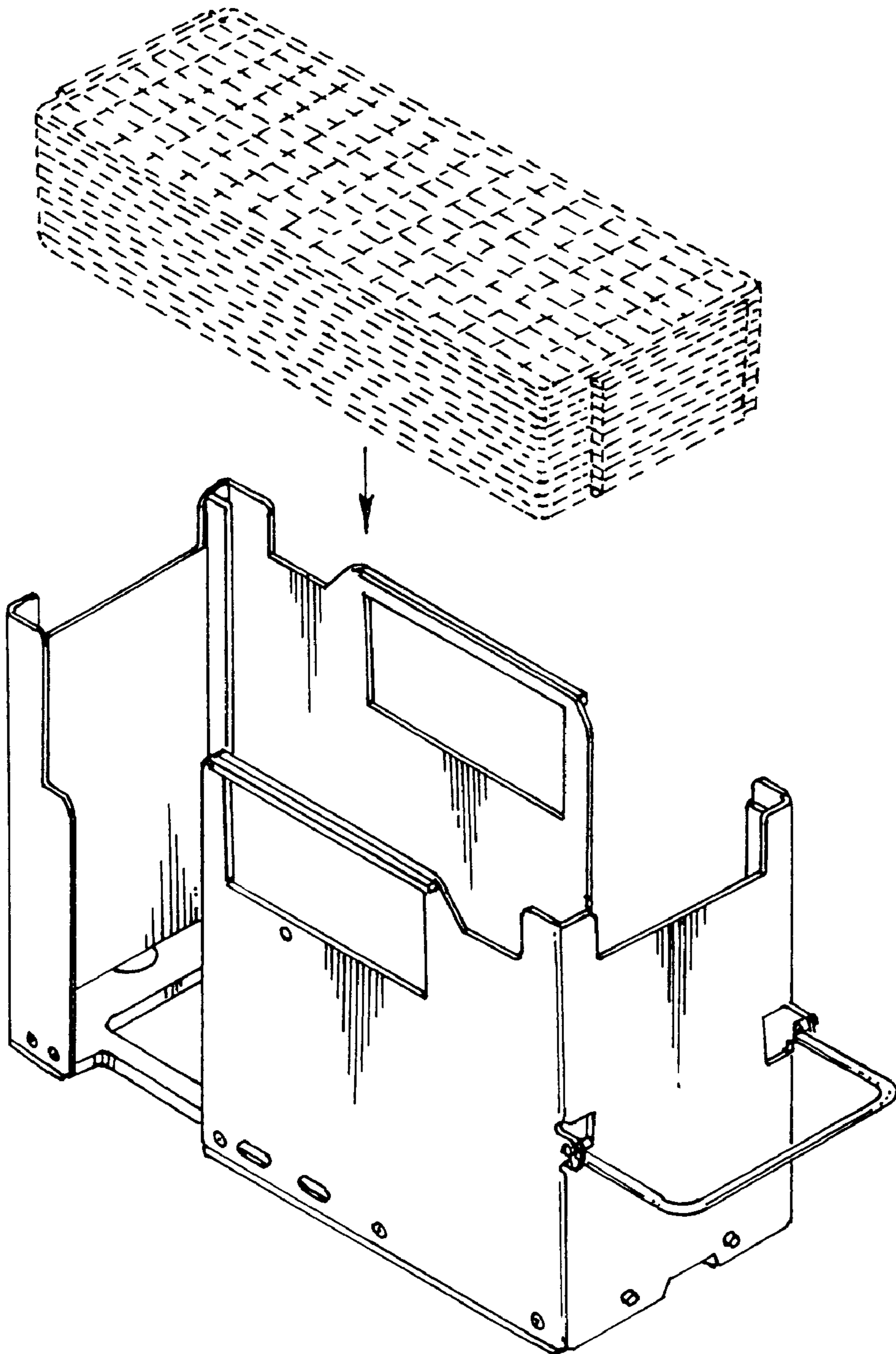


FIG. 8

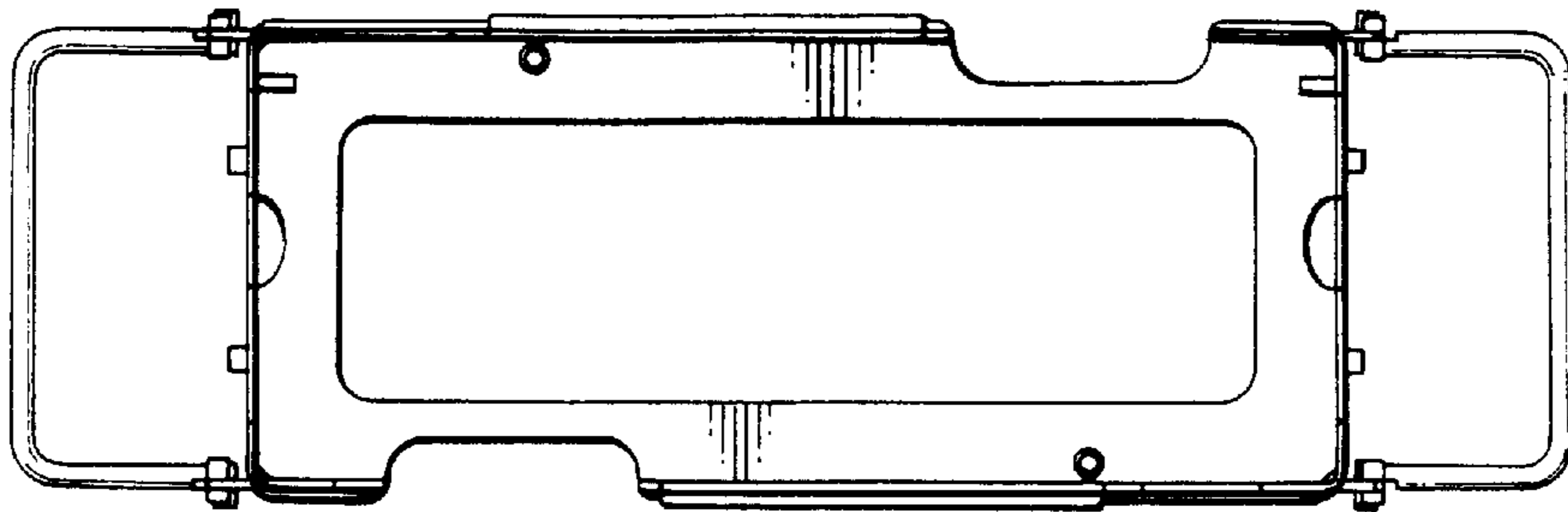


FIG. 9

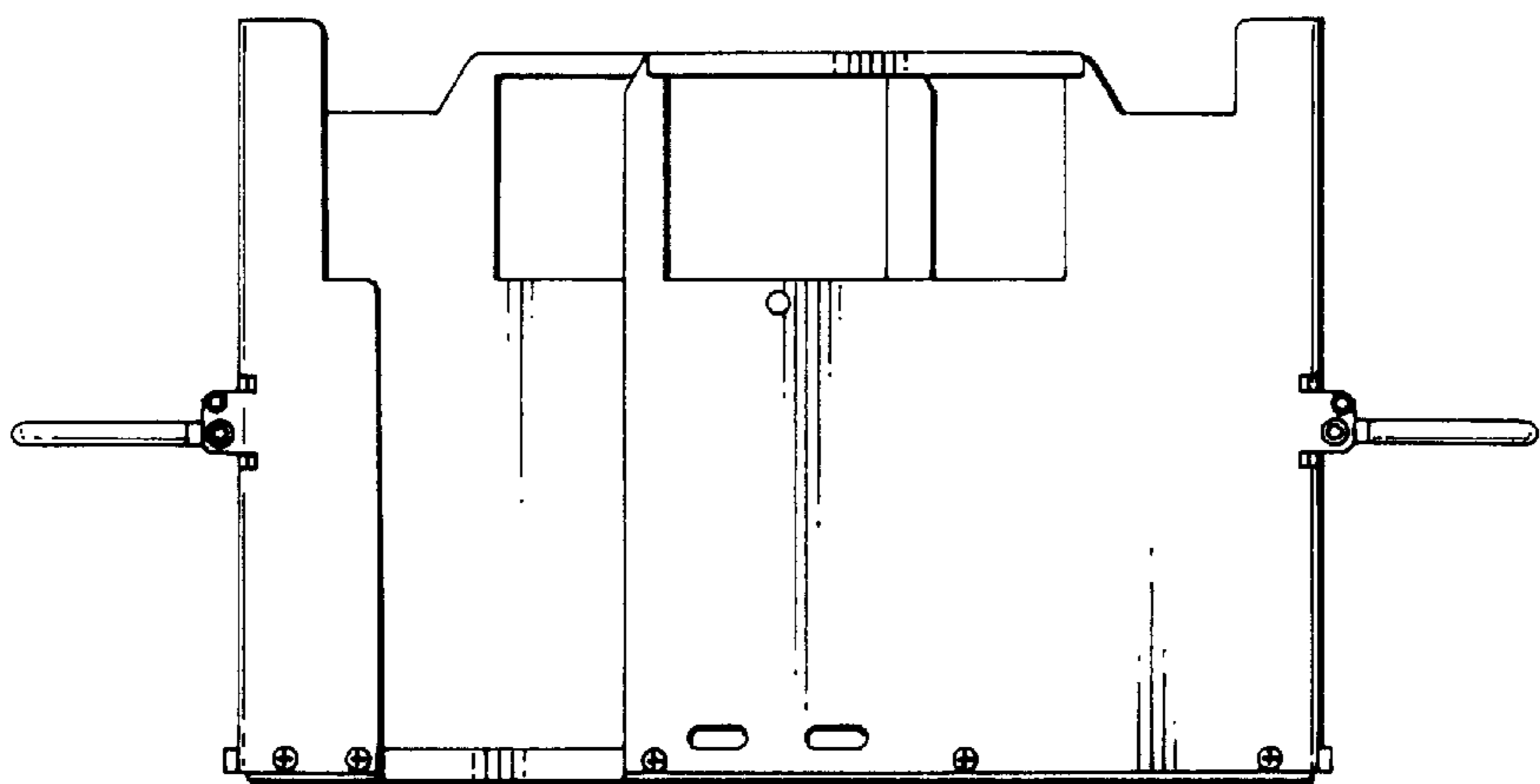


FIG. 10

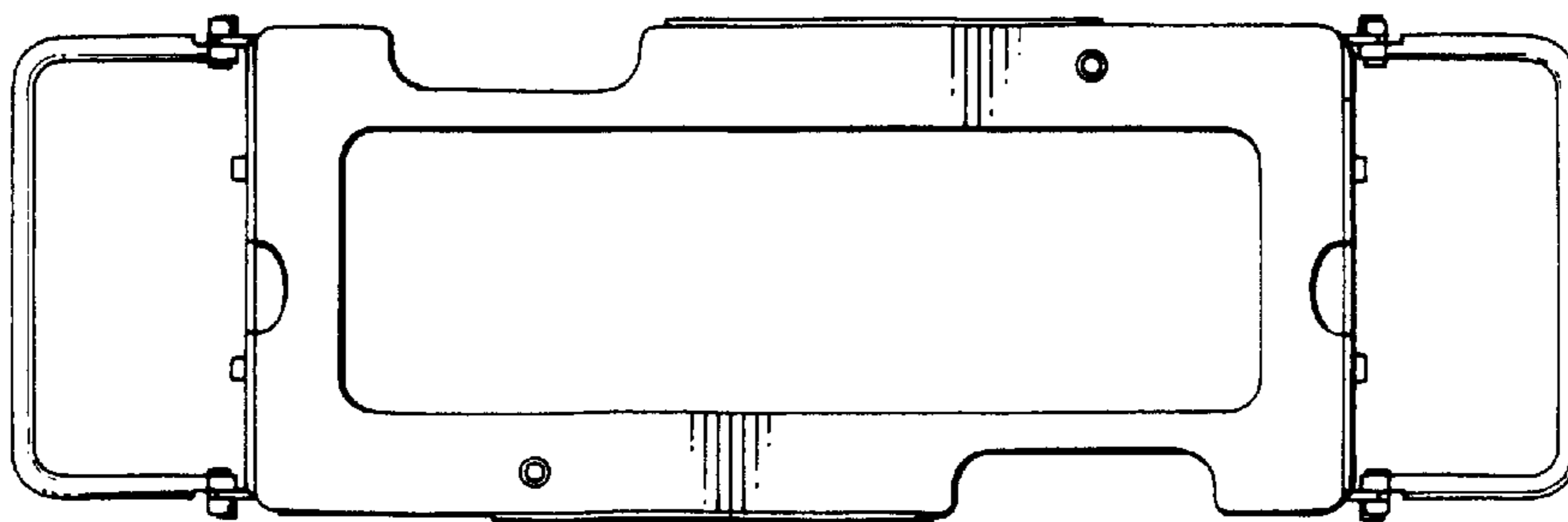


FIG. 11

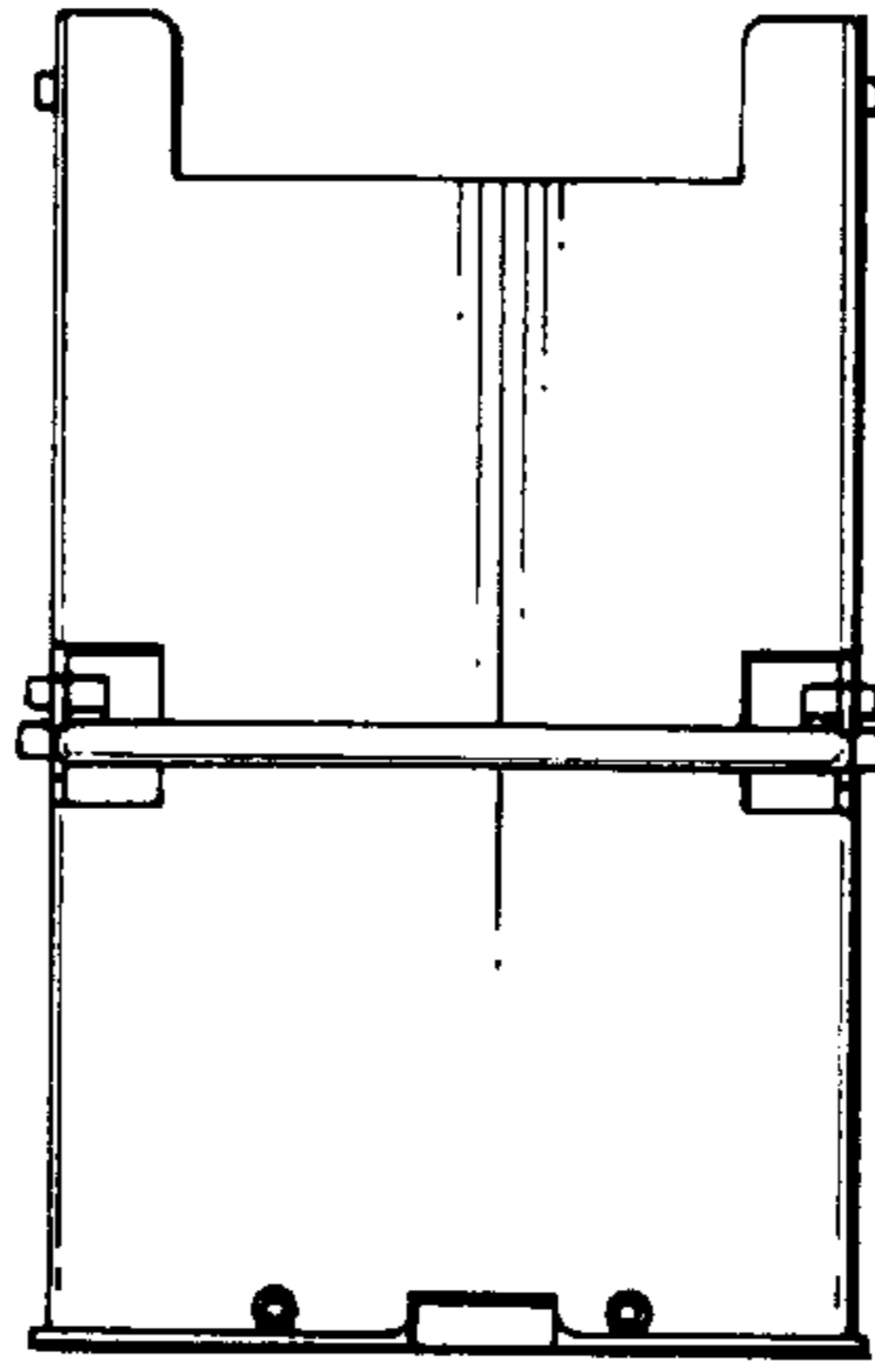


FIG. 12

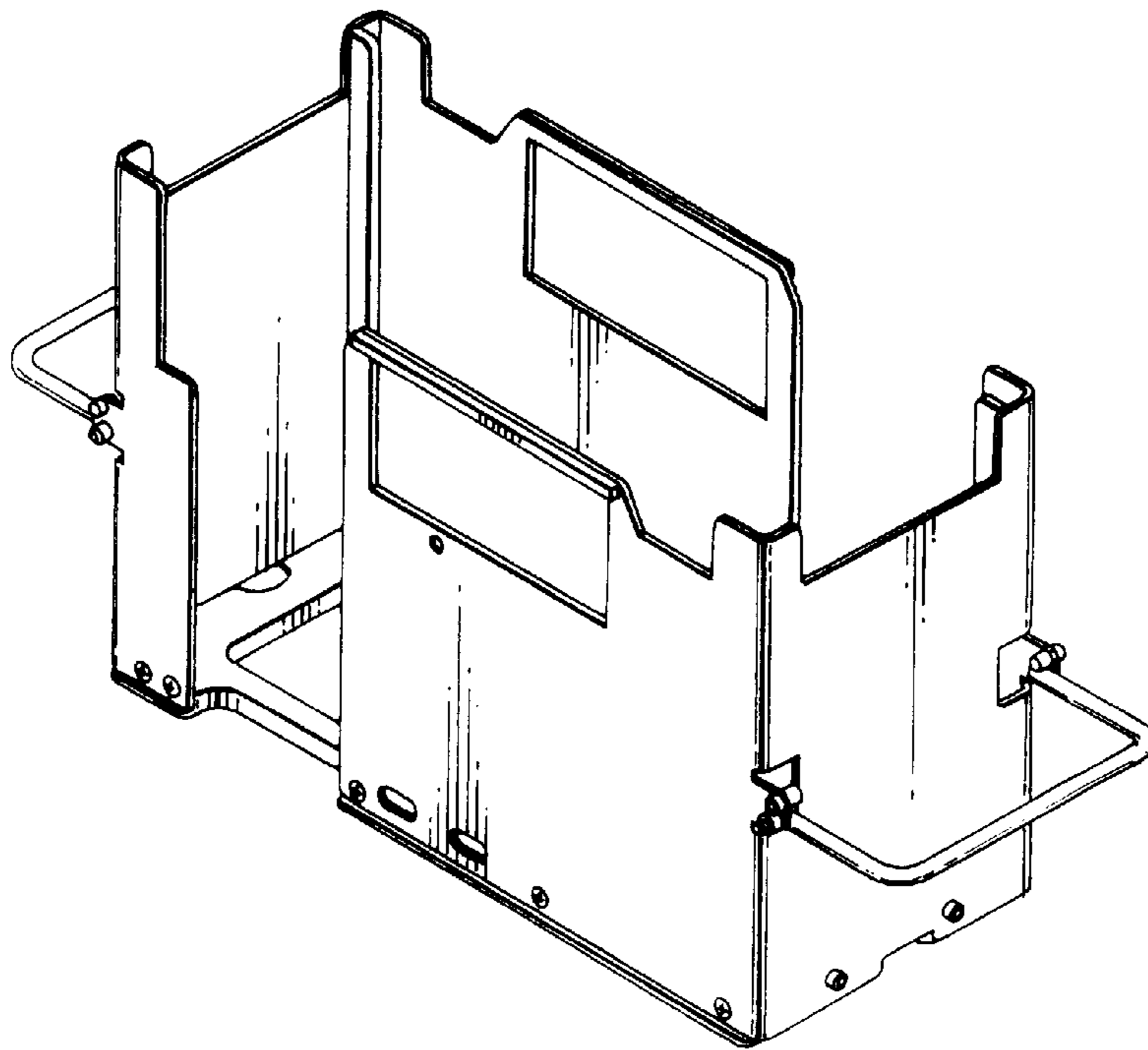


FIG.13

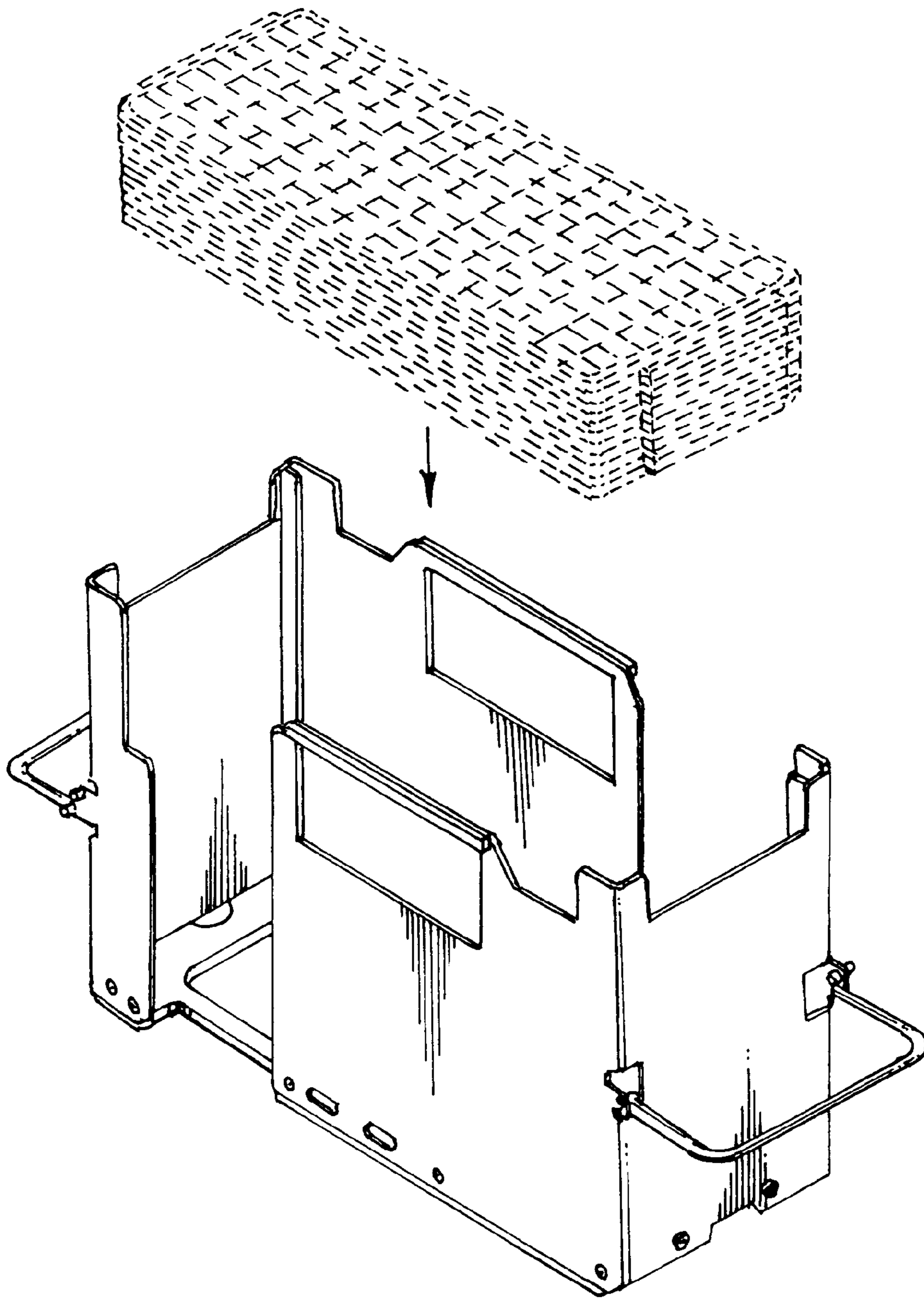


FIG. 14

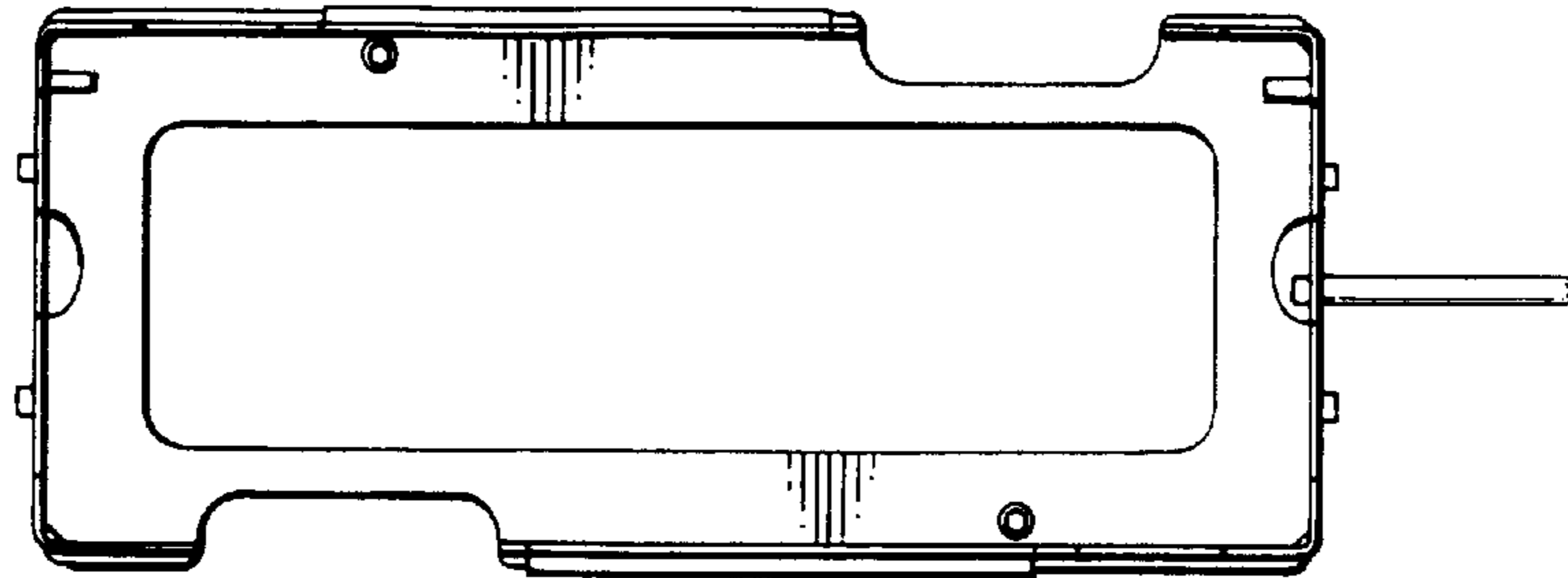


FIG. 15

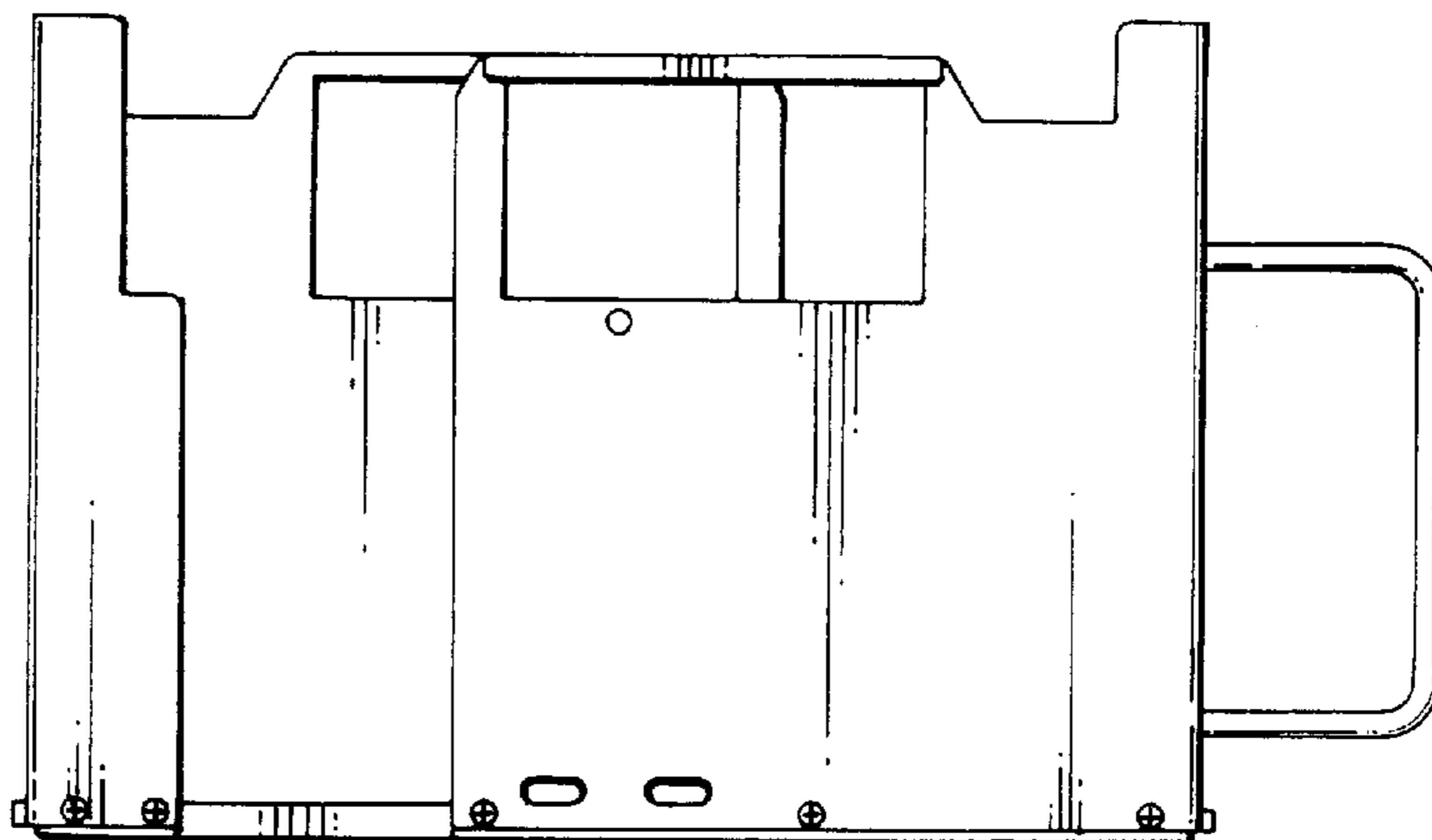


FIG. 16

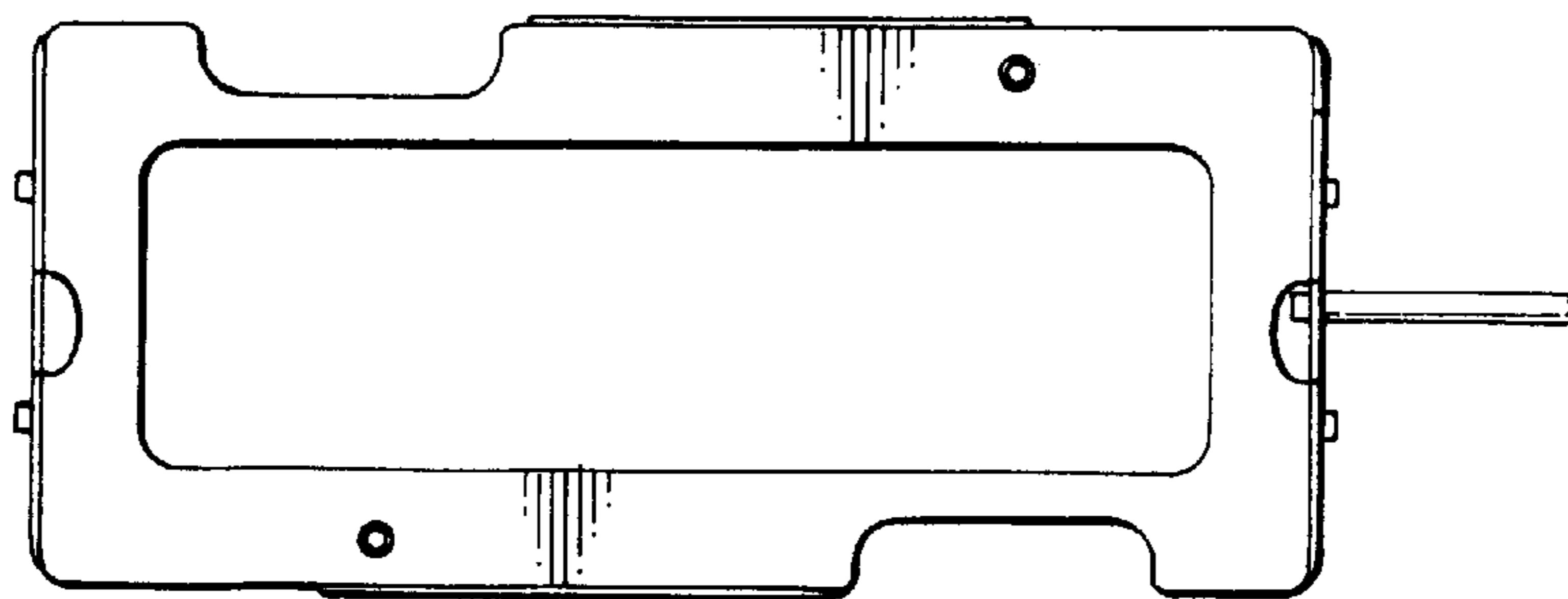


FIG. 17

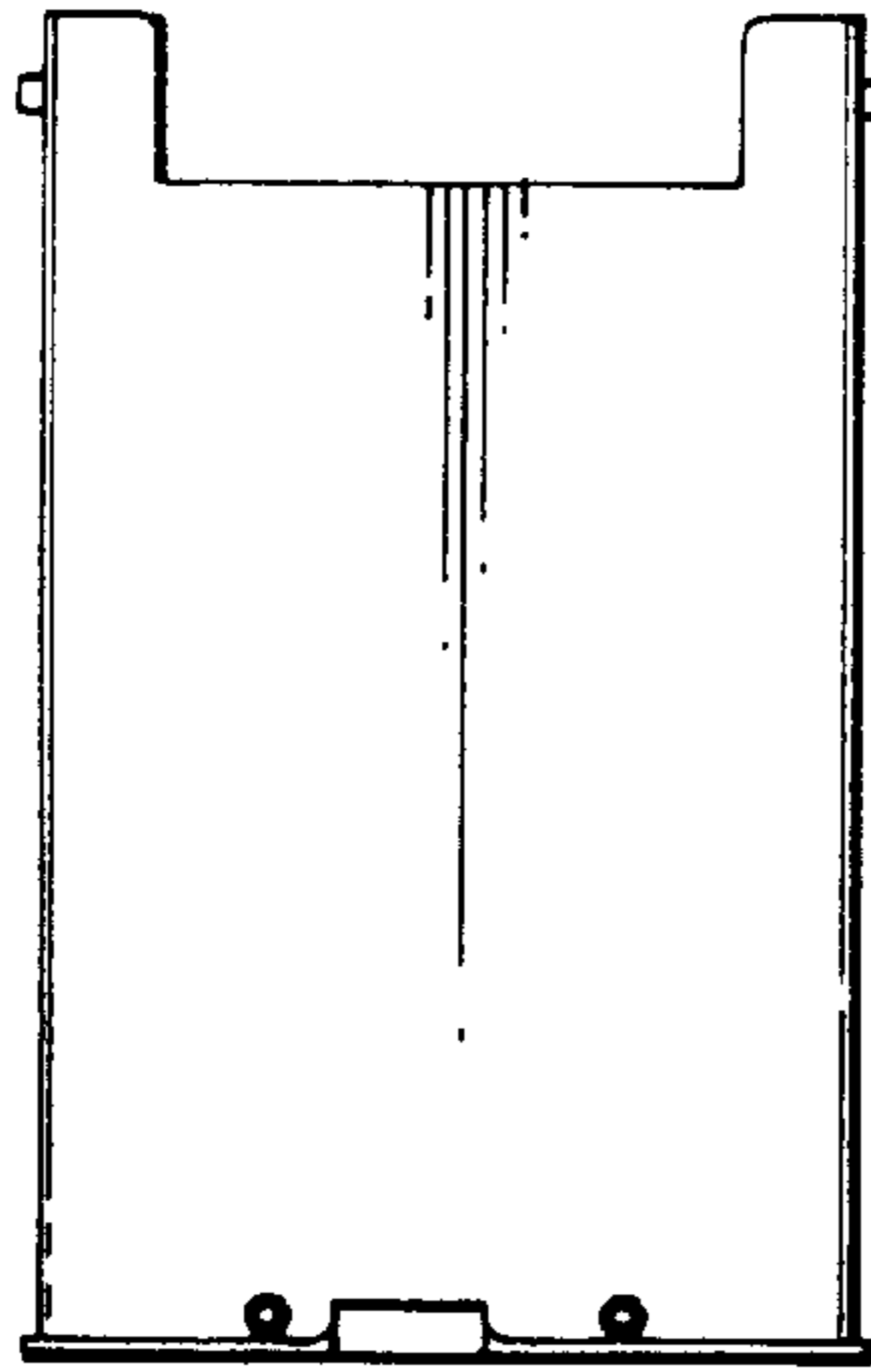


FIG. 18

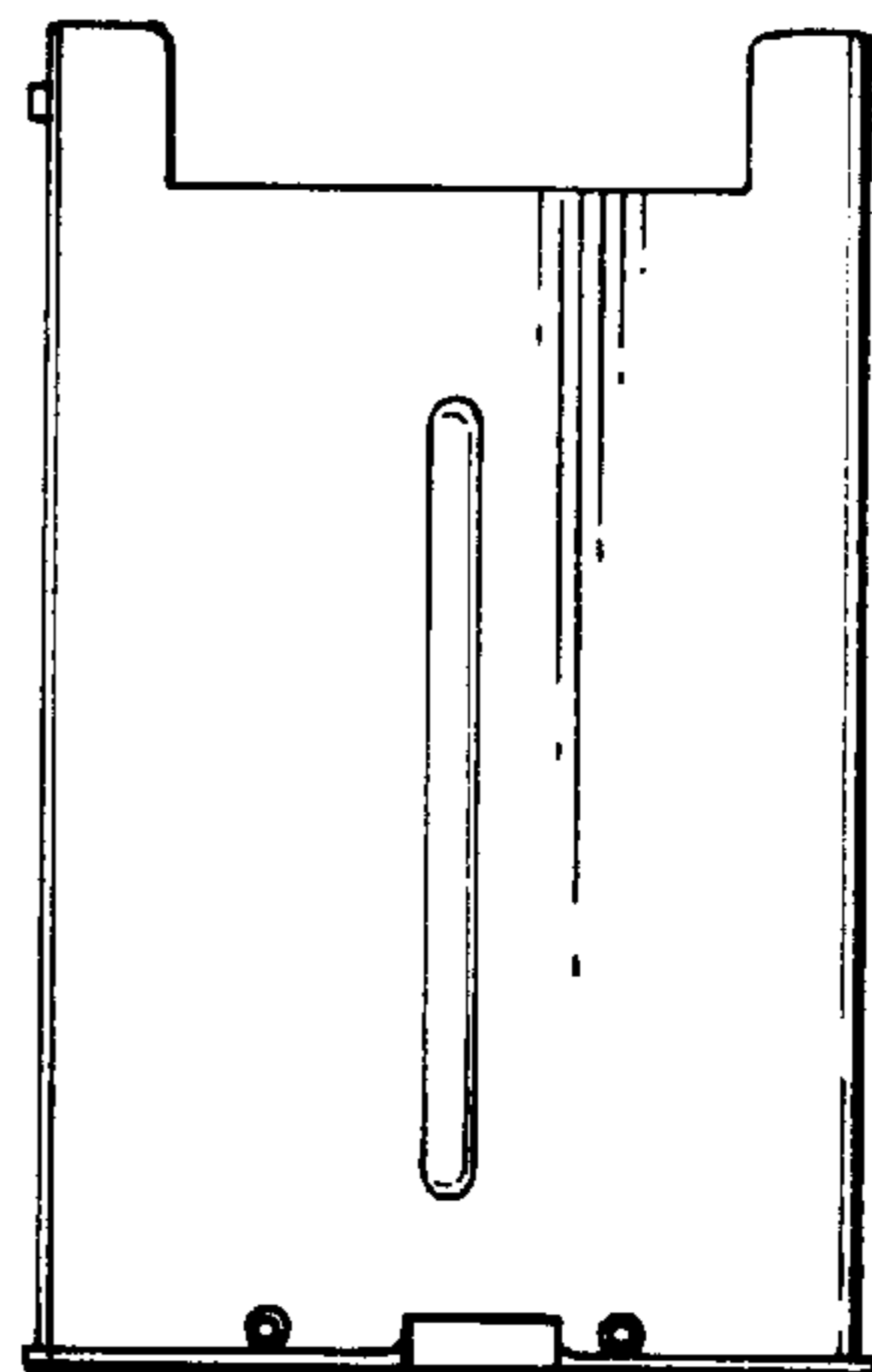


FIG. 19

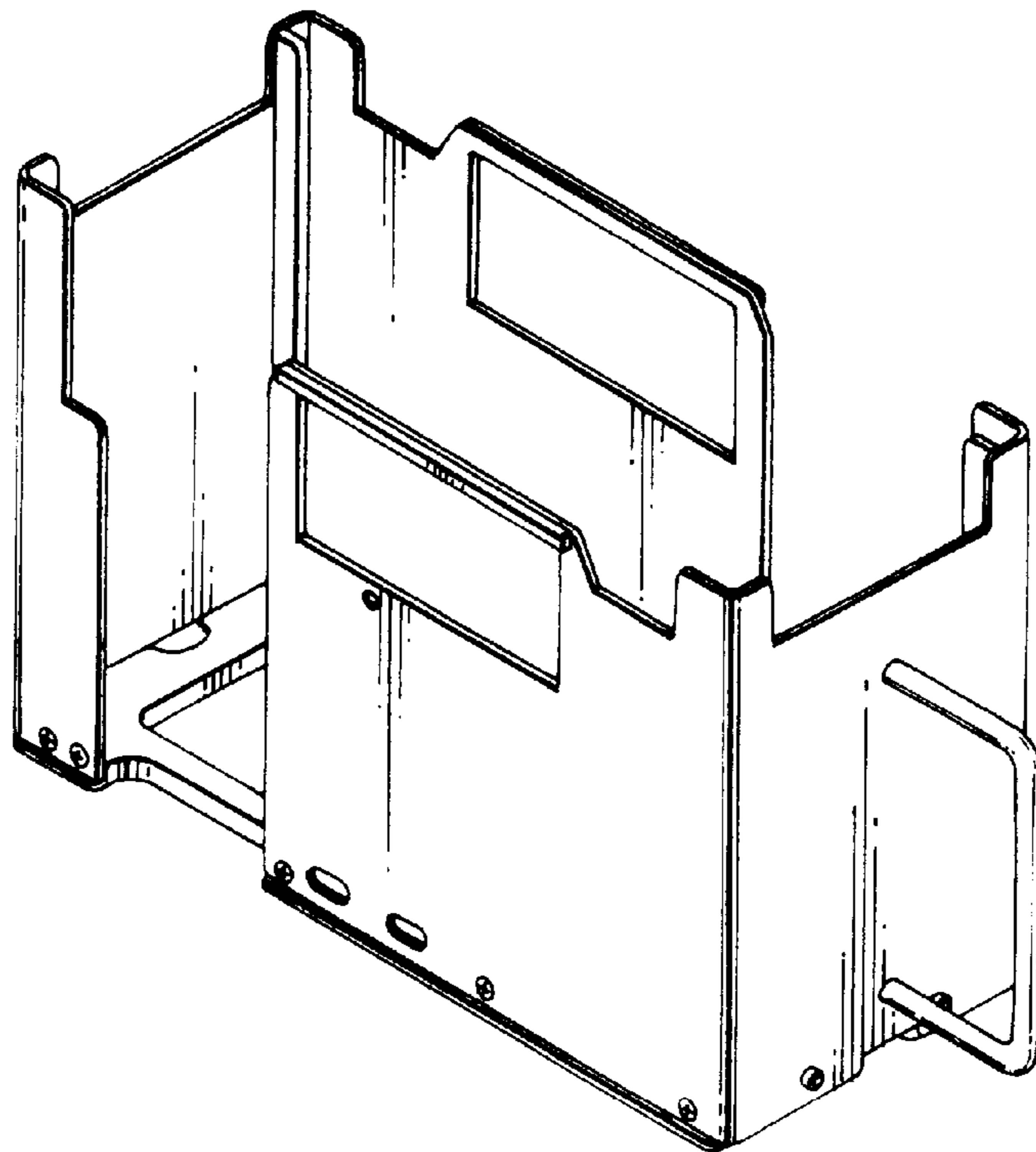


FIG. 20

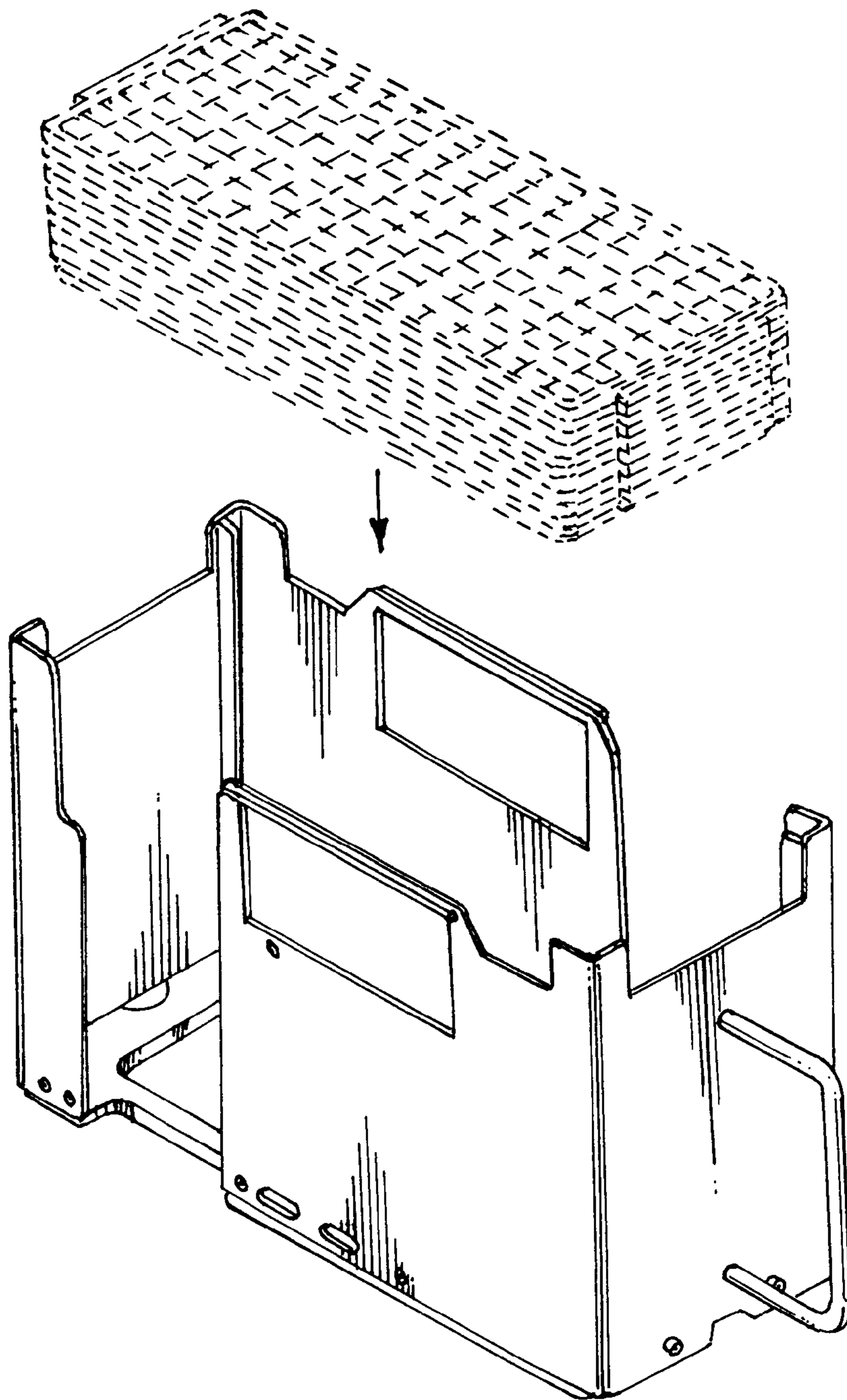


FIG. 21

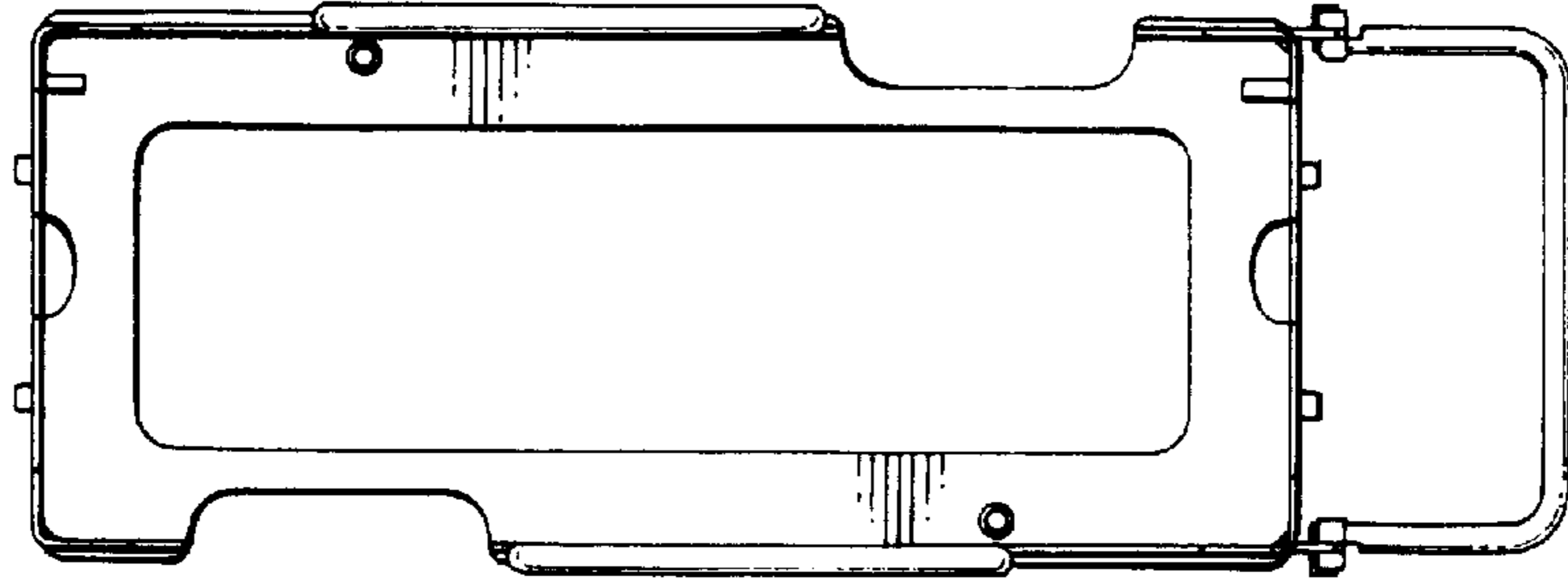


FIG. 22

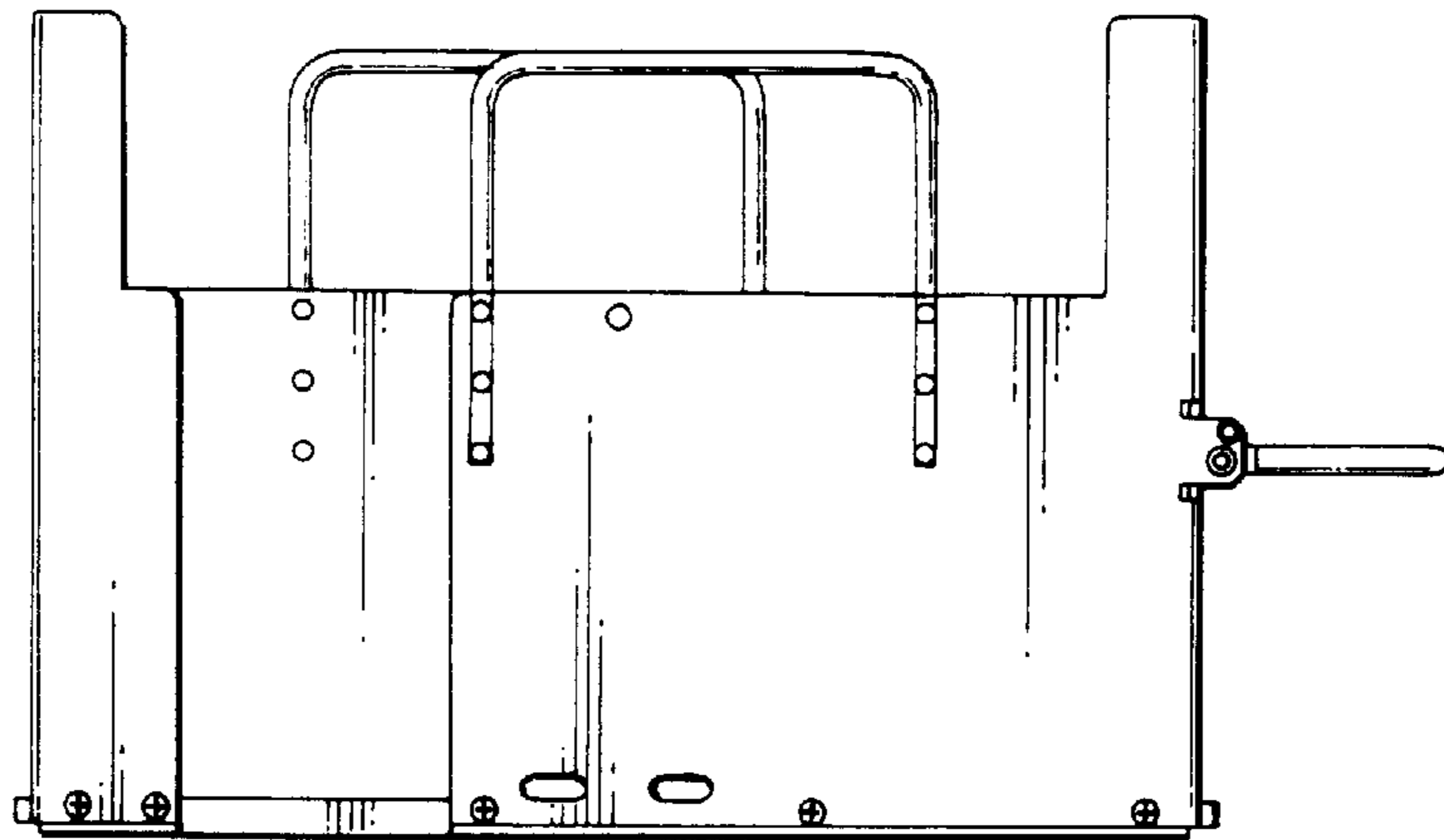


FIG. 23

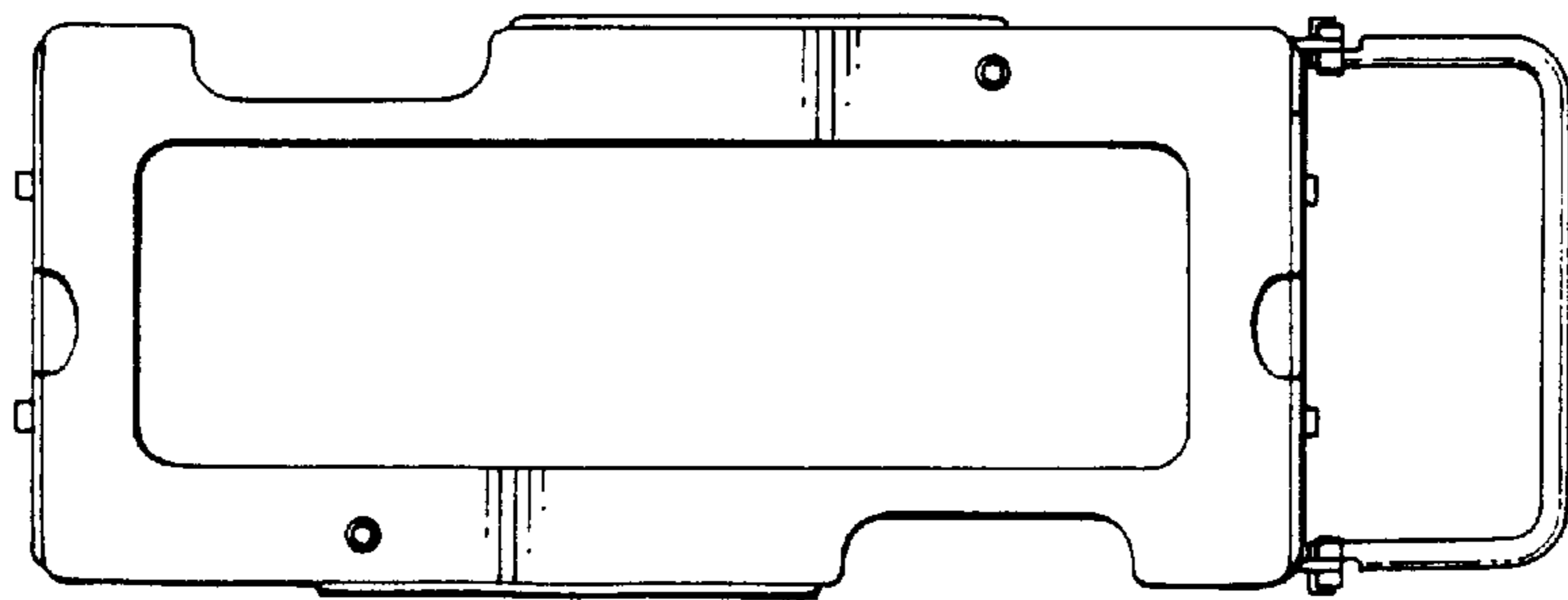


FIG. 24

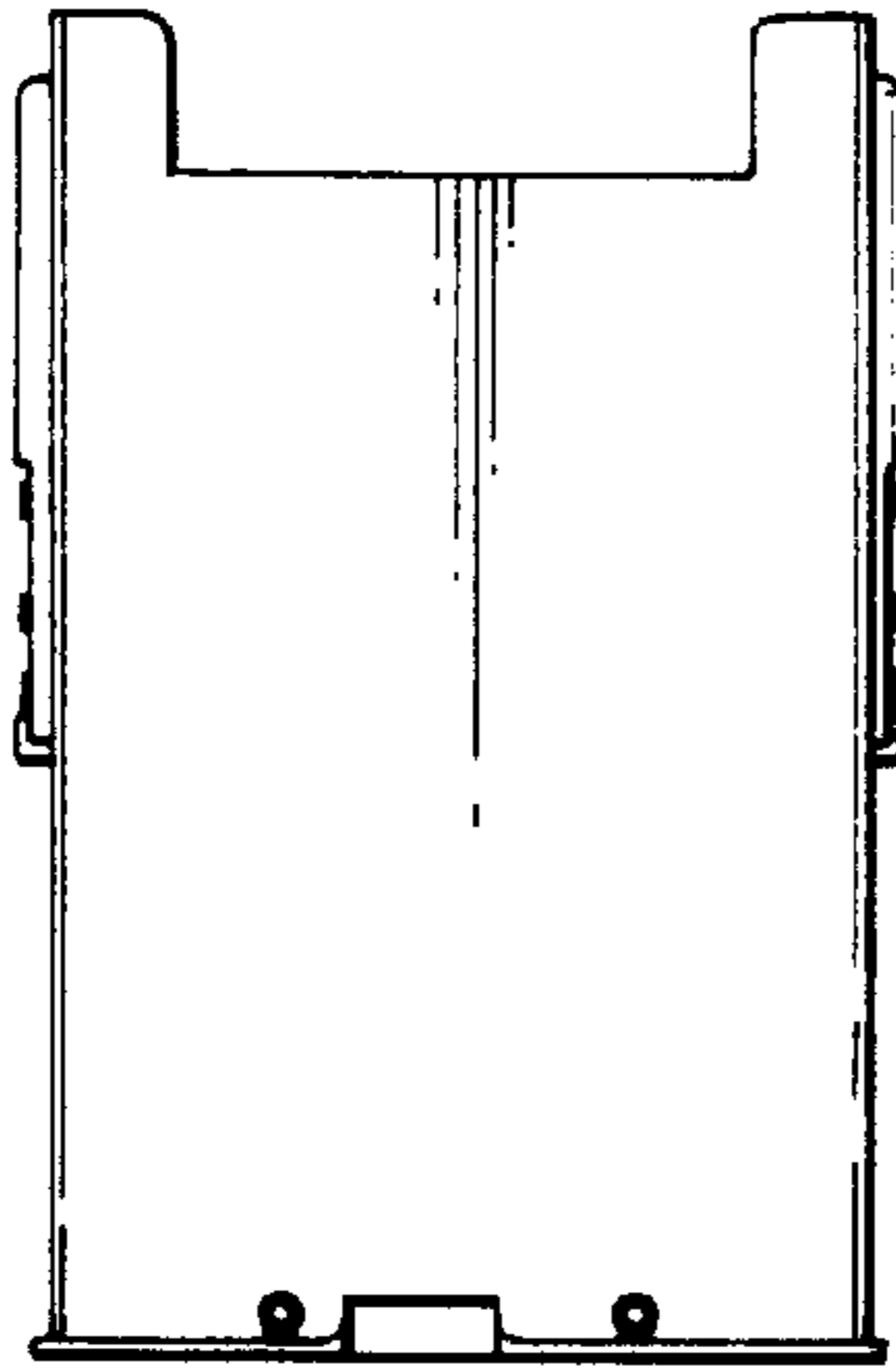


FIG. 25

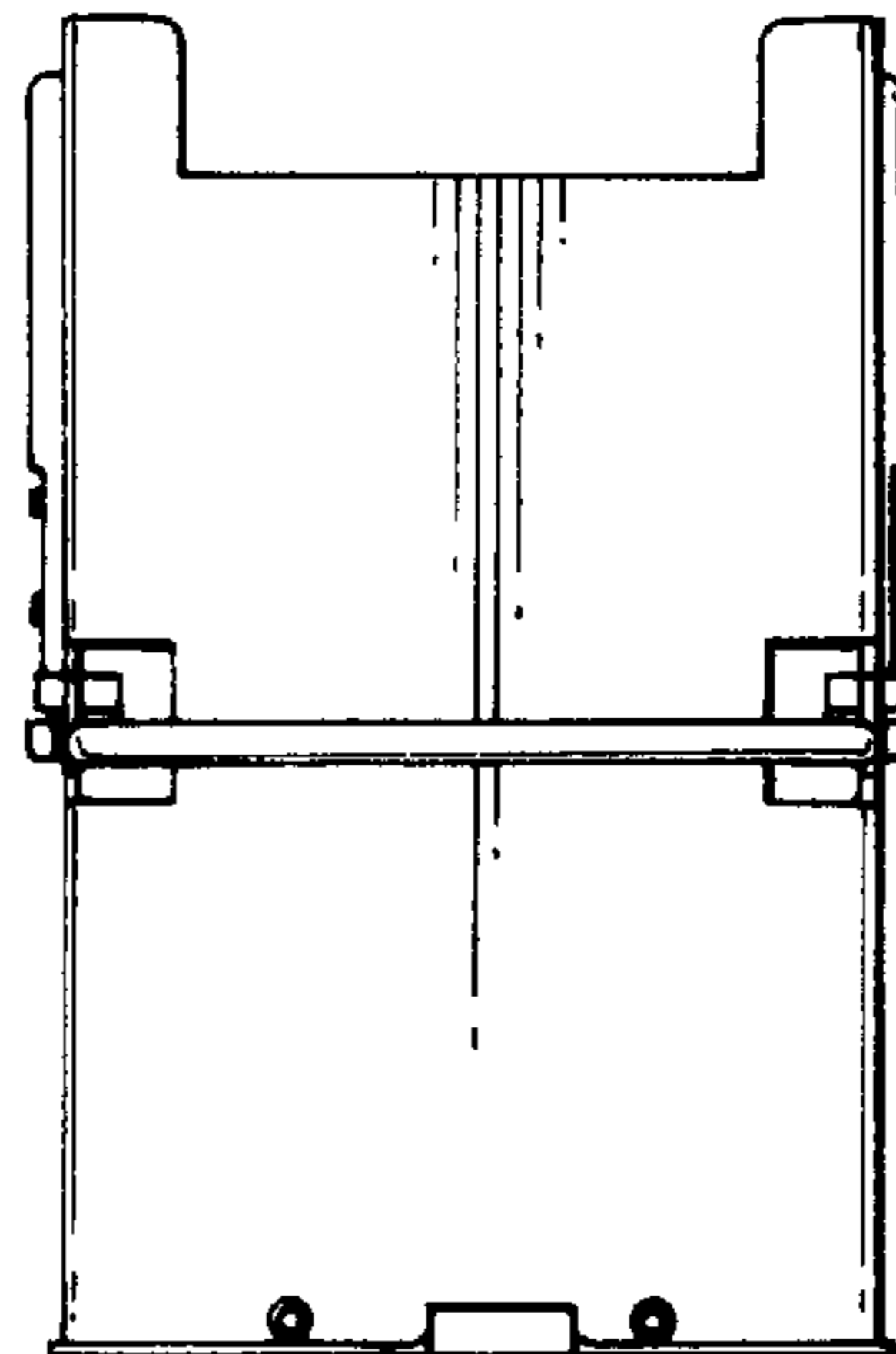


FIG. 26

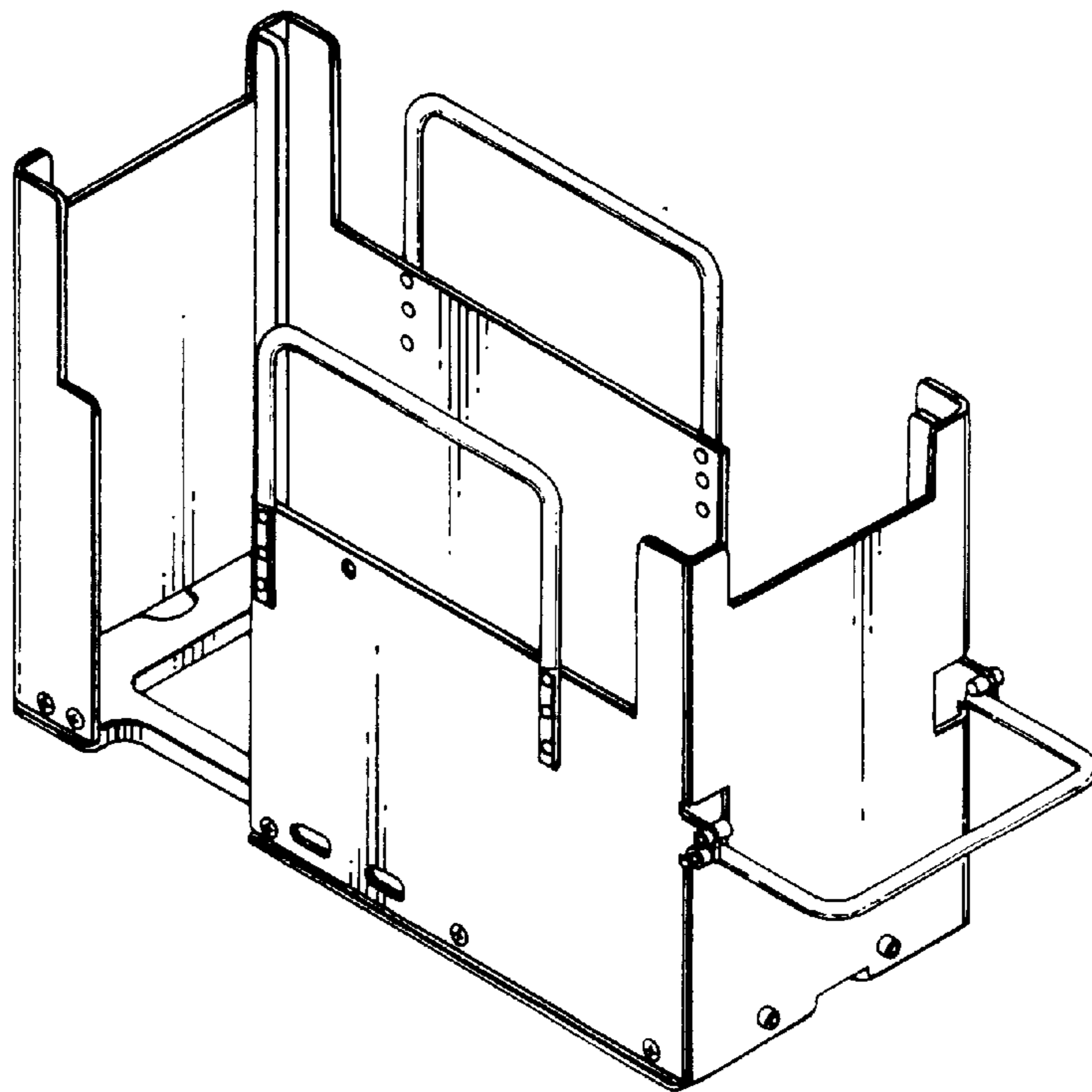


FIG. 27

