



US00D429703S

United States Patent [19] Gauffin

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[54] **ENCLOSURE FOR ACTIVE ELECTRONIC CIRCUITS**

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[73] Assignee: **Allgon AB**, Akersberga, Sweden

[**] Term: **14 Years**

[21] Appl. No.: **29/099,480**

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[30] Foreign Application Priority Data

Jul. 22, 1998 [SE] Sweden 98-1464

[51] **LOC (7) Cl.** **13-03**

[52] **U.S. Cl.** **D13/179**

[58] **Field of Search** D13/179, 184;
361/704, 709, 710, 712, 718-720

[56] References Cited

U.S. PATENT DOCUMENTS

D. 208,547 9/1967 Van Houten D13/179

D. 281,315 11/1985 Kjeld D13/179
D. 315,832 4/1991 Rocheleua et al. D13/179
D. 338,449 8/1993 Sahyoun D13/179
D. 387,333 12/1997 Pellow et al. D13/179

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[57] CLAIM

The ornamental design for an enclosure for active electronic circuits, as shown and described.

DESCRIPTION

FIG. 1 is a left side view showing my new design for an enclosure for active electronic circuits;
FIG. 2 is a top view thereof;
FIG. 3 is an inverted right side view thereof;
FIG. 4 is a bottom view thereof;
FIG. 5 is a rear view thereof;
FIG. 6 is a front view thereof;
FIG. 7 is an enlarged bottom perspective view thereof; and,
FIG. 8 is an enlarged top perspective view thereof.

1 Claim, 4 Drawing Sheets

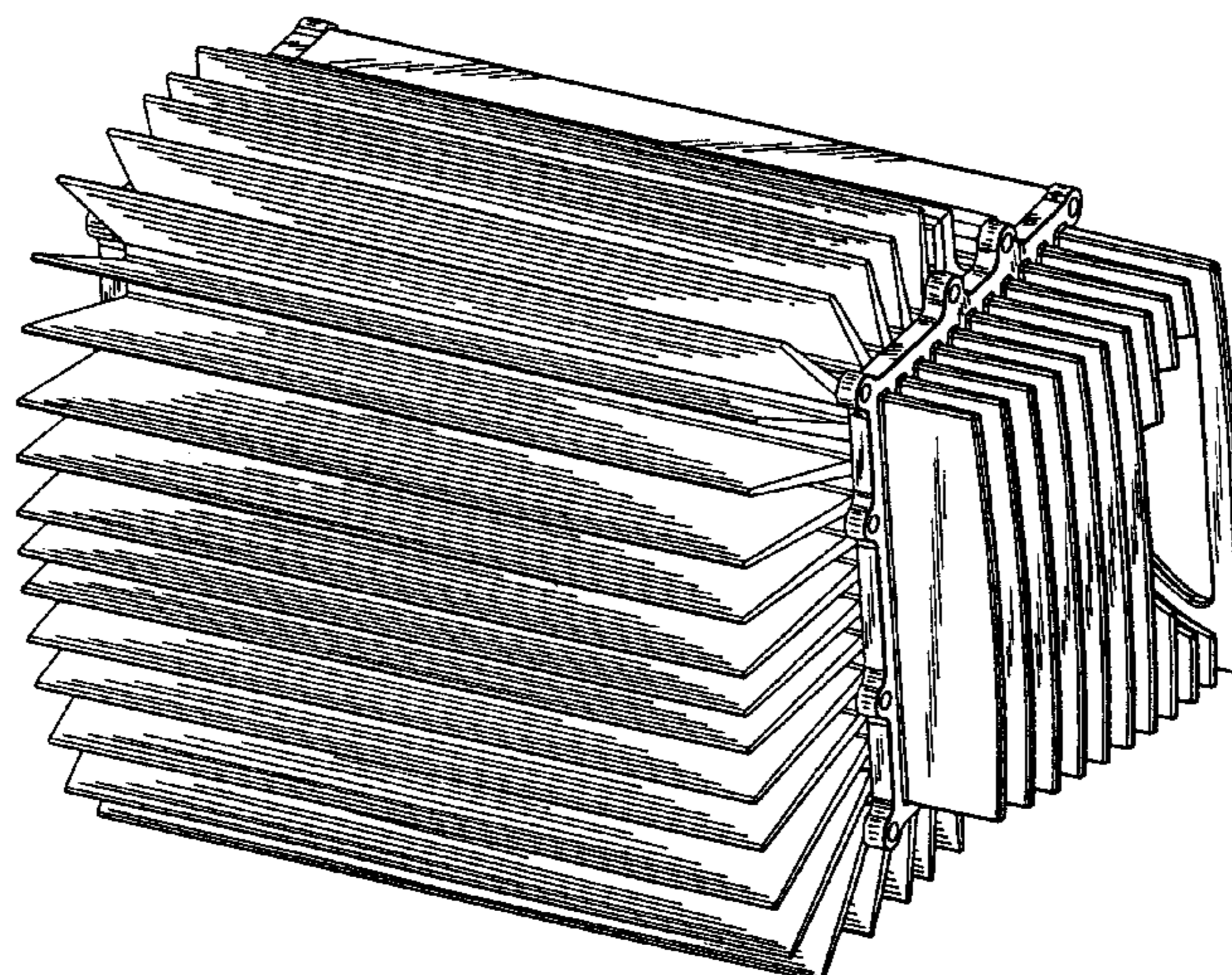
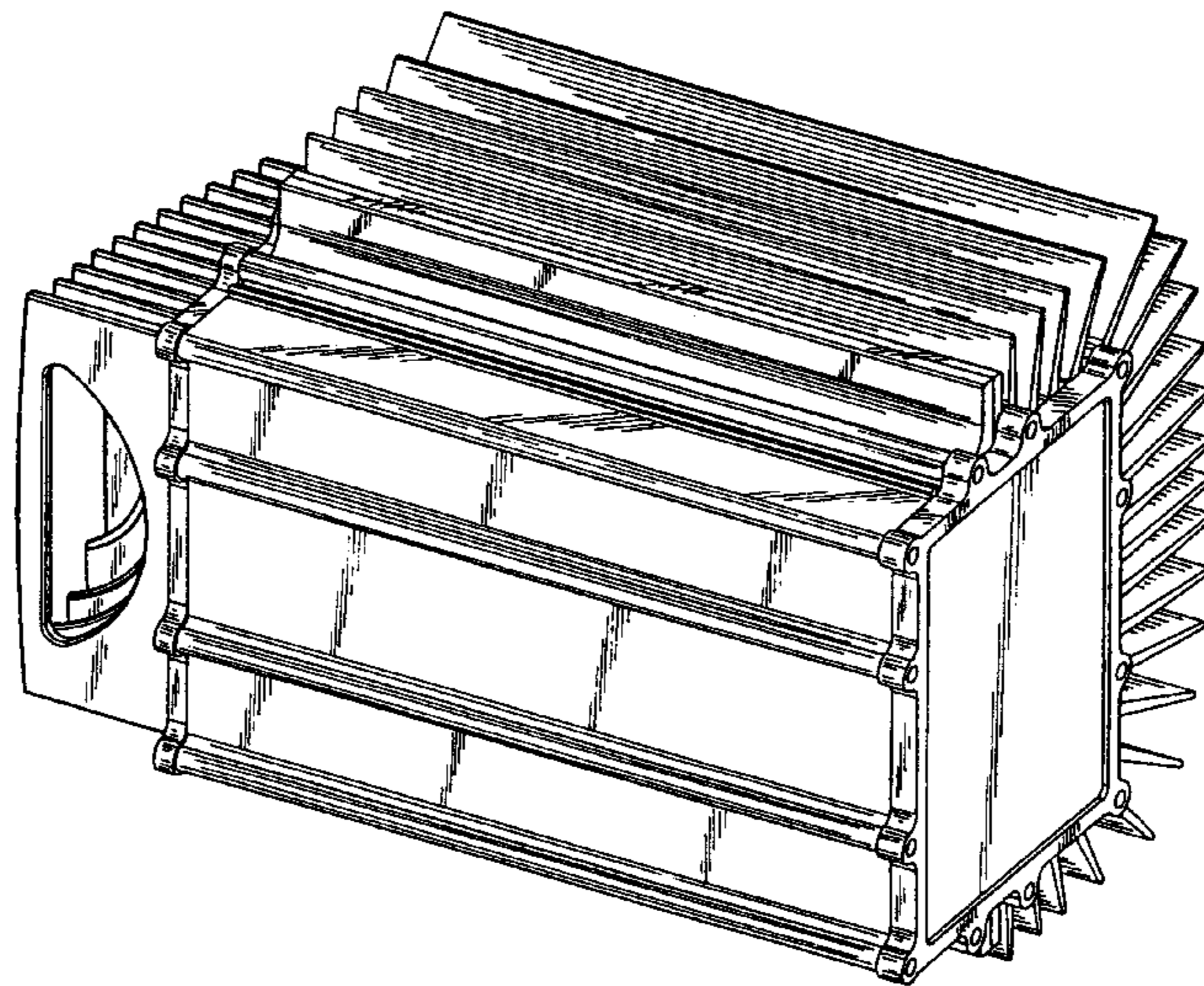


FIG. 1

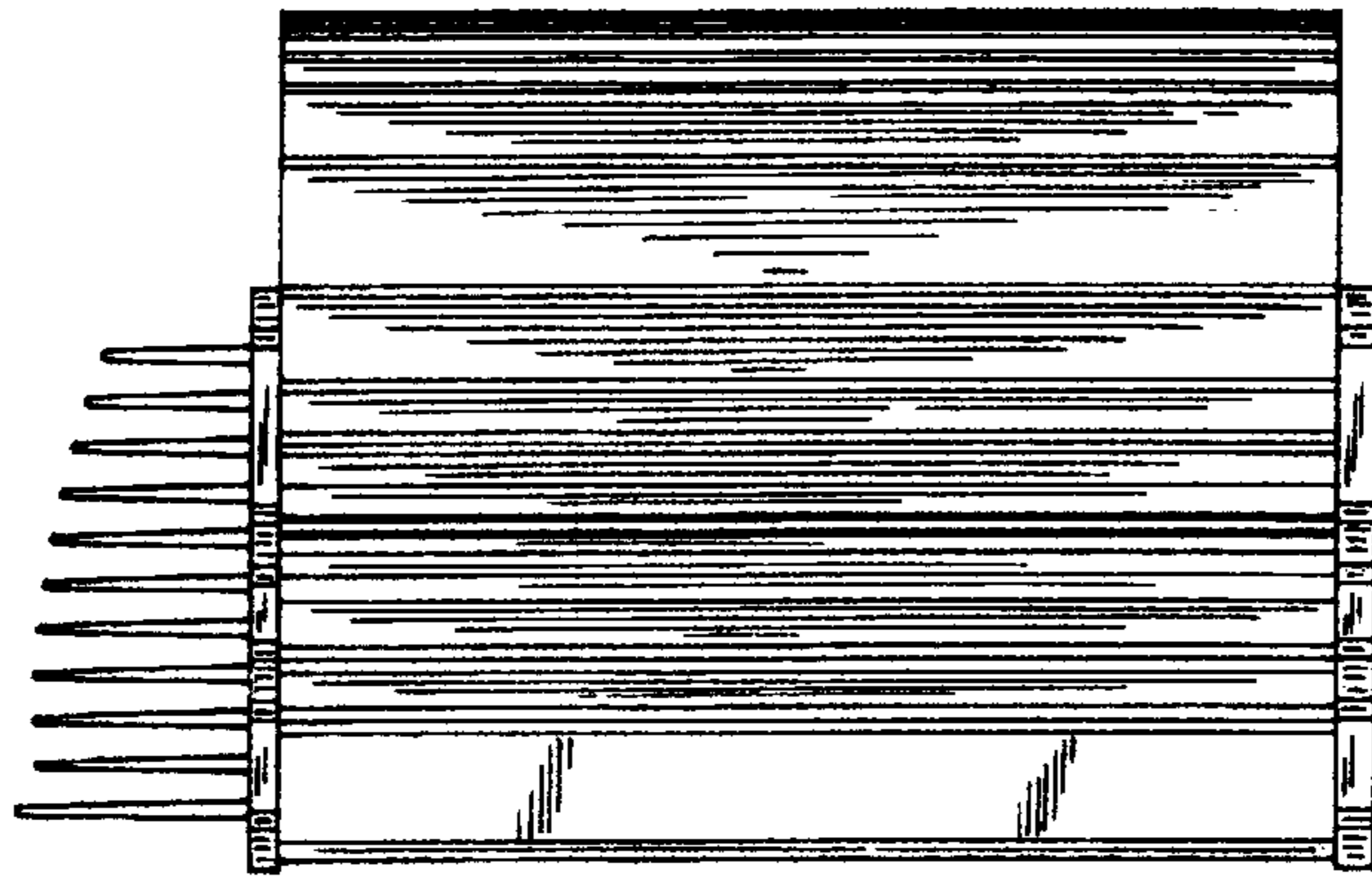


FIG. 2

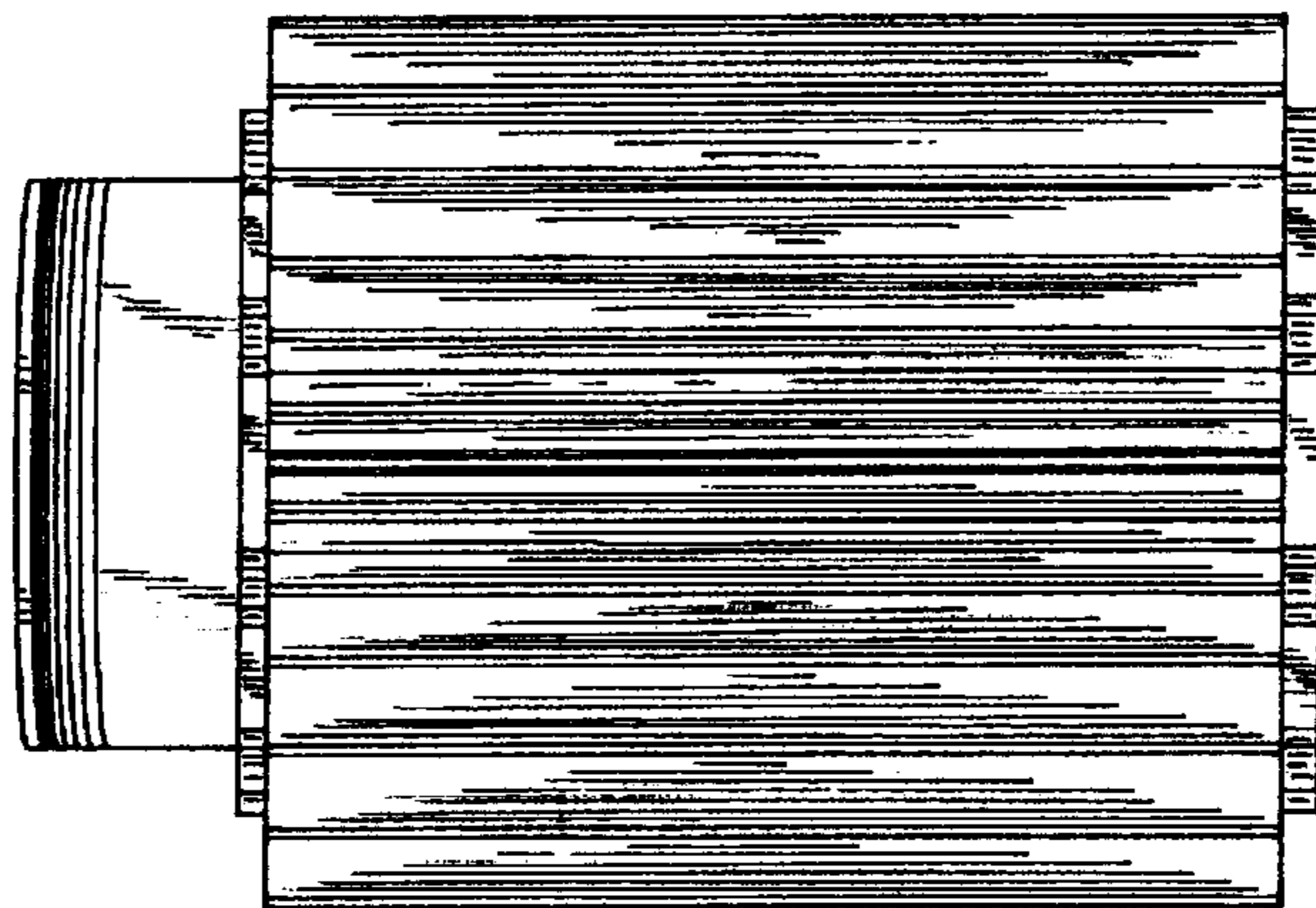


FIG. 3

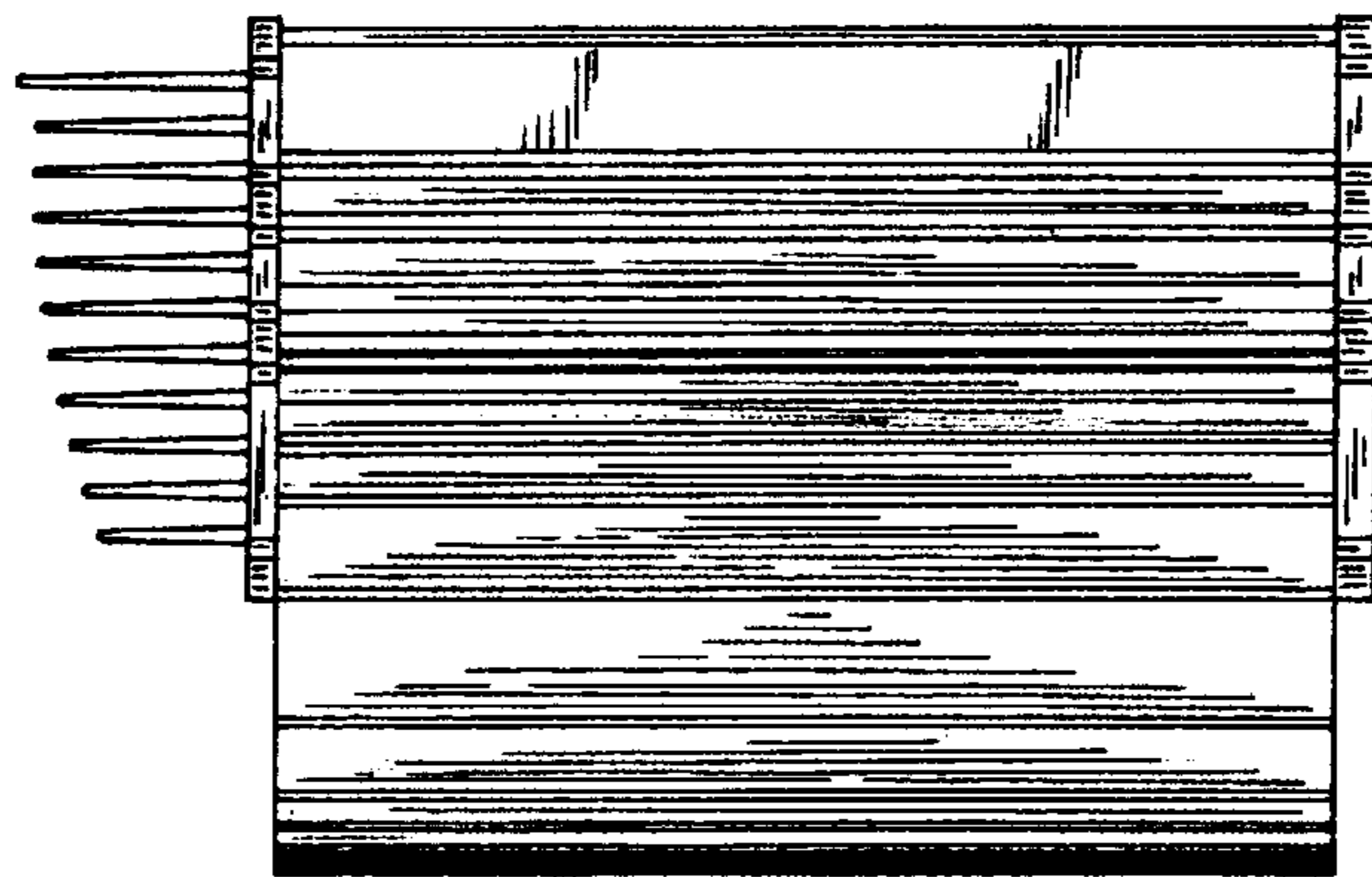


FIG. 4

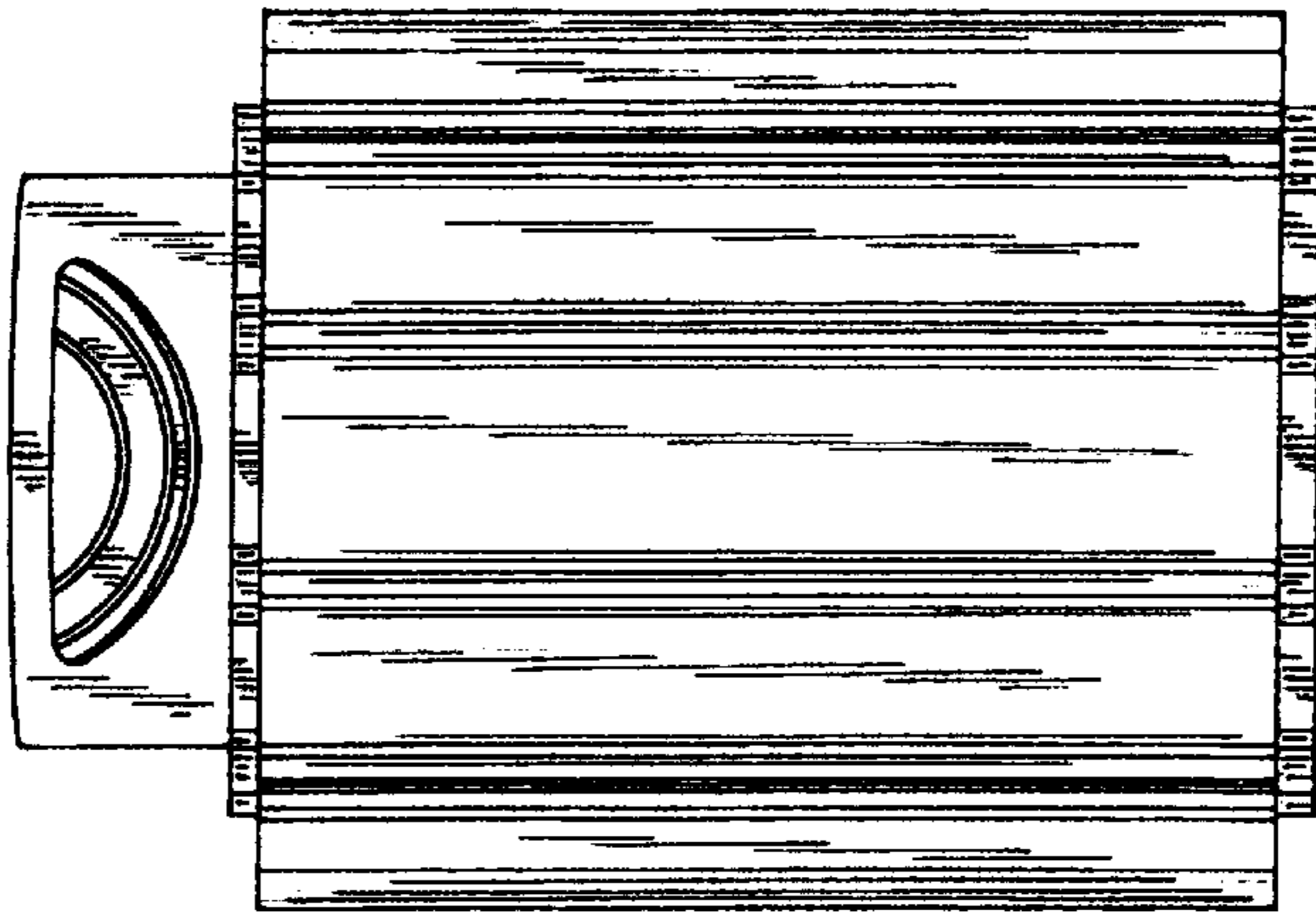


FIG. 5

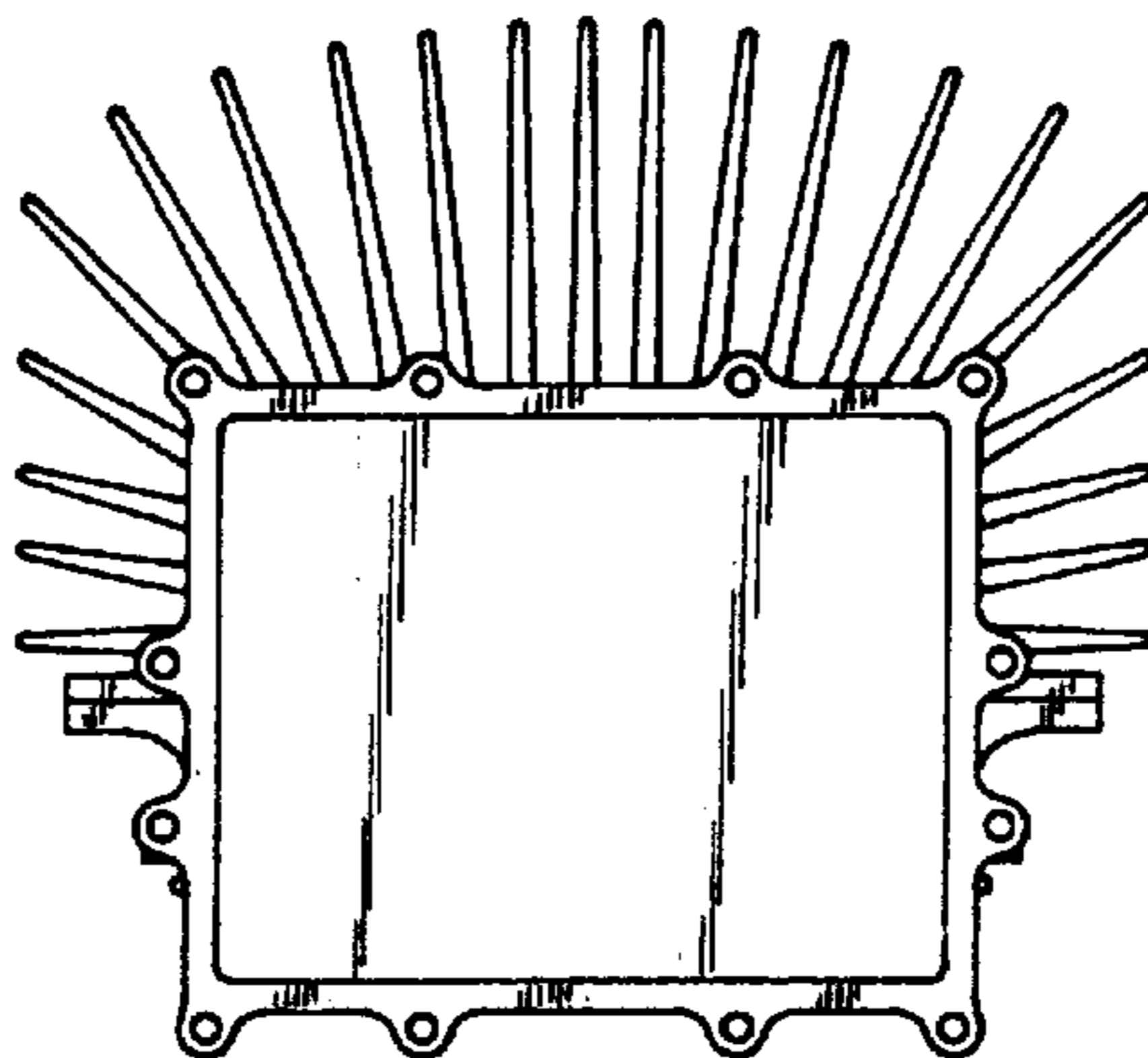


FIG. 6

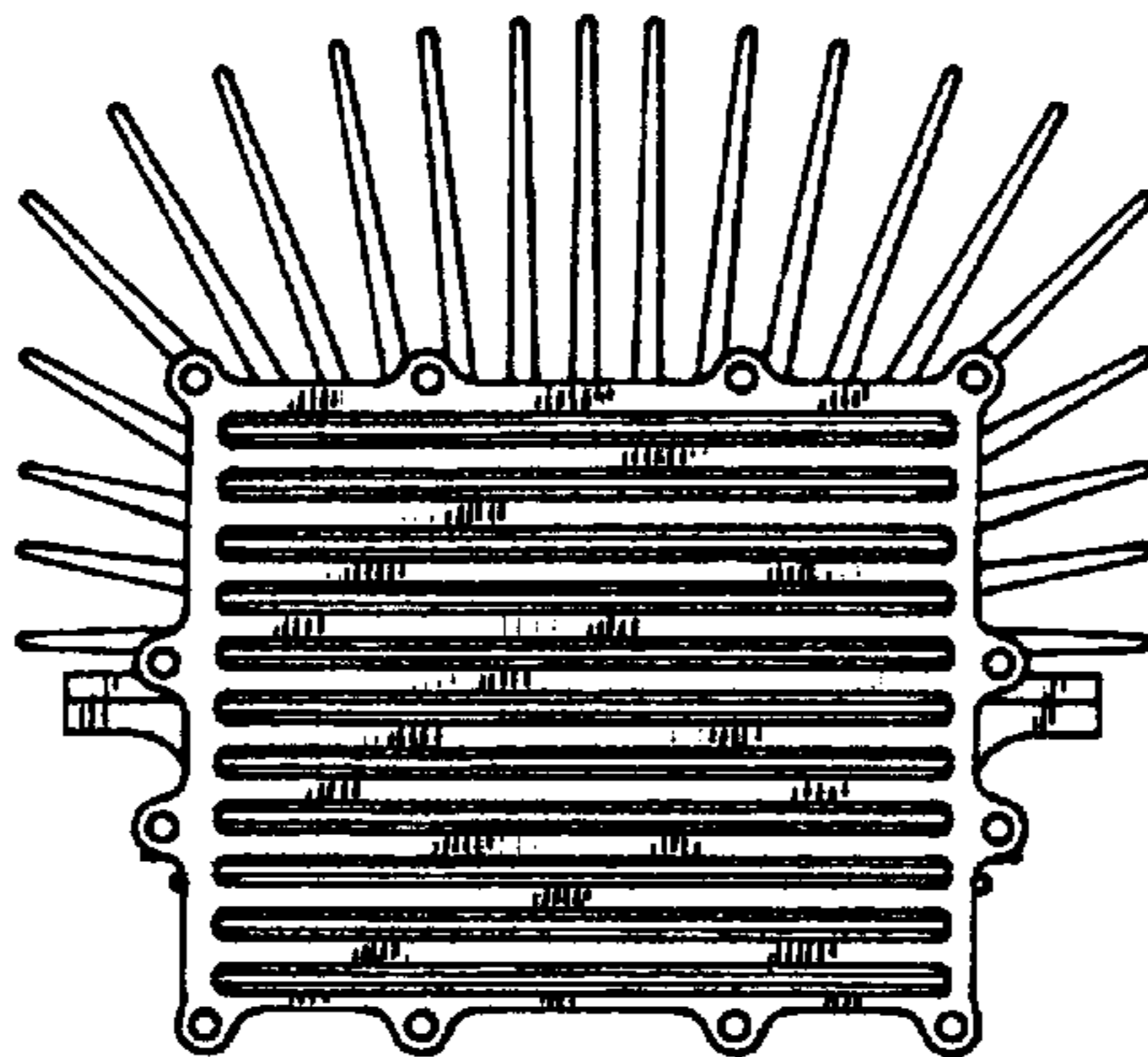


FIG. 7

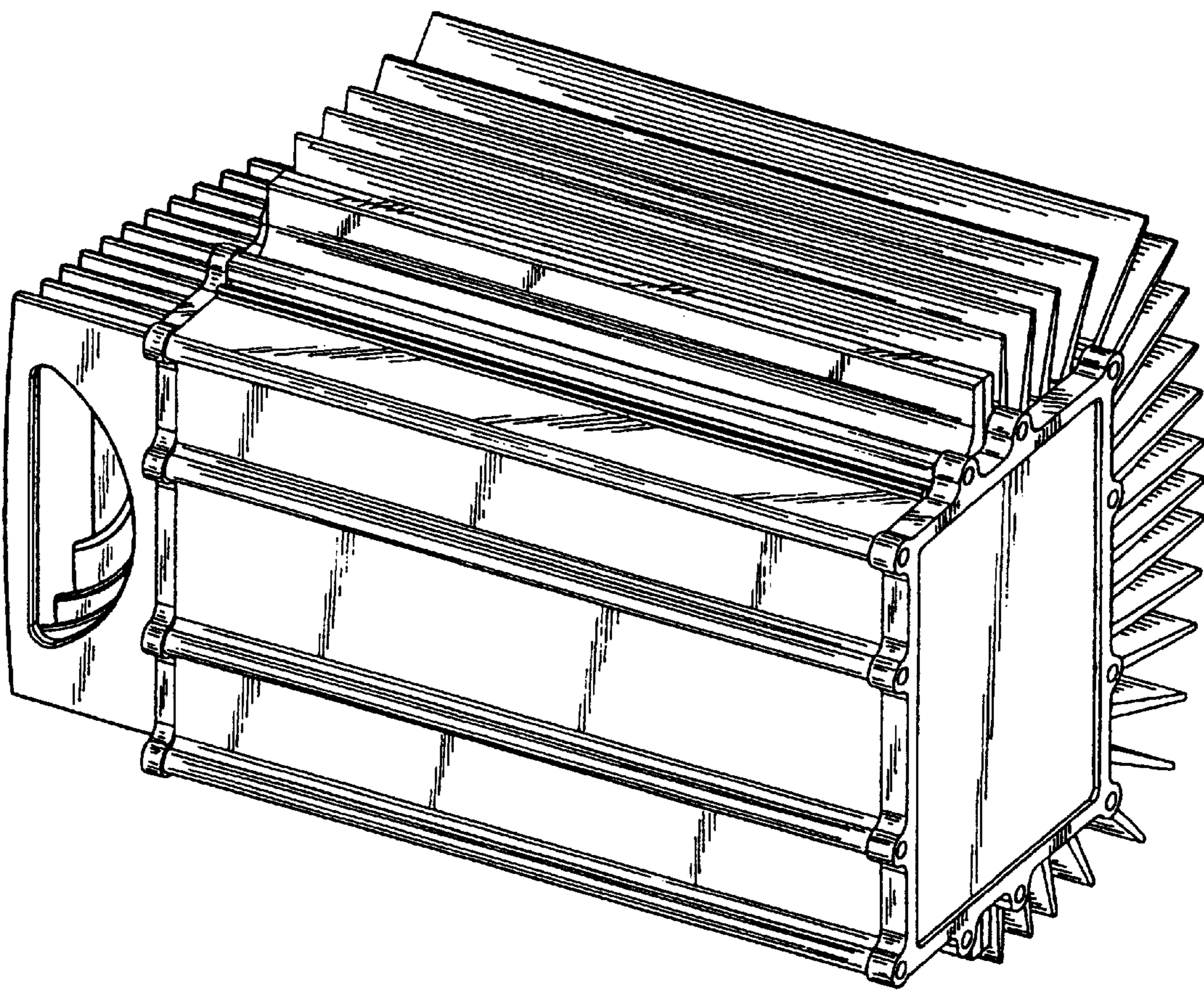


FIG. 8

