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United States Patent [19] Siperek

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[54] **CIRCUIT BOARD WITHIN A DISPLAY BOX**

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[**] Term: **14 Years**

[21] Appl. No.: **29/107,326**

[22] Filed: **Jun. 30, 1999**

[51] **LOC (7) Cl. 13-03**

[52] **U.S. Cl. D13/182**

[58] **Field of Search D13/182; 361/380, 361/384, 392, 395, 397, 399, 728**

[56] **References Cited**

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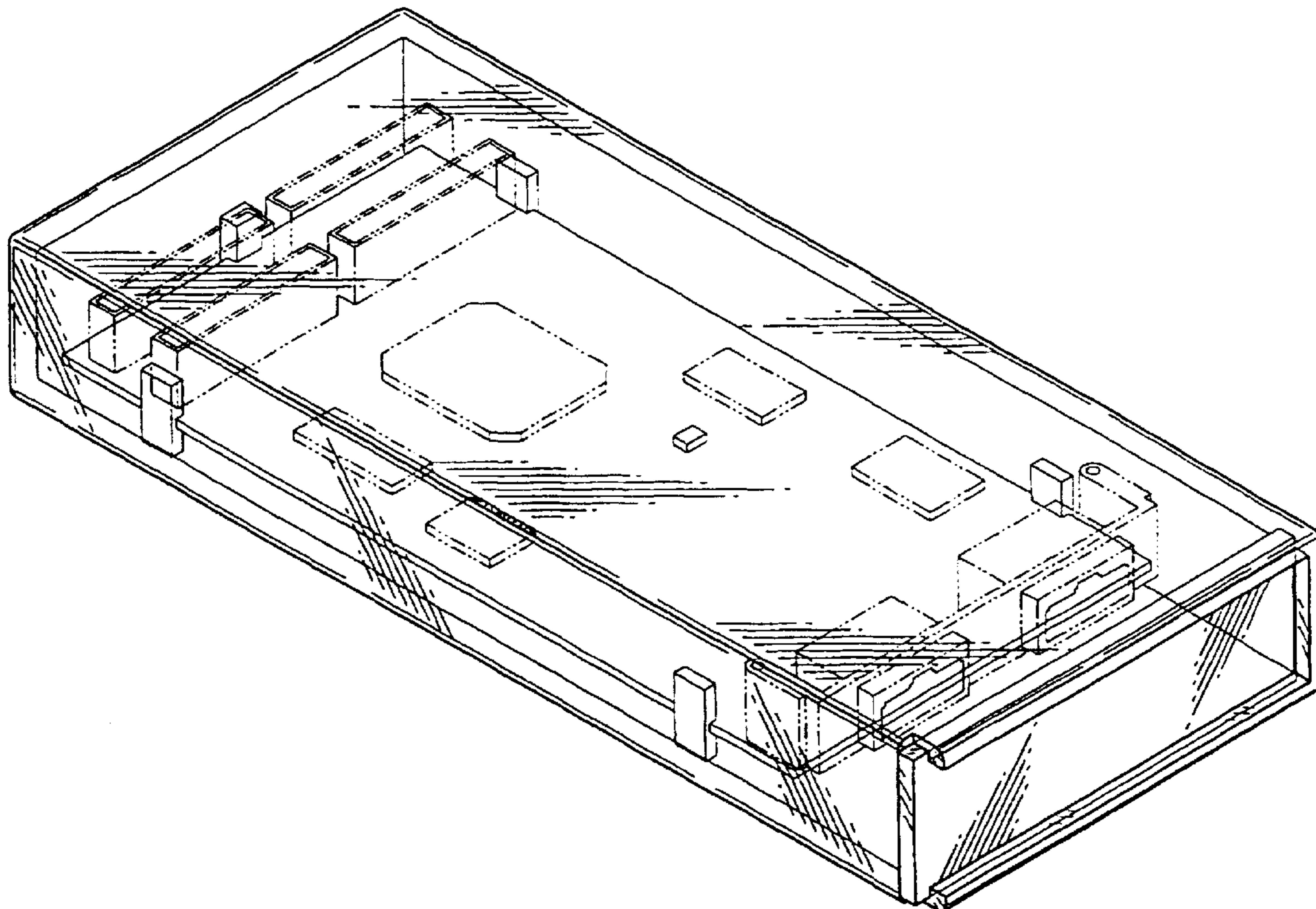
[57] **CLAIM**

The ornamental design for a circuit board within a display box, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a circuit board within a display box, showing my new design;
FIG. 2 is a top plan view thereof, the bottom plan view being a mirror image thereof;
FIG. 3 is a left side elevation view thereof,
FIG. 4 is a right side elevation view thereof,
FIG. 5 is a rear elevational view thereof; and,
FIG. 6 is a front elevational view thereof.
The broken line environmental structure is for illustrative purposes only and forms no part of the claimed design.

1 Claim, 2 Drawing Sheets



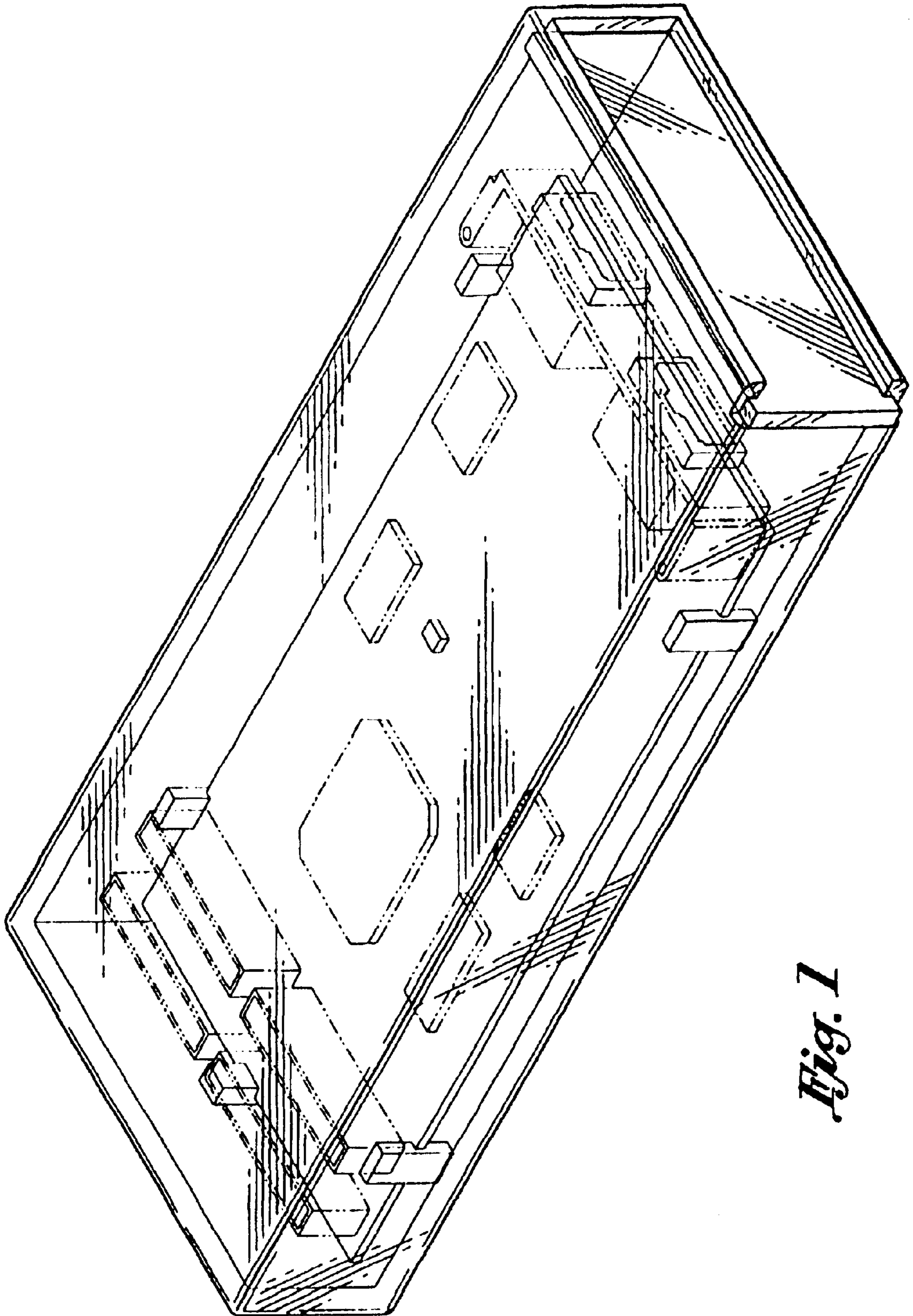


Fig. 1

Fig. 2

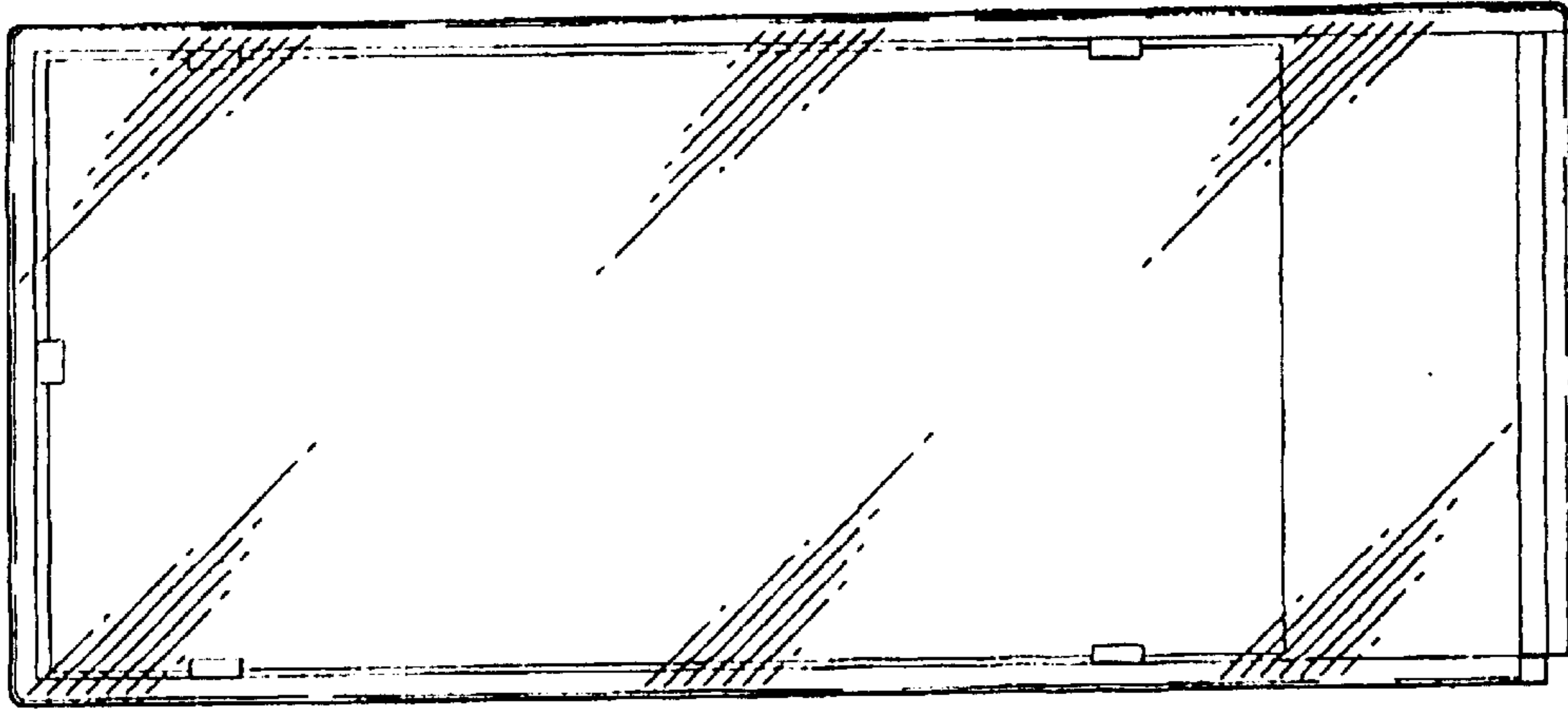


Fig. 3

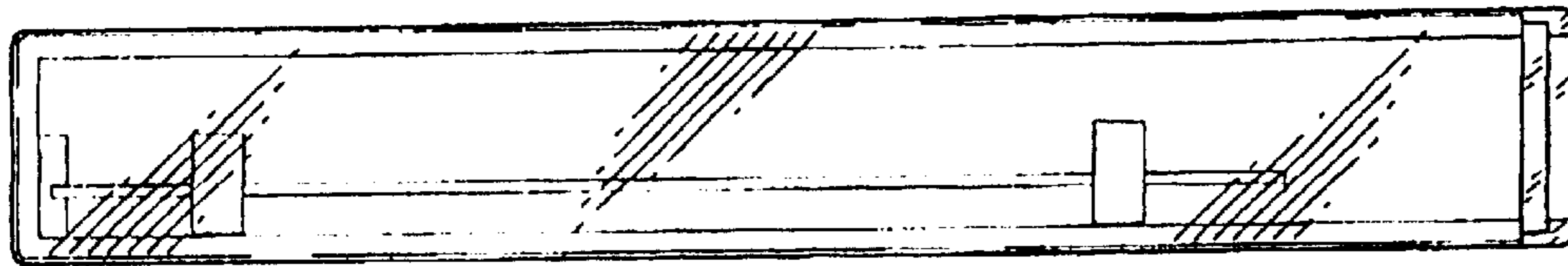


Fig. 4

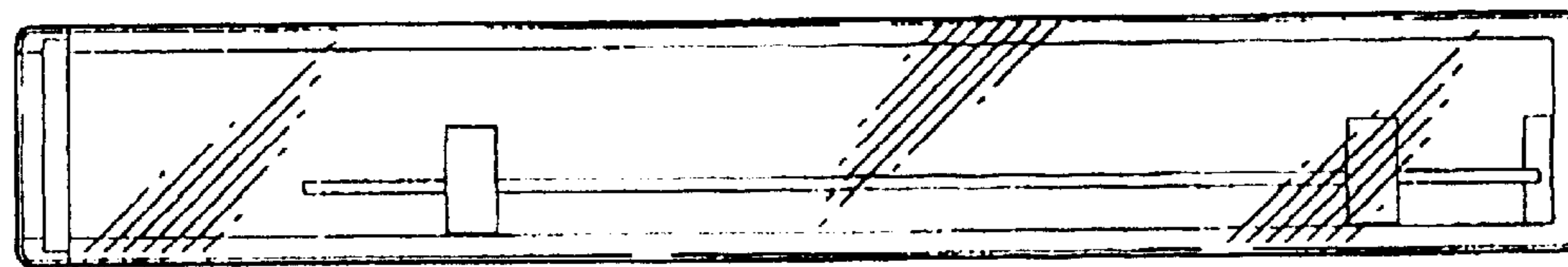


Fig. 5

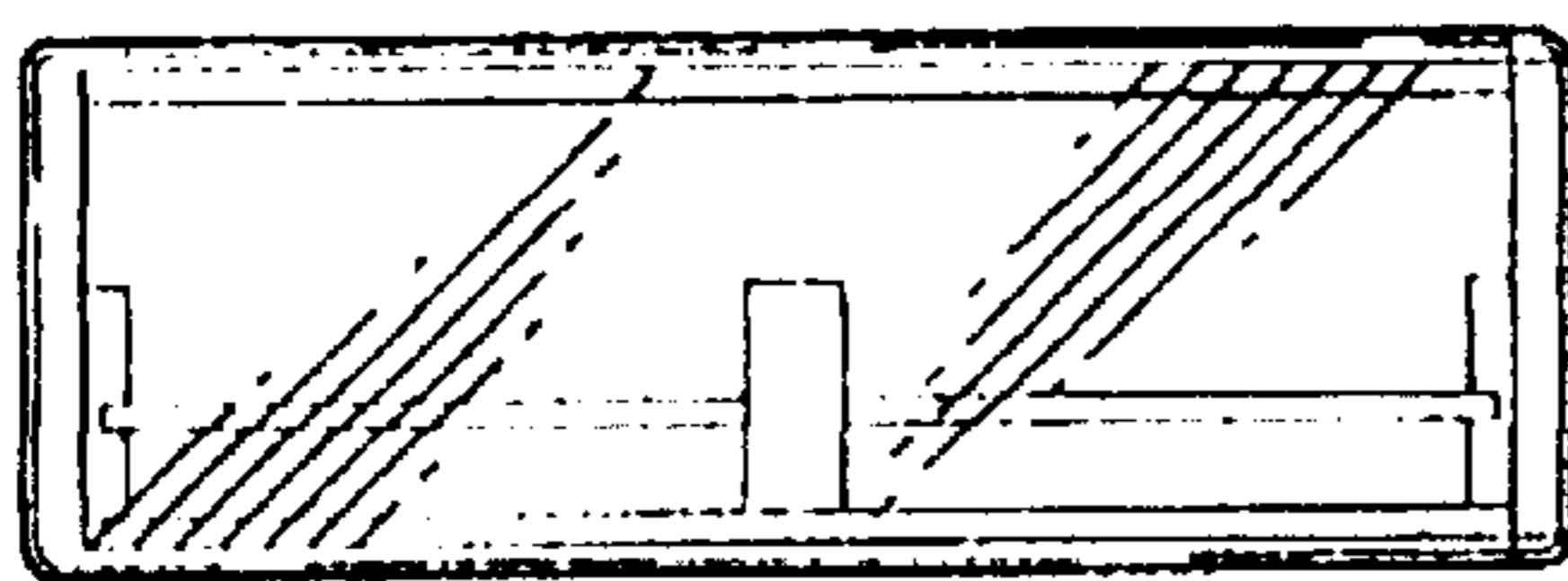


Fig. 6

