



US00D427921S

United States Patent [19]
Horstmann

[11] **Patent Number: Des. 427,921**
[45] **Date of Patent: ** Jul. 11, 2000**

[54] **RATE ADJUSTABLE FAULTED CIRCUIT INDICATOR**
[75] Inventor: **Hendrik Horstmann**, Ratingen, Germany
[73] Assignee: **Dipl.-Ing H. Horstmann GmbH**, Germany
[**] Term: **14 Years**
[21] Appl. No.: **29/092,250**
[22] Filed: **Aug. 14, 1998**
[51] **LOC (7) Cl. 10-05**
[52] **U.S. Cl. D10/75; D13/160**
[58] **Field of Search D10/75; D13/160; 324/133, 522, 525; 340/664, 650**

[56] **References Cited**

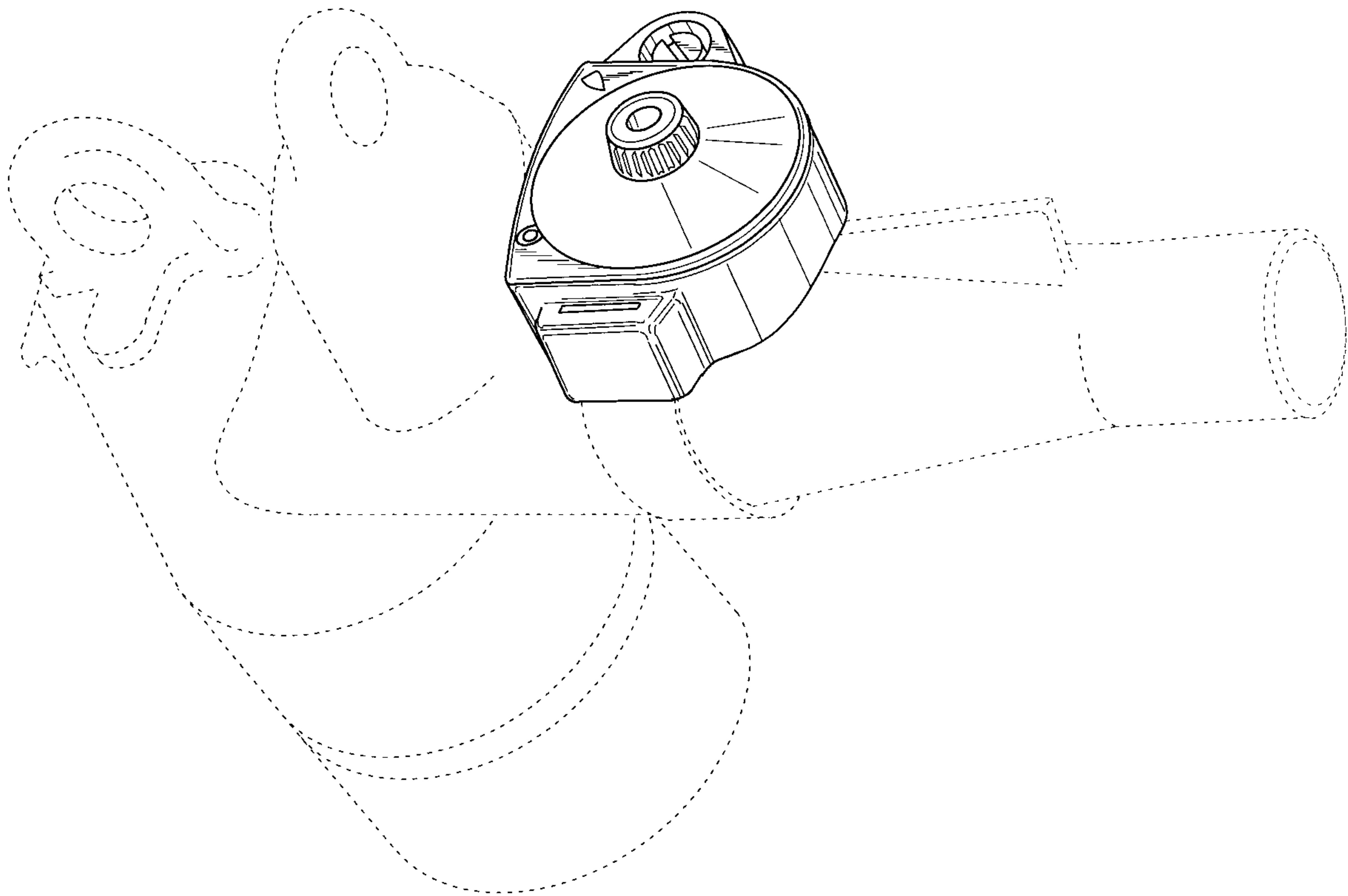
U.S. PATENT DOCUMENTS				
D. 223,472	4/1972	Natkanski		D10/75
D. 386,098	11/1997	Horstmann		D10/75
4,536,758	8/1985	Schweitzer, Jr.		340/664
5,497,096	3/1996	Banting		324/555
5,748,095	5/1998	Horstmann		340/664

5,801,526 9/1998 Horstmann 324/133
Primary Examiner—Adir Aronovich
Attorney, Agent, or Firm—Davis Chin

[57] **CLAIM**
The ornamental design for a rate adjustable faulted circuit indicator, as shown and described.

DESCRIPTION
FIG. 1 is a front elevational view of a rate adjustable faulted circuit indicator showing my design;
FIG. 2 is a top plan view of the design shown in FIG. 1;
FIG. 3 is a right side elevational view of the design shown in FIG. 1;
FIG. 4 is a bottom plan view of the design shown in FIG. 1;
FIG. 5 is a rear elevational view of the design shown in FIG. 1;
FIG. 6 is a perspective view of the design shown in FIG. 1, the broken line showing of an elbow type connector is illustrated to show the environment only and forms no part of the claimed design; and,
FIG. 7 is a left side elevational view of the design shown in FIG. 1.

1 Claim, 2 Drawing Sheets



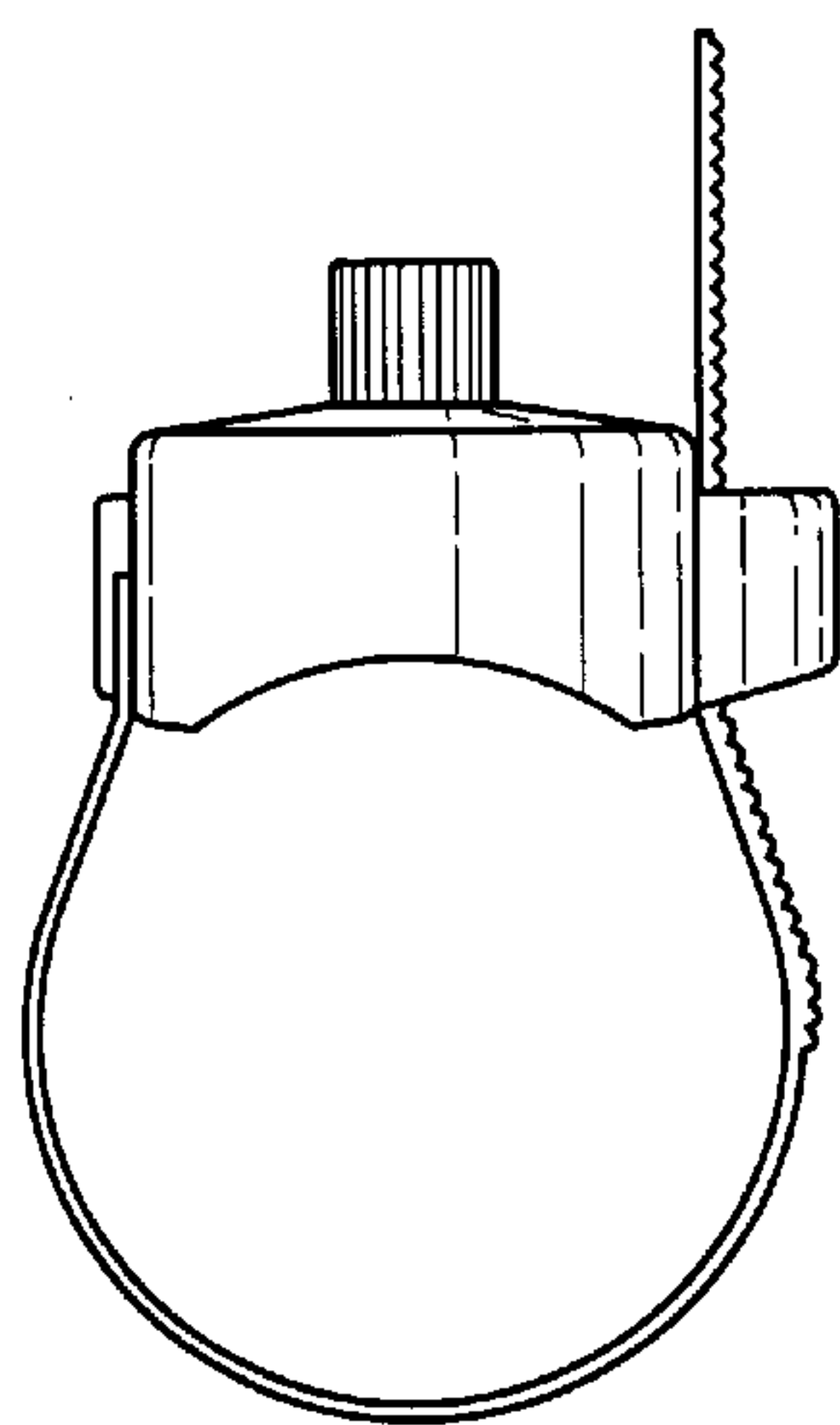


Fig. 1

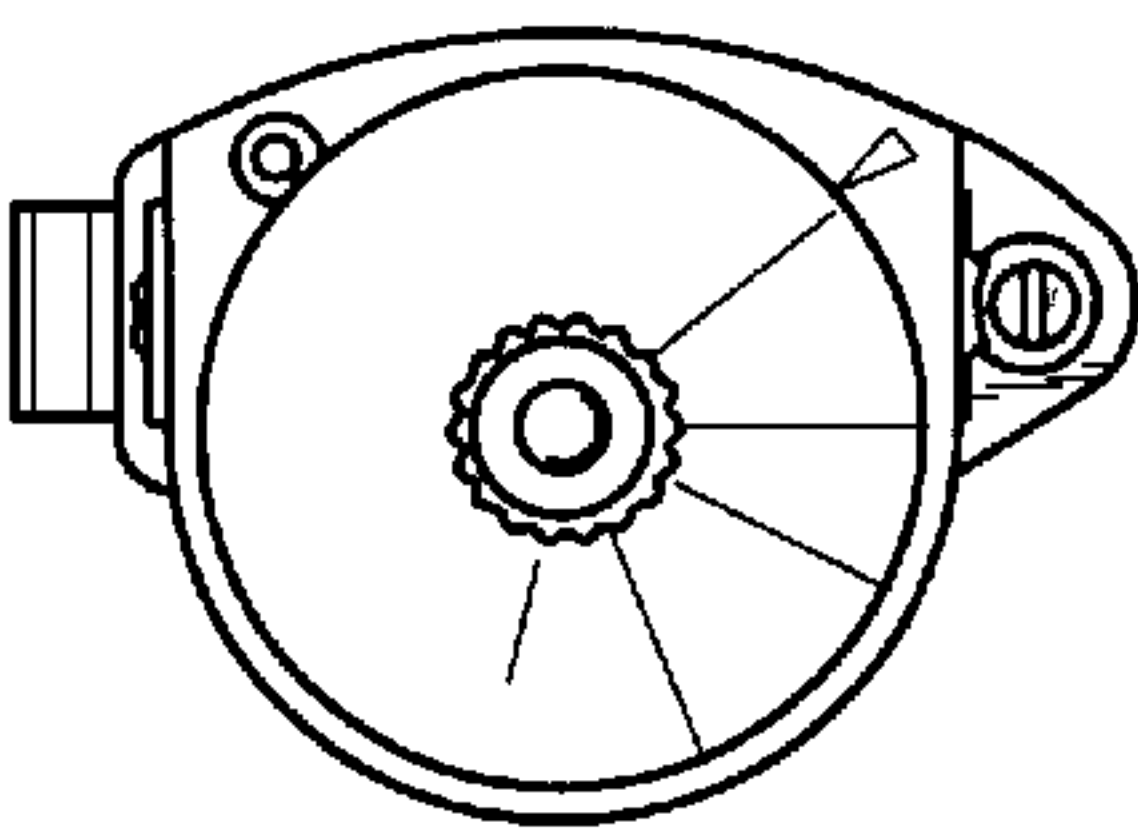


Fig. 2

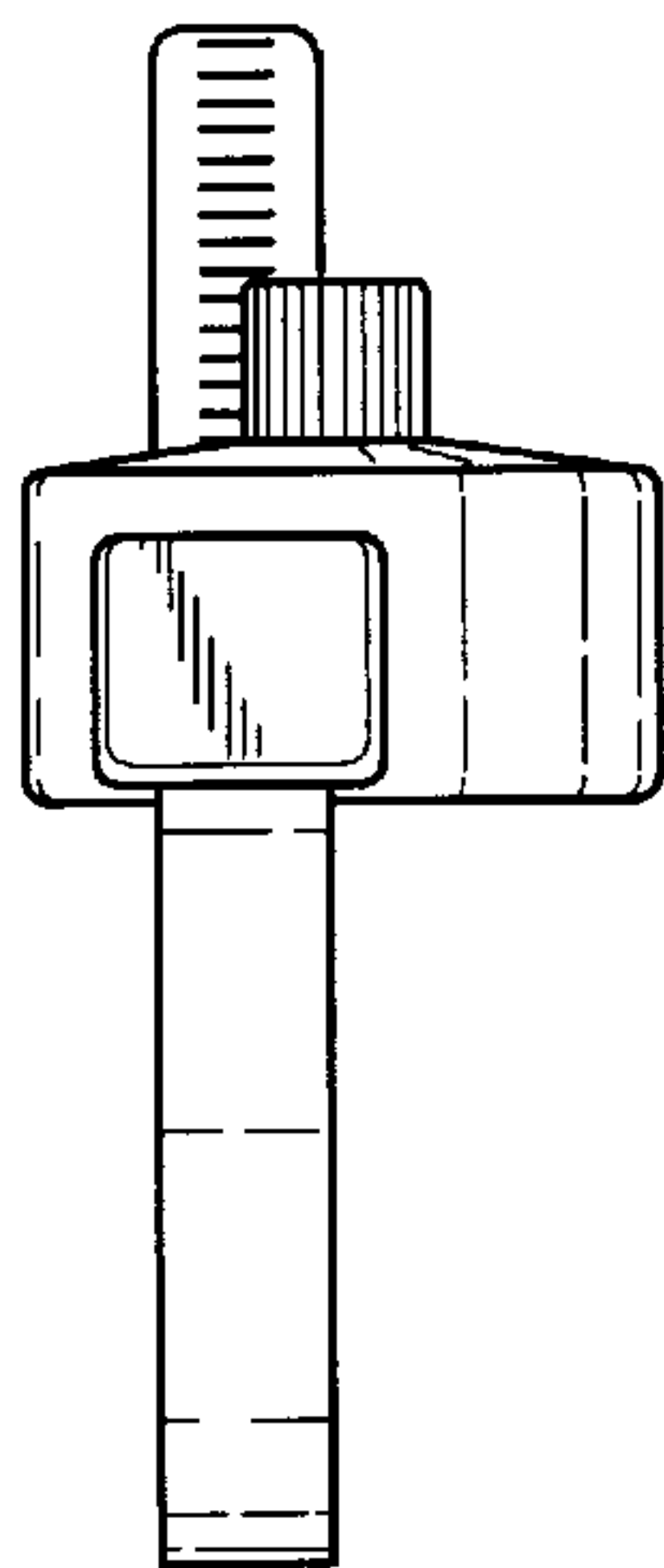


Fig. 3

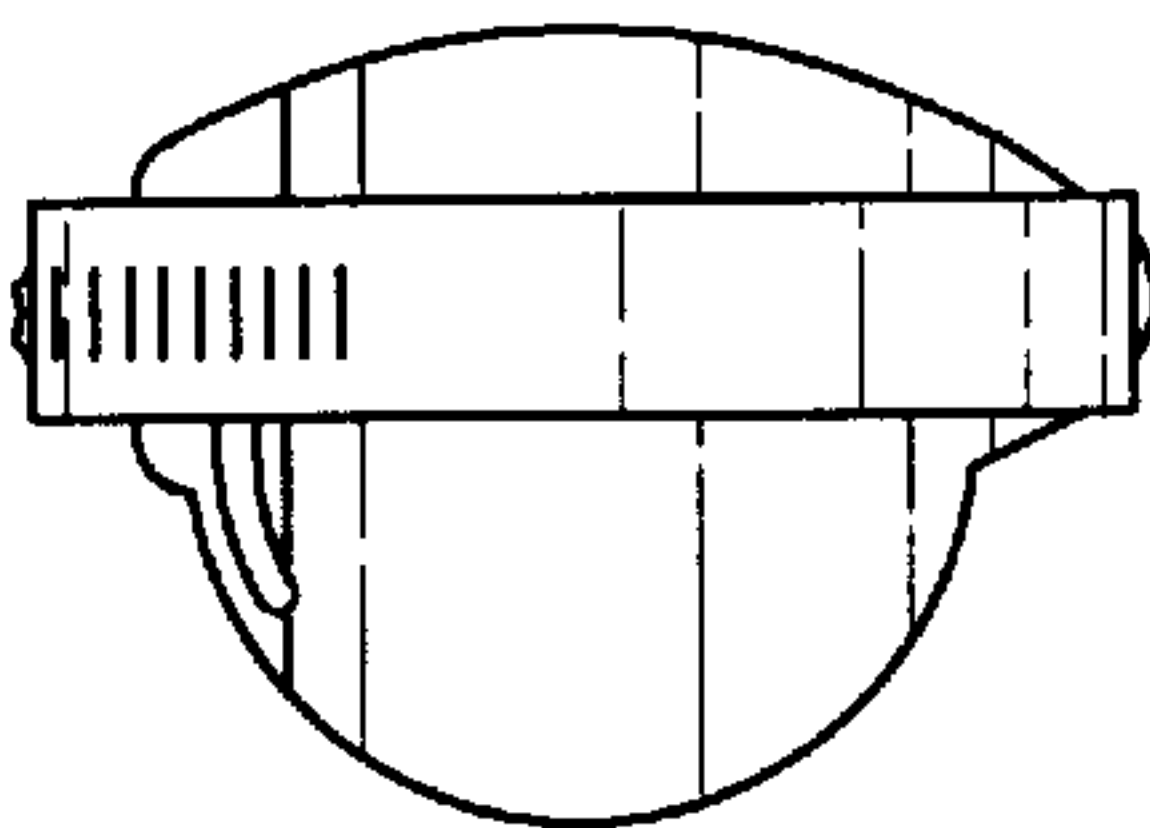


Fig. 4

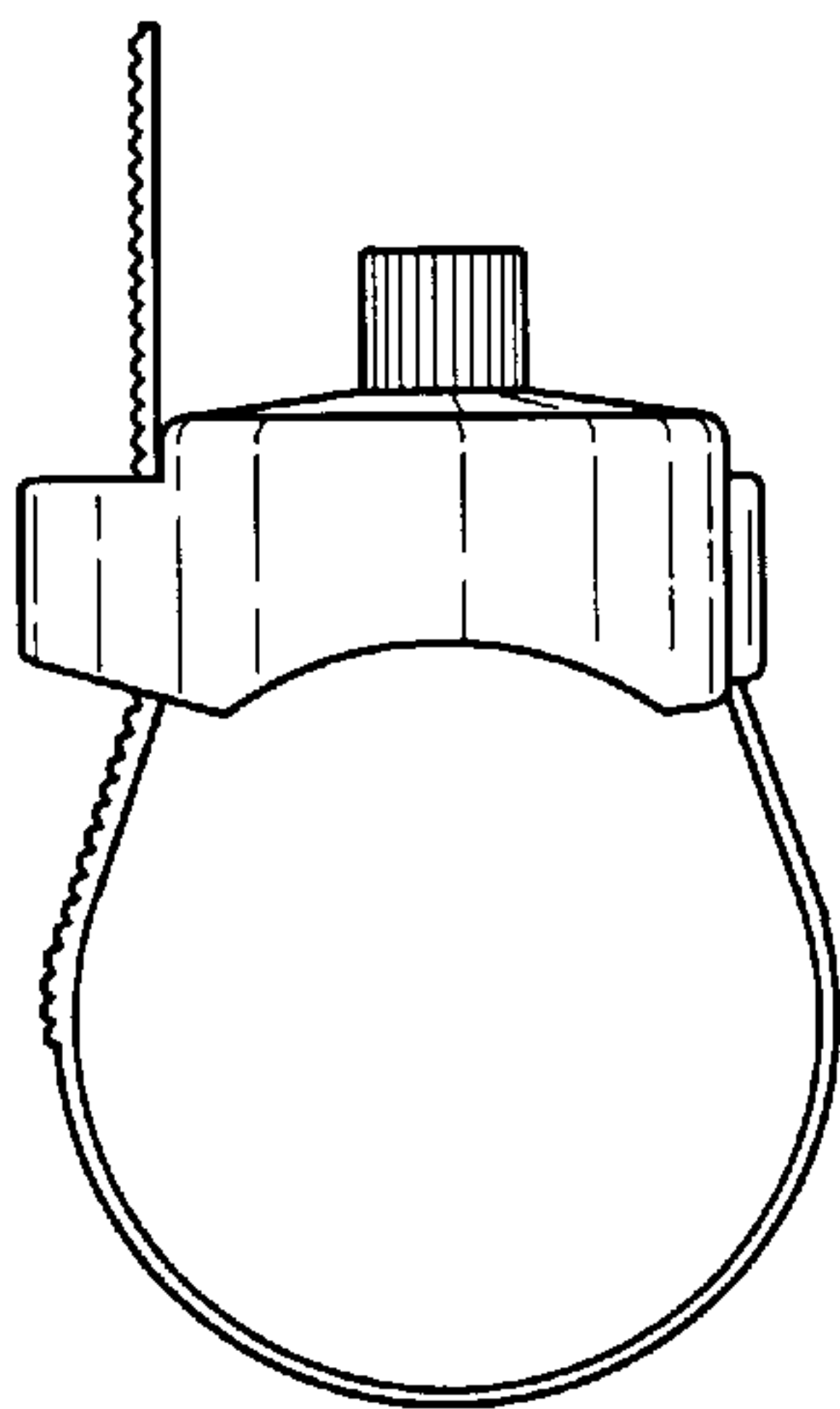


Fig. 5

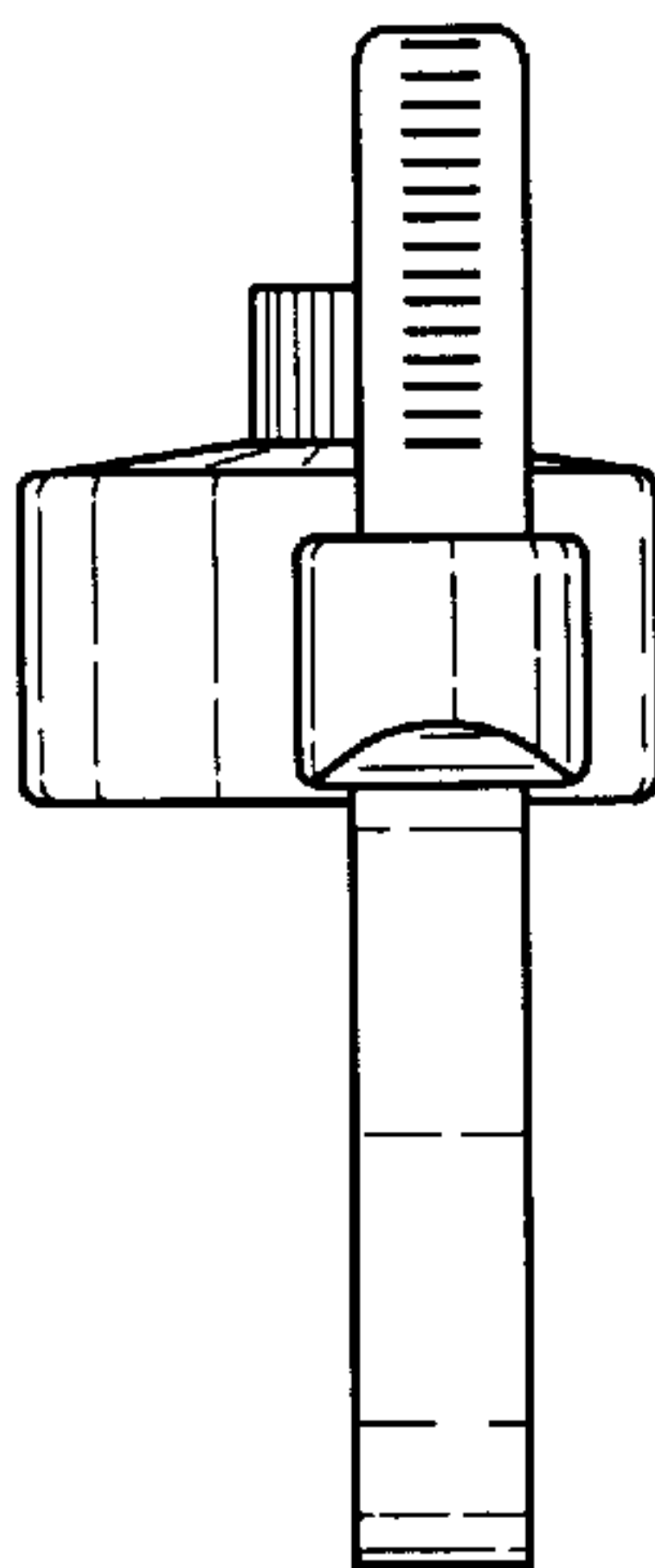


Fig. 7

