



US00D427167S

# United States Patent [19] Iwasaki

[11] **Patent Number: Des. 427,167**  
[45] **Date of Patent: \*\* Jun. 27, 2000**

[54] **ADAPTER FOR AN IC CARD**

5,986,891 11/1999 Sugimoto ..... 361/737

[75] Inventor: **Hiroshi Iwasaki**, Yokohama, Japan

### FOREIGN PATENT DOCUMENTS

923611 4/1995 Japan .

[73] Assignee: **Kabushiki Kaisha Toshiba**, Kawasaki, Japan

### OTHER PUBLICATIONS

[\*\*] Term: **14 Years**

Toshiba, pp. 1-32, "MOS Digital Integrated Circuit Silicon Gate CMOS 128Mbit (16M × 8 bit) CMOS NAND E<sup>2</sup>PROM (16M Byte SmartMedia™)", Nov. 6, 1998.

[21] Appl. No.: **29/107,611**

Toshiba, pp. 1-32, "MOS Digital Integrated Circuit Silicon Gate CMOS 64Mbit (8M × 8 bit) CMOS Nand E<sup>2</sup>PROM (8M Byte SmartMedia™)", Nov. 6, 1998.

[22] Filed: **Jul. 12, 1999**

### [30] Foreign Application Priority Data

Jan. 22, 1999 [JP] Japan ..... 11-1433

*Primary Examiner*—M. H. Tung

[51] **LOC (7) Cl.** ..... **14-02**

*Attorney, Agent, or Firm*—Oblon, Spivak, McClelland, Maier & Neustadt, P.C.

[52] **U.S. Cl.** ..... **D14/117**

[58] **Field of Search** ..... D14/114, 117;  
D13/182; 235/441-3, 487-92, 486; 361/737,  
686; 439/76.1

### [57] CLAIM

The ornamental design for an adapter for an IC card, as shown.

### [56] References Cited

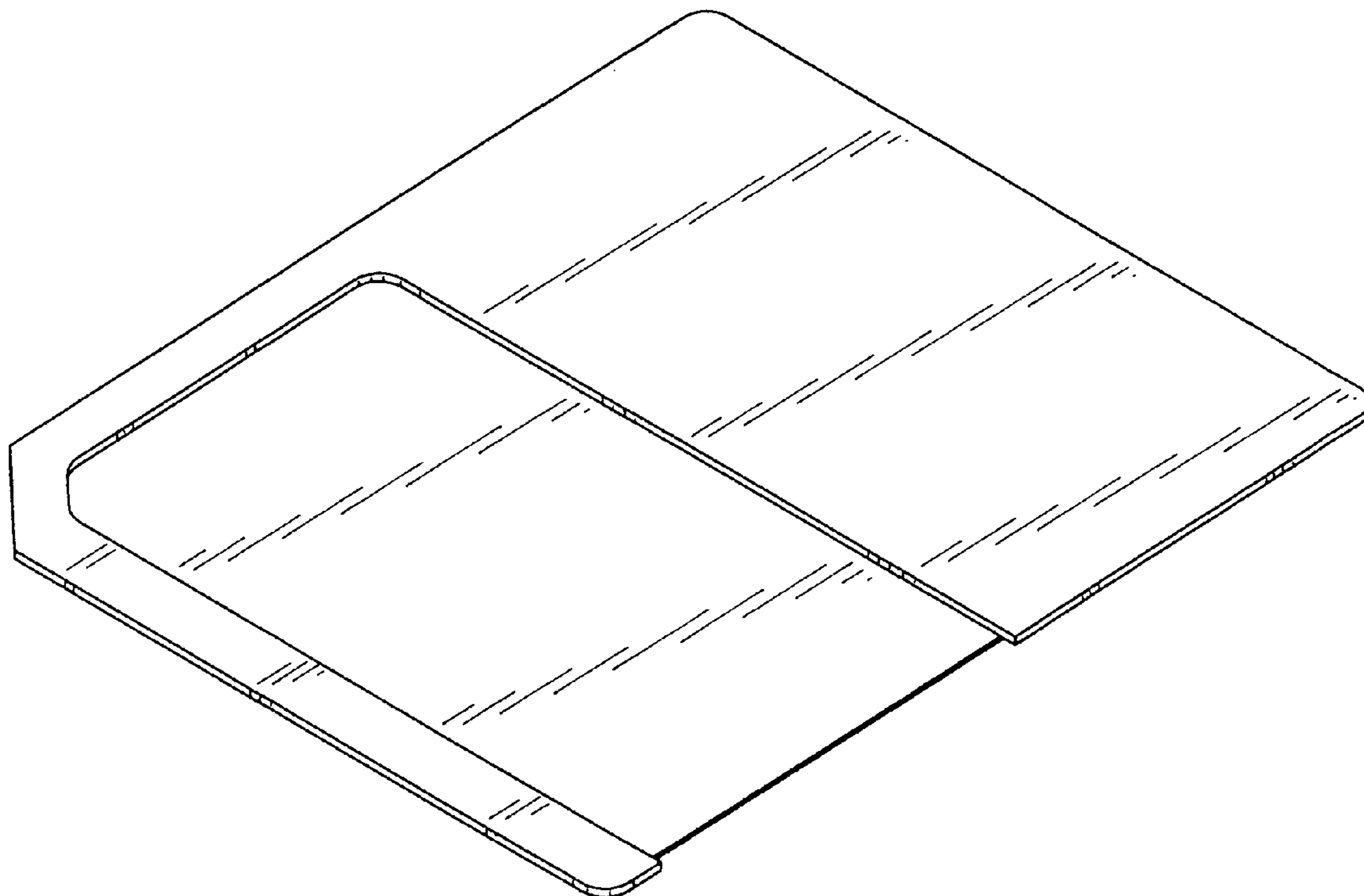
### DESCRIPTION

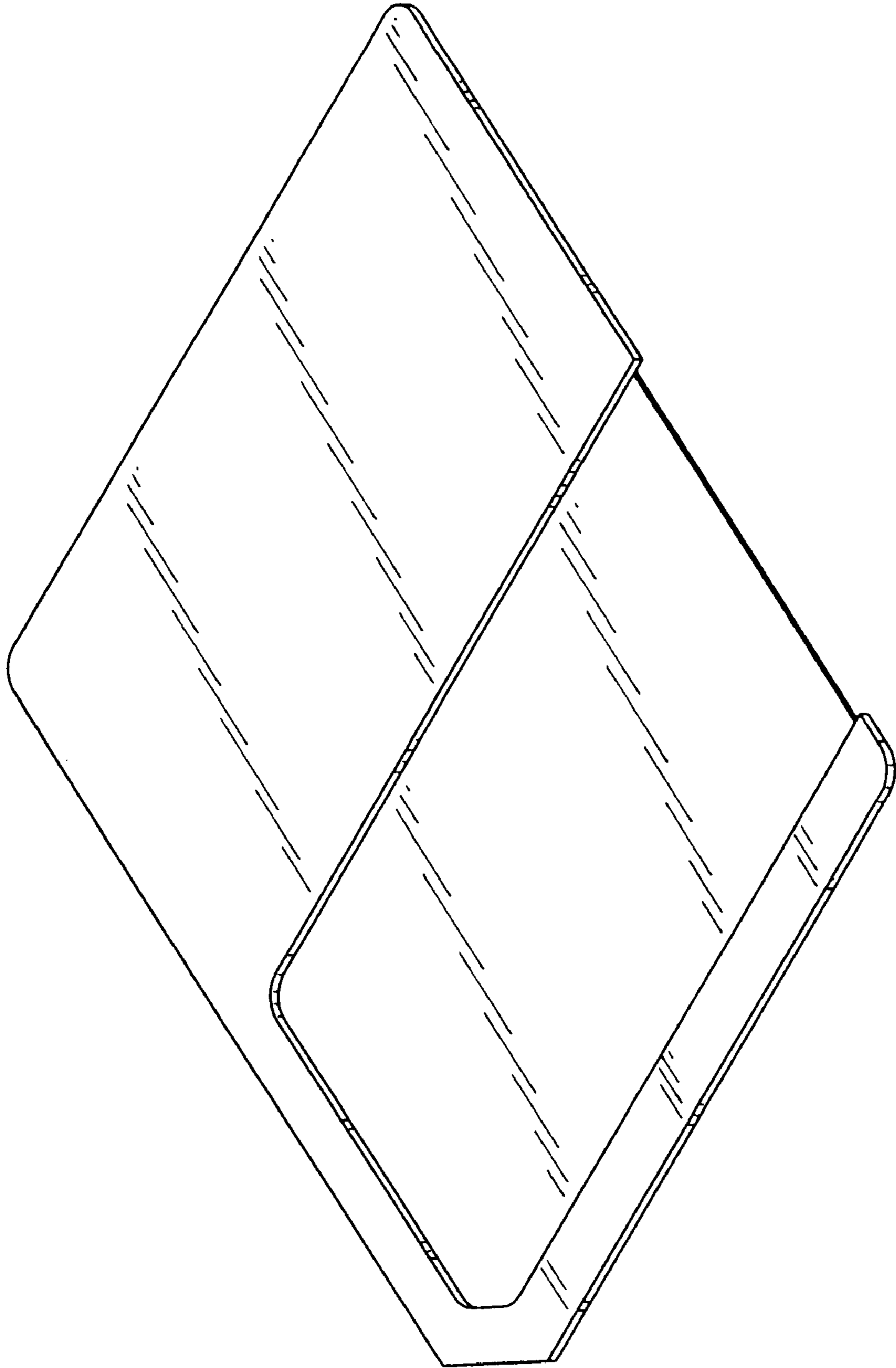
#### U.S. PATENT DOCUMENTS

D. 267,009	11/1982	Doi	.....	D14/117
D. 305,886	2/1990	Banjo et al.	.....	D14/117
D. 330,022	10/1992	Chen	.....	D14/117
D. 356,787	3/1995	Miyazaki	.....	D14/114
D. 358,586	5/1995	Miyazaki et al.	.....	D14/114
D. 375,303	11/1996	Gaumet	.....	D14/117
D. 407,392	3/1999	Kleineidam	.....	D14/117
D. 410,913	6/1999	Saito et al.	.....	D14/114
5,184,282	2/1993	Kaneda et al.	..	
5,846,092	12/1998	Feldman et al.	.....	439/76.1

FIG. 1 is a front, bottom and right side perspective view of an adapter for an IC card, showing my new design;  
FIG. 2 is a top plan view thereof;  
FIG. 3 is a right side elevational view thereof;  
FIG. 4 is a front elevational view thereof;  
FIG. 5 is a bottom plan view thereof;  
FIG. 6 is a left side elevational view thereof; and,  
FIG. 7 is a rear elevational view thereof.

**1 Claim, 2 Drawing Sheets**

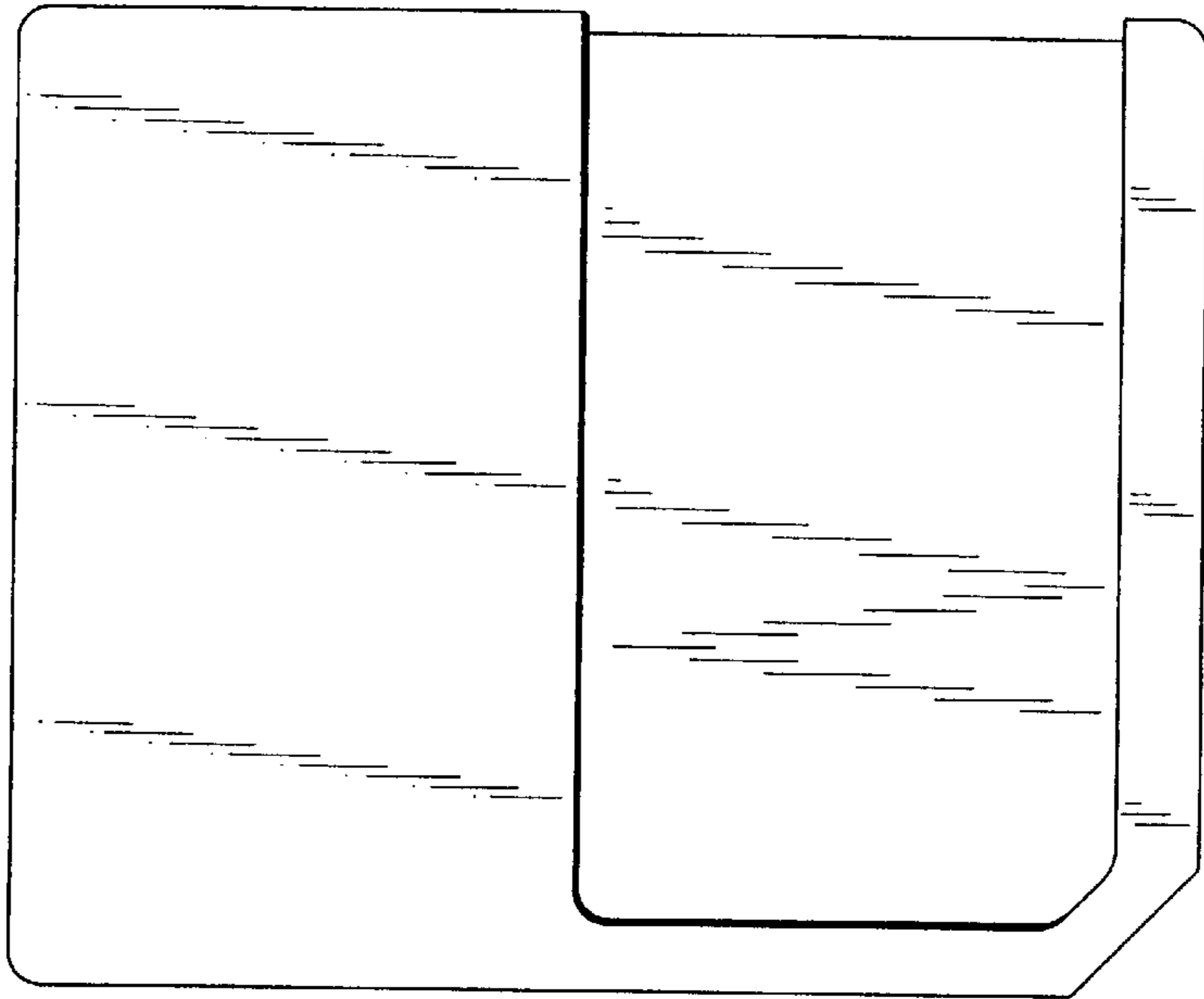




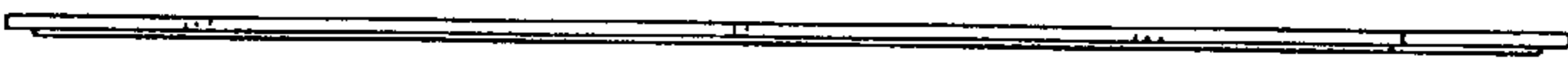
*Fig. 1*



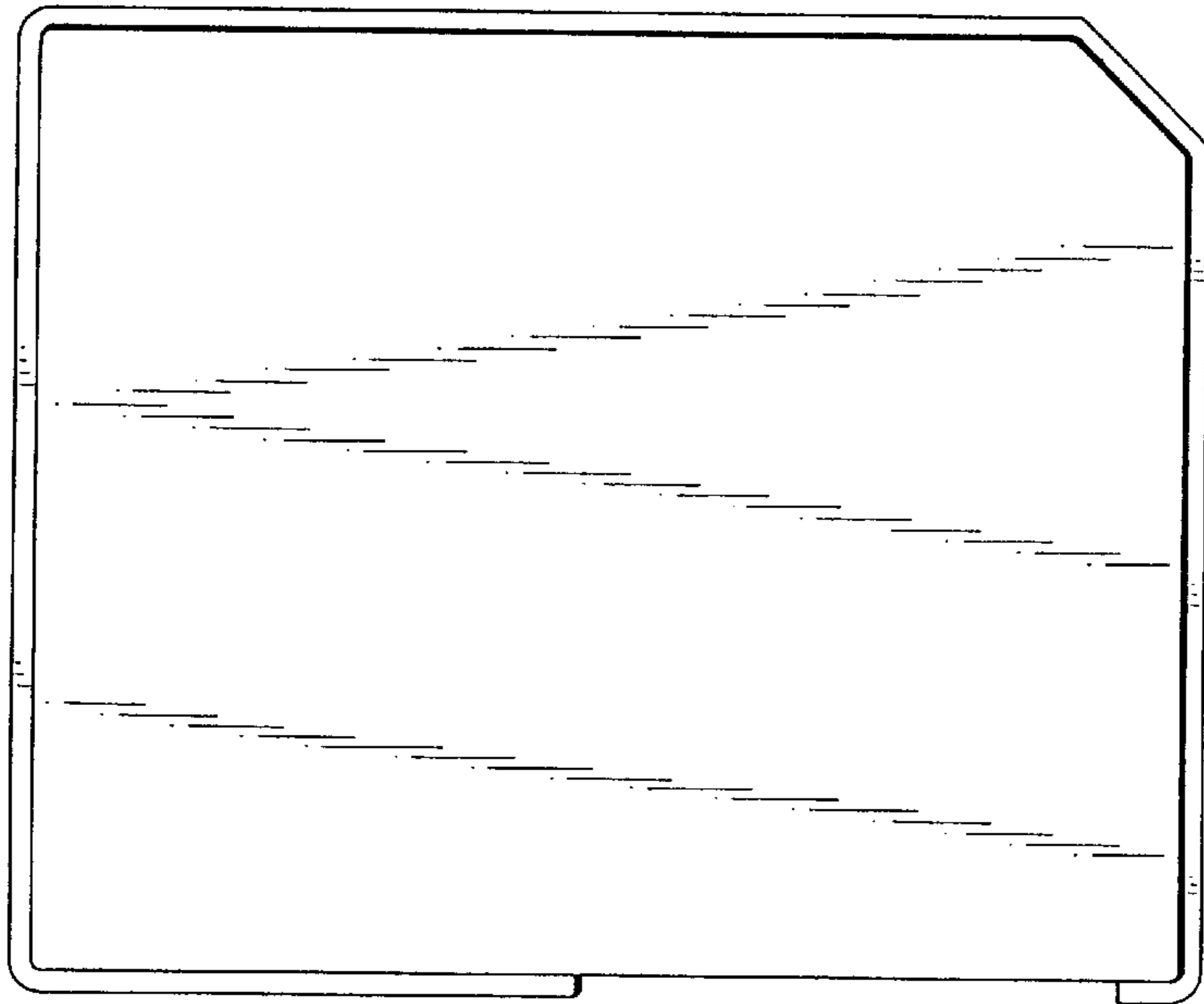
*Fig. 2*



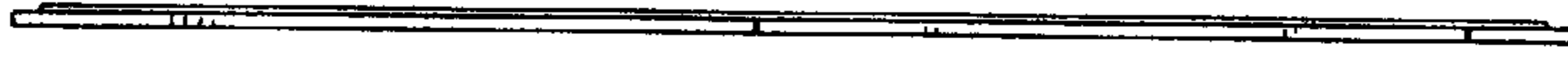
*Fig. 4*



*Fig. 6*



*Fig. 7*



*Fig. 3*



*Fig. 5*