



US00D427162S

# United States Patent [19]

Nagase

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[54] ARITHMETIC OPERATION CONTROL MACHINE FOR AN ELECTRONIC COMPUTER

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[\*\*] Term: 14 Years

[21] Appl. No.: 29/107,801

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### [30] Foreign Application Priority Data

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[51] LOC (7) Cl. .... 14-02

[52] U.S. Cl. .... D14/107

[58] Field of Search ..... D14/100, 102, D14/107-109; D6/432, 436, 445, 448; D13/162, 184, 199; 312/223.3; 360/99.12; 369/34, 36; 361/690-696

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Primary Examiner—Freda Nunn  
Attorney, Agent, or Firm—Oblon, Spivak, McClelland, Maier & Neustadt, P.C.

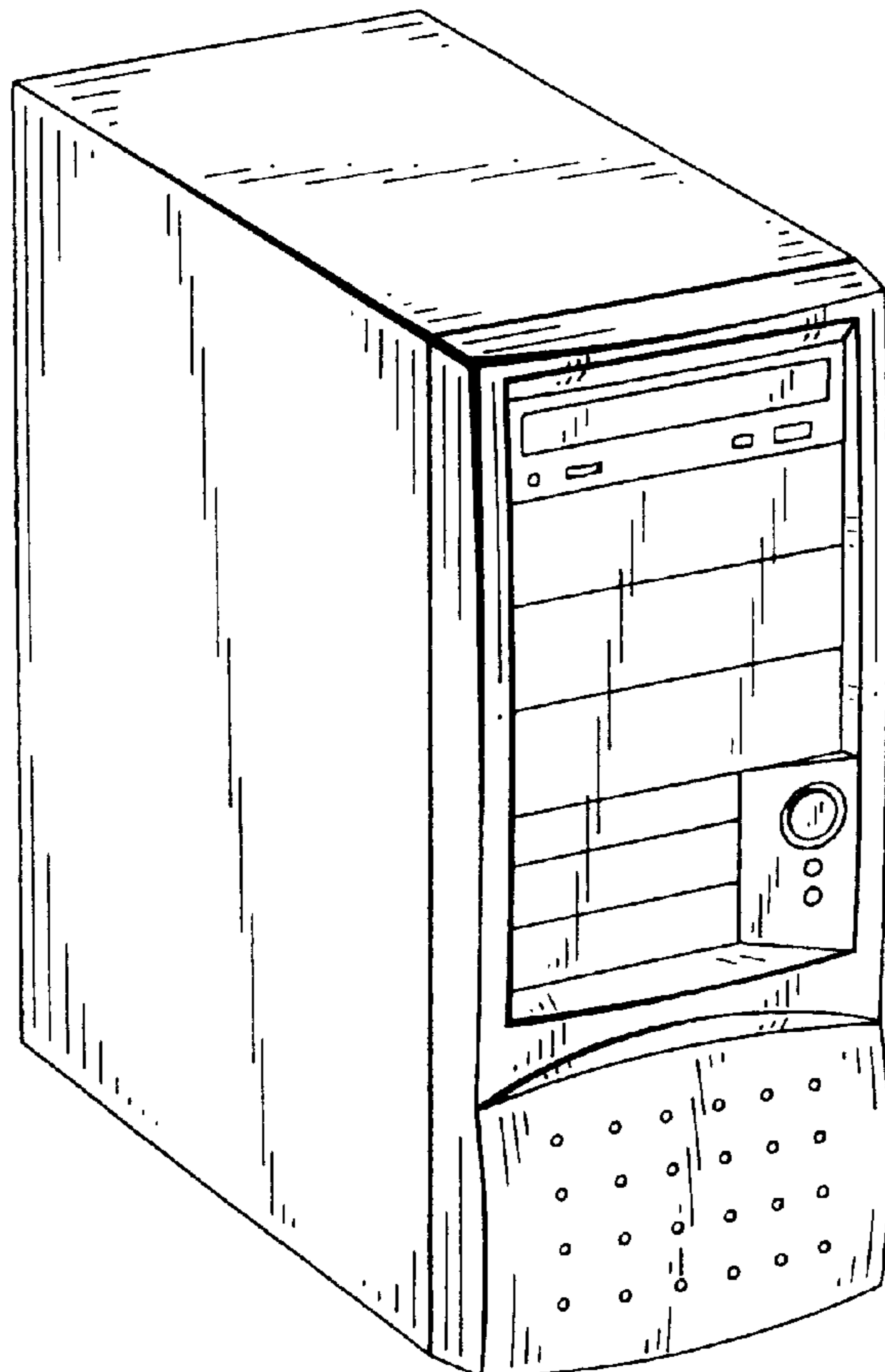
### [57] CLAIM

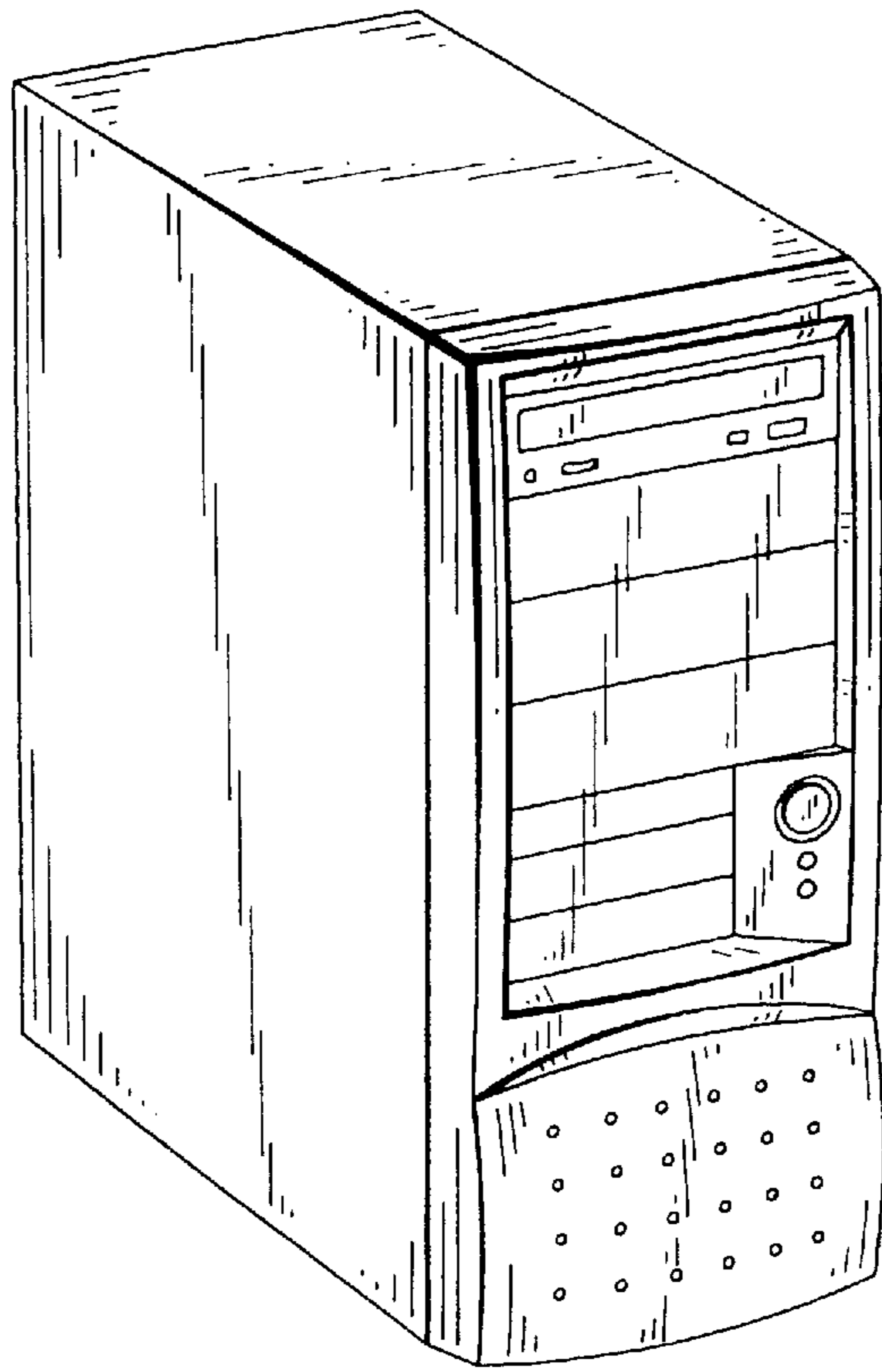
The ornamental design for an arithmetic operation control machine for an electronic computer, as shown and described.

#### DESCRIPTION

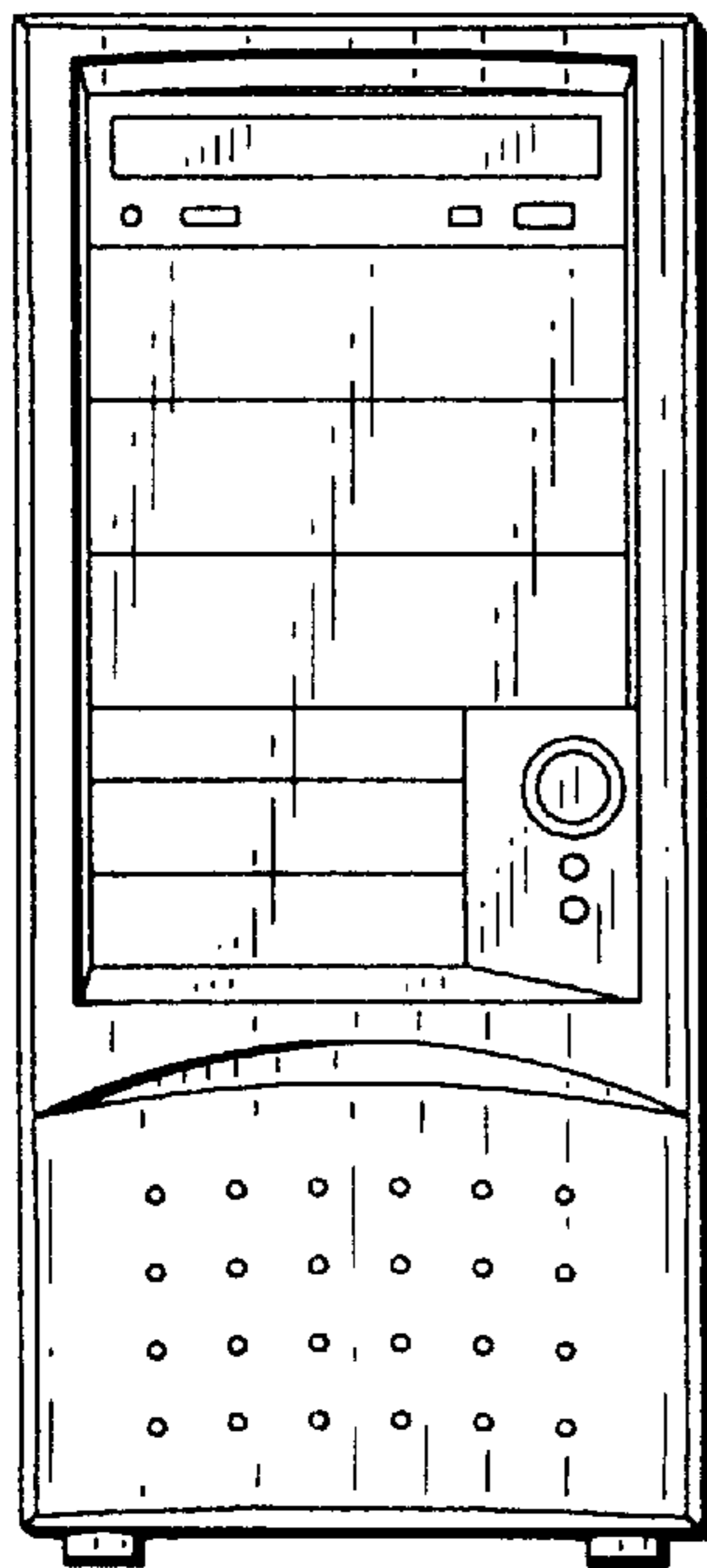
FIG. 1 is a front, top and left side perspective view of an arithmetic operation control machine for an electronic computer, showing my new design;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a right side elevational view thereof the opposite side being a mirror image thereof;  
FIG. 4 is a top plan view thereof;  
FIG. 5 is a bottom plan view thereof; and,  
FIG. 6 is a rear elevational view thereof.

1 Claim, 2 Drawing Sheets

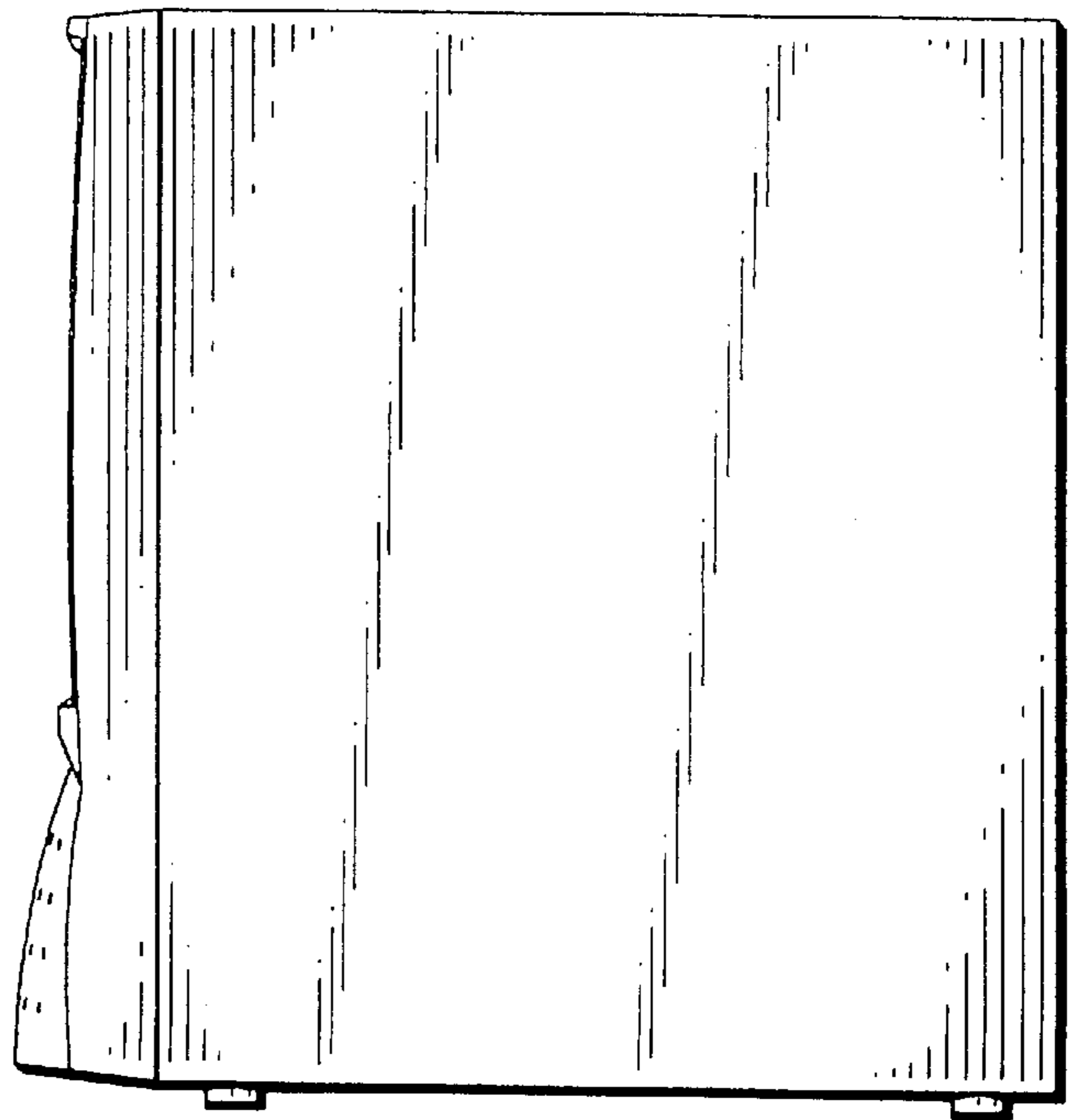




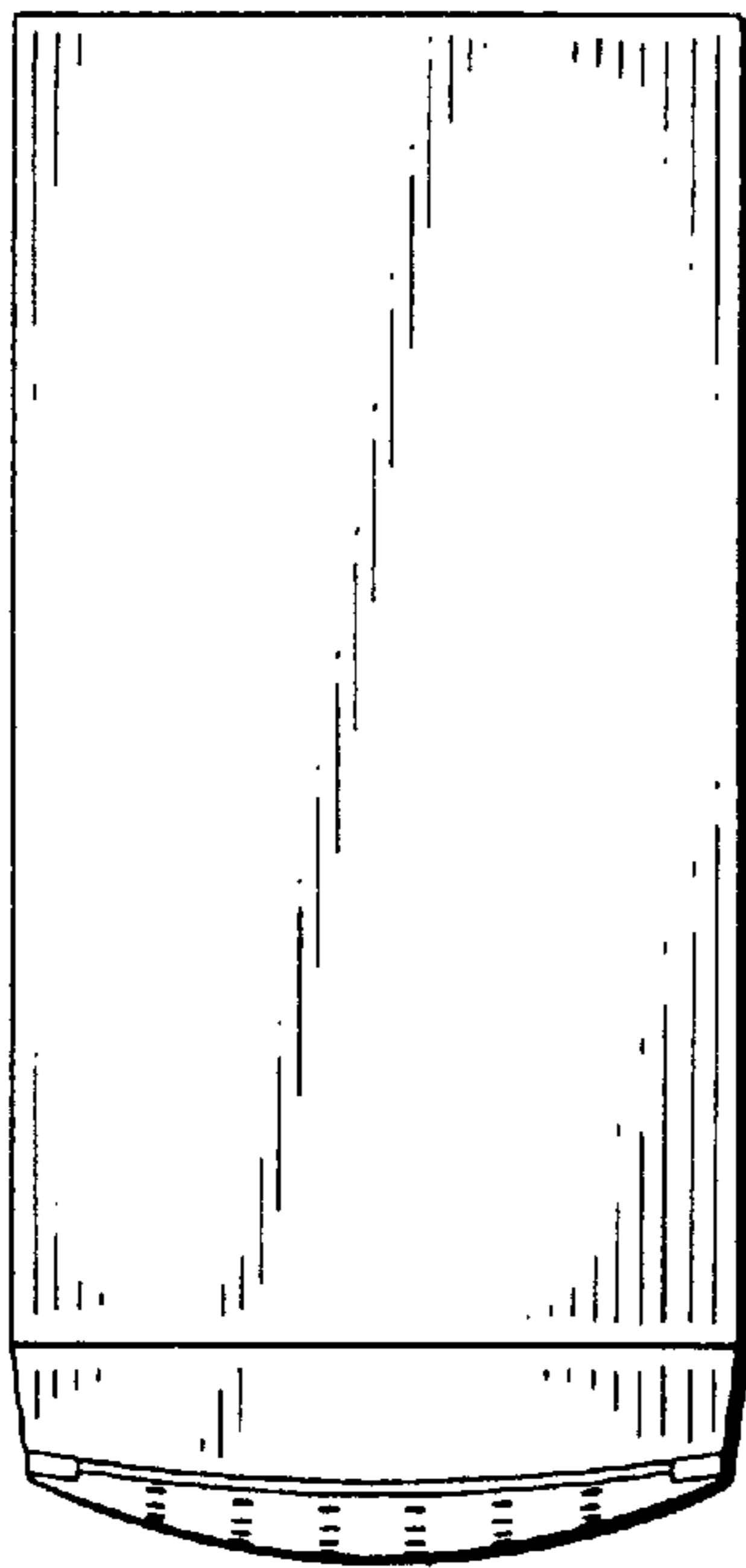
*Fig. 1*



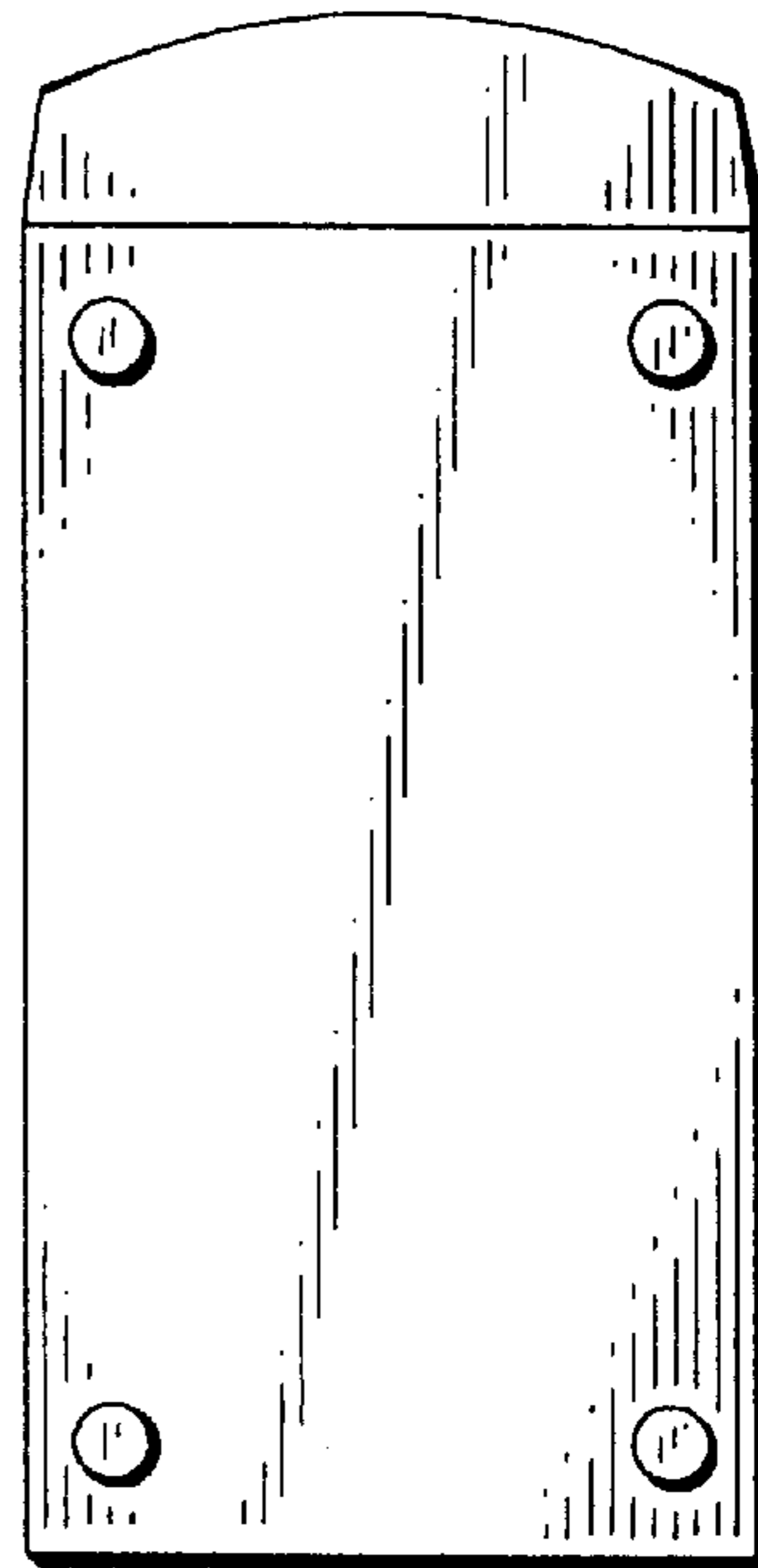
*Fig. 2*



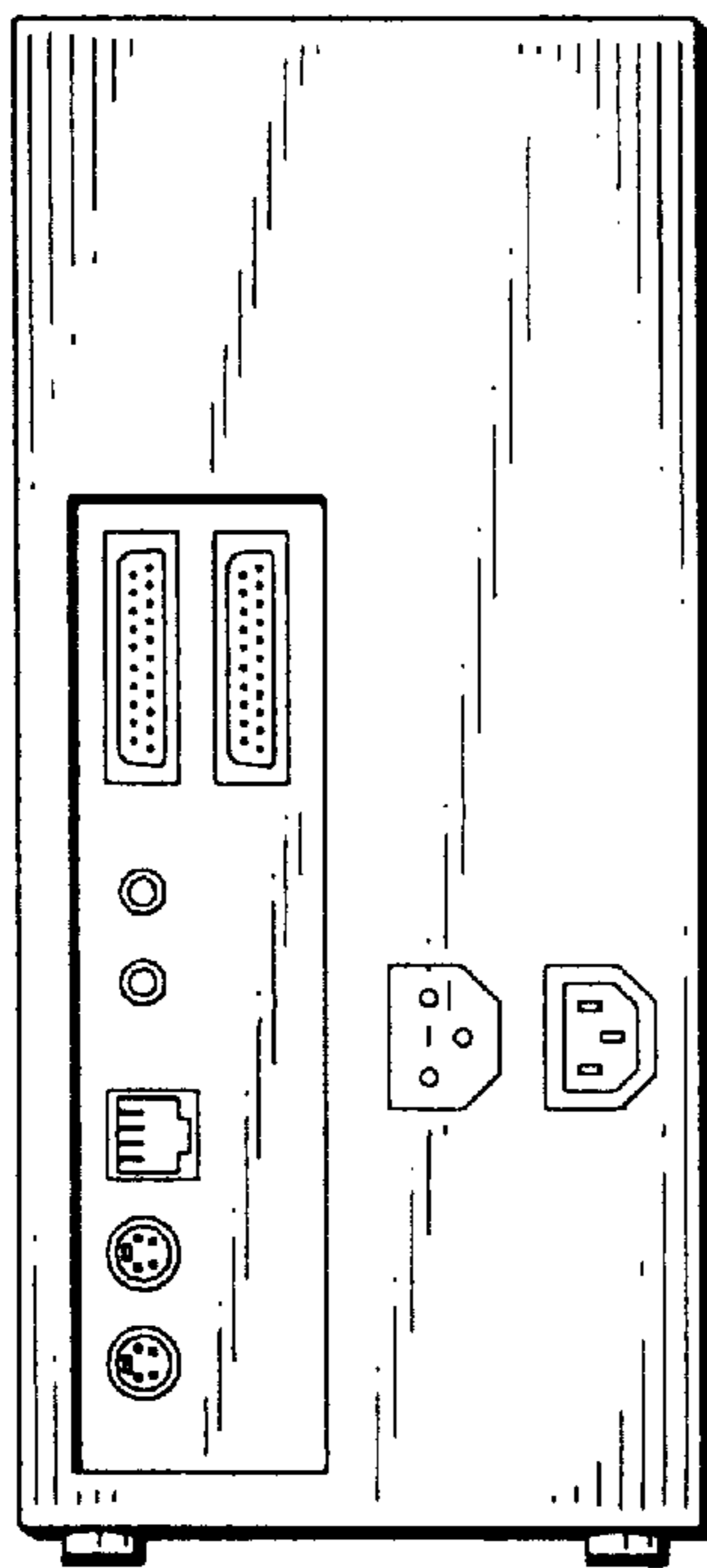
*Fig. 3*



*Fig. 4*



*Fig. 5*



*Fig. 6*