



US00D426223S

**United States Patent** [19]  
**Nomura**

[11] **Patent Number: Des. 426,223**  
[45] **Date of Patent: \*\* Jun. 6, 2000**

[54] **IC RECORDER**

D. 351,173 10/1994 Yeh ..... D14/218  
D. 379,009 4/1997 Osiecki ..... D14/167  
D. 387,783 12/1997 Mariotta ..... D14/218 X

[75] Inventor: **Yasuhiro Nomura**, Suwa, Japan

[73] Assignee: **Seiko Epson Corporation**, Tokyo, Japan

*Primary Examiner*—Nanda Bondade  
*Attorney, Agent, or Firm*—Oliff & Berridge, PLC

[\*\*] Term: **14 Years**

[57] **CLAIM**

[21] Appl. No.: **29/095,859**

The ornamental design for IC recorder, as shown and described.

[22] Filed: **Oct. 30, 1998**

[30] **Foreign Application Priority Data**

**DESCRIPTION**

Apr. 30, 1998 [JP] Japan ..... 10-12708

FIG. 1 is a front view of an IC recorder of my inventive design;

[51] **LOC (7) Cl.** ..... **14-01**

FIG. 2 is a top view thereof;

[52] **U.S. Cl.** ..... **D14/167**

FIG. 3 is a bottom view thereof;

[58] **Field of Search** ..... D14/124, 160-165,  
D14/167-168, 188-189, 192-198, 217-218,  
260, 299; 455/344, 347-350

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a rear view thereof; and,

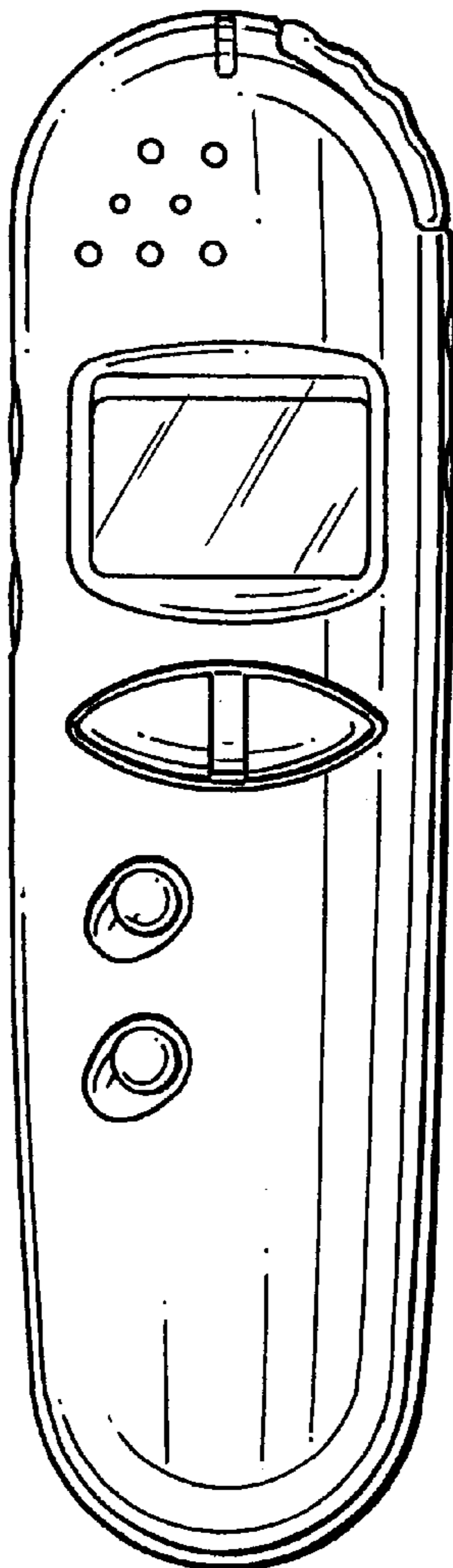
FIG. 7 is a perspective view thereof.

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

D. 338,203 8/1993 Yuen et al. .... D14/218

**1 Claim, 5 Drawing Sheets**



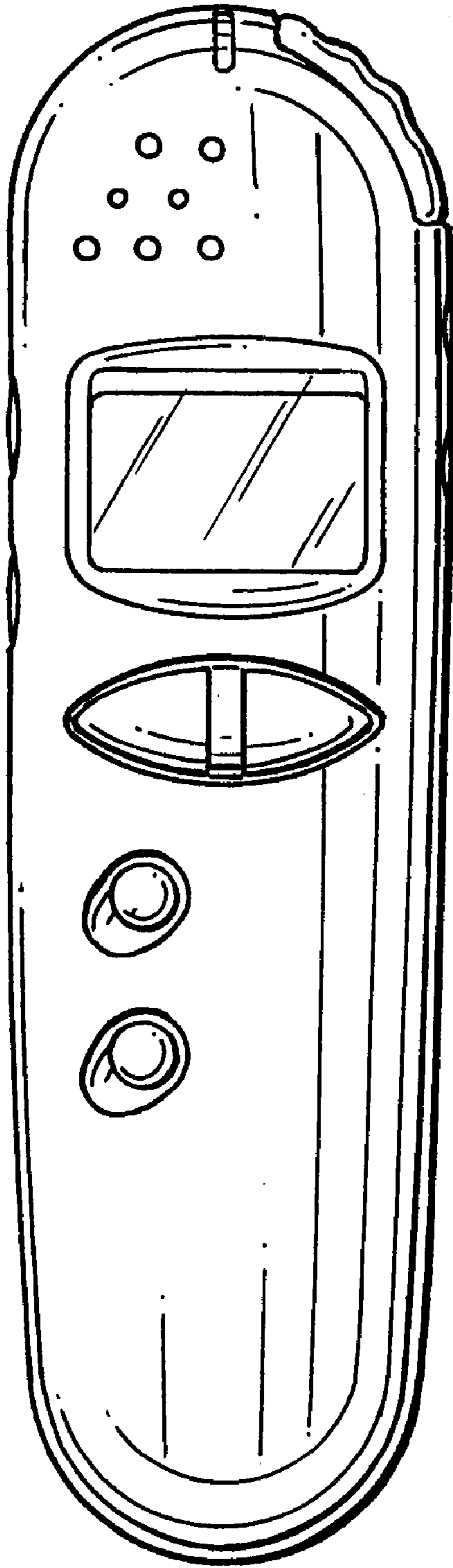


FIG. 1

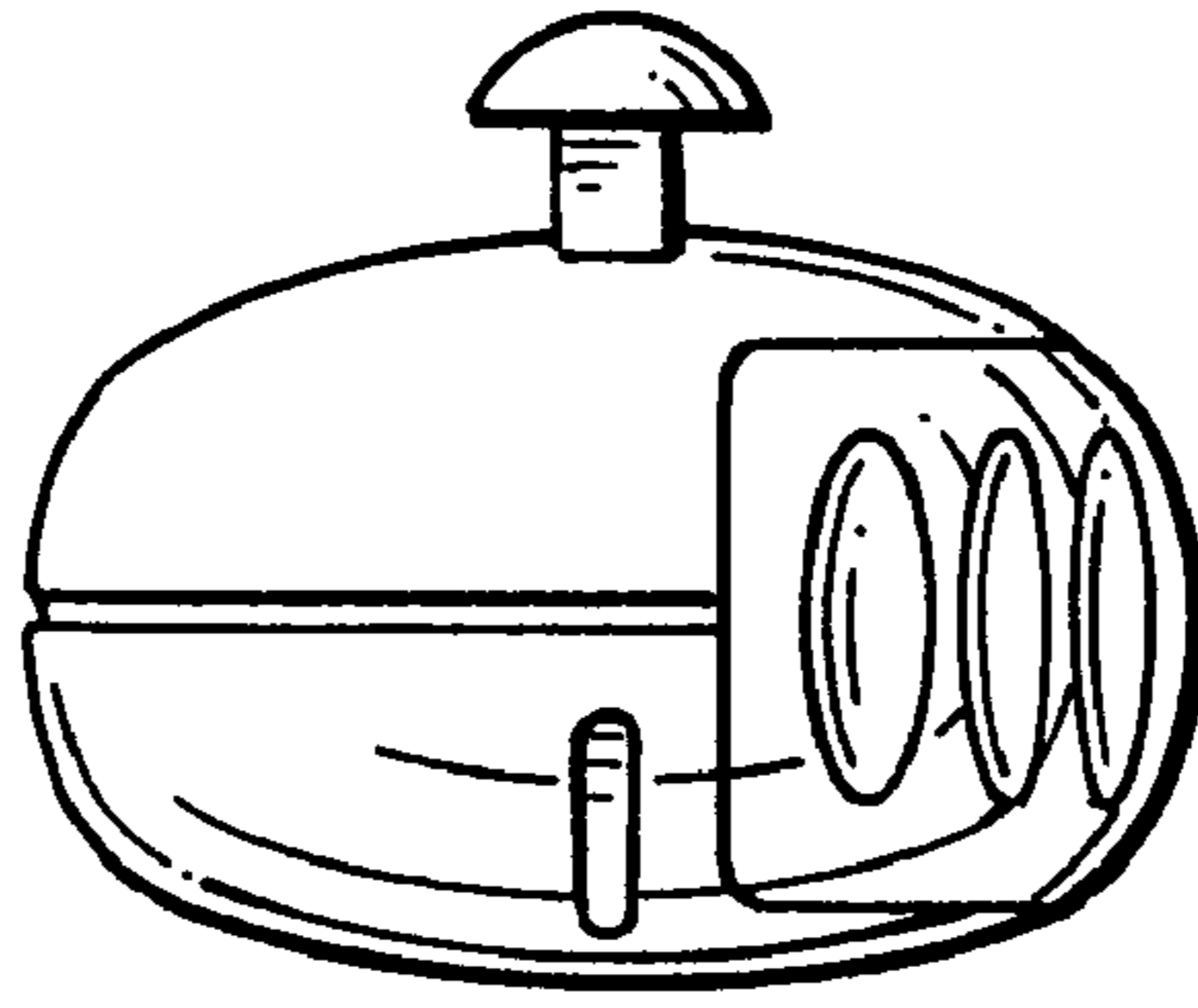


FIG. 2

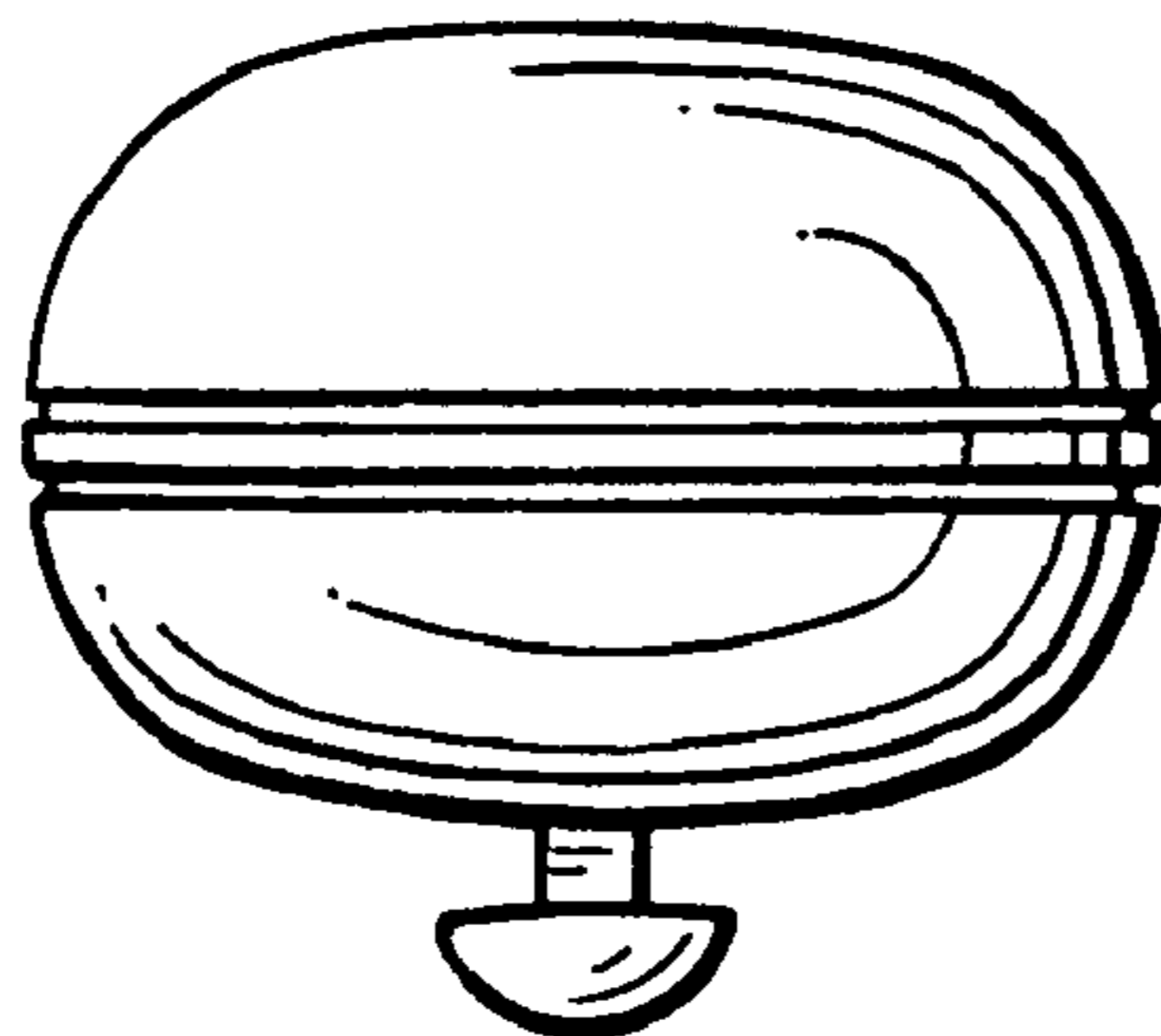


FIG. 3

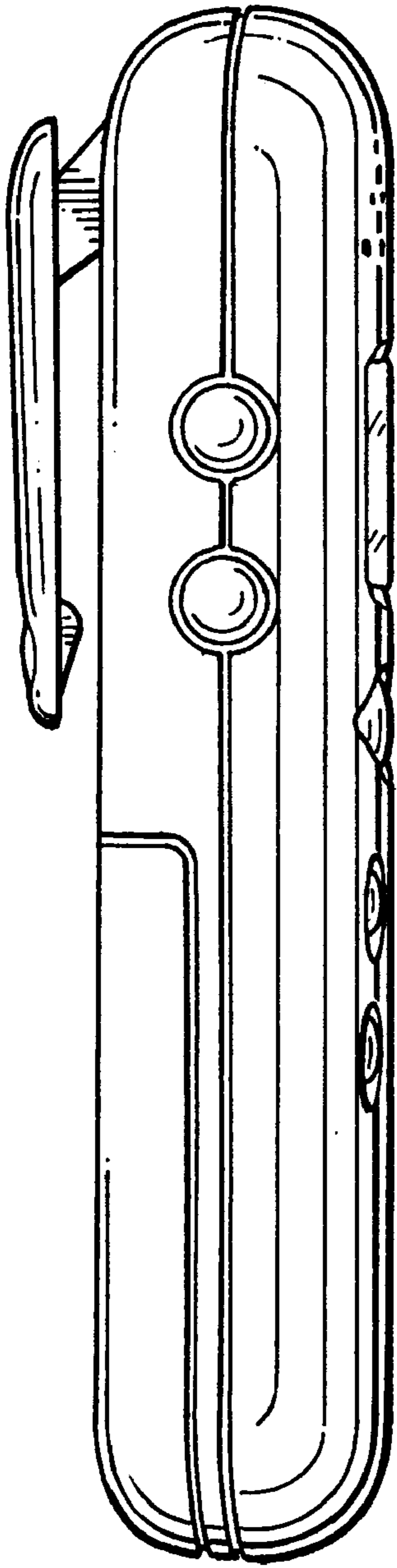


FIG. 4

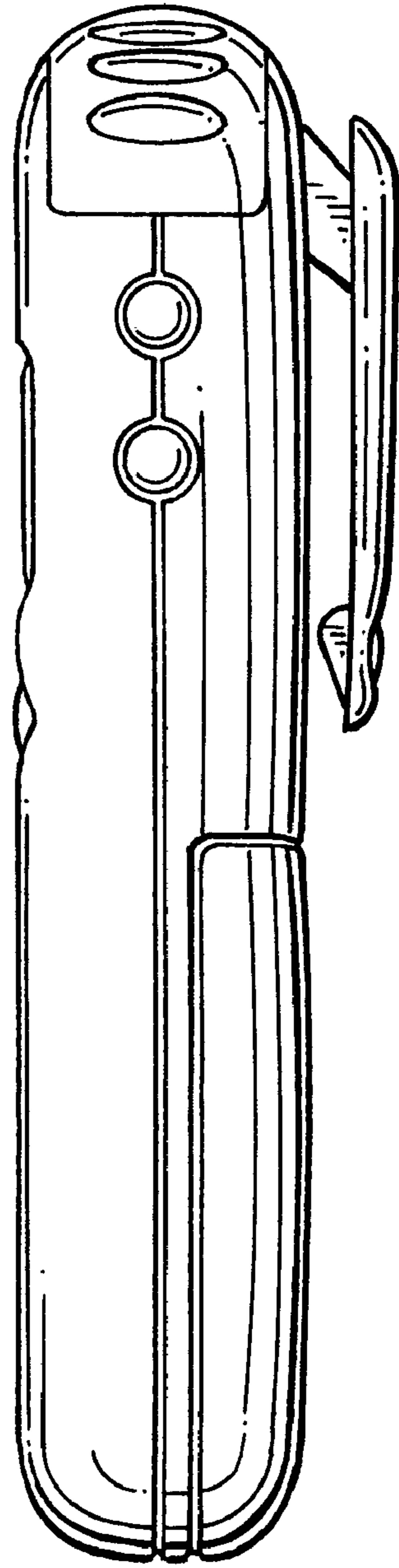


FIG. 5

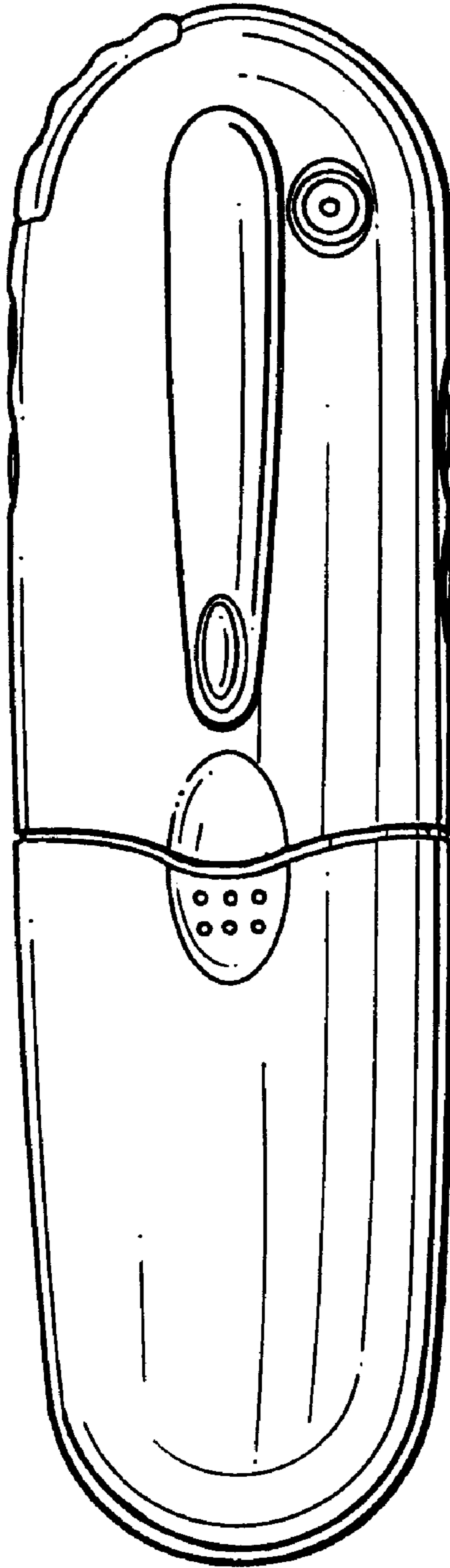


FIG. 6

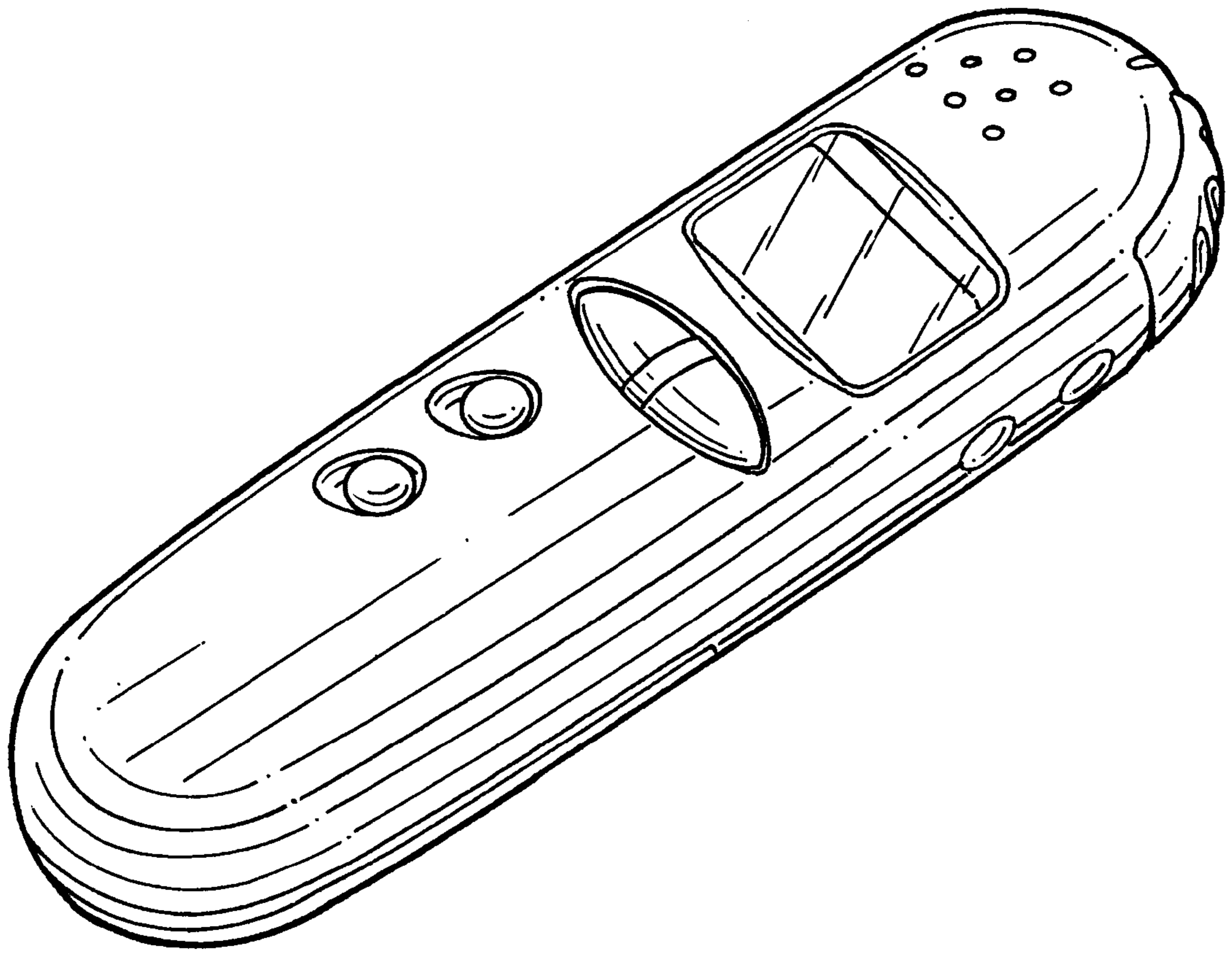


FIG. 7