



US00D421598S

United States Patent [19]

Berusha et al.

[11] Patent Number: **Des. 421,598**

[45] Date of Patent: **** Mar. 14, 2000**

[54] **ELECTRONICS ENCLOSURE**

[75] Inventors: **Michael S. Berusha**, Lake in the Hills;
Michael R. Lenz, Cary, both of Ill.

[73] Assignee: **Motorola, Inc.**, Schaumburg, Ill.

[**] Term: **14 Years**

[21] Appl. No.: **29/053,350**

[22] Filed: **Apr. 8, 1996**

Related U.S. Application Data

[63] Continuation-in-part of application No. 29/040,969, Jun. 30, 1995, Pat. No. Des. 377,339.

[51] **LOC (6) Cl.** **30-27**

[52] **U.S. Cl.** **D13/184**

[58] **Field of Search** D13/184; D10/75,
D10/78; D14/125, 158, 239, 242; 73/431;
312/7.1; 220/3.18, 281, 326, 345; 324/136;
348/10

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 335,129 4/1993 Theisen et al. D14/158
5,497,893 3/1996 Mangone, Jr. 220/3.8

Primary Examiner—Antoine D. Davis
Attorney, Agent, or Firm—Hugh C. Dunlop

[57] **CLAIM**

The ornamental design for an electronics enclosure, as shown and described.

DESCRIPTION

FIG. 1 is a top, right side, and front perspective view of an electronics enclosure showing our new design; FIG. 2 is a left elevational view of an electronics enclosure showing our new design; FIG. 3 is a rear elevation view of an electronics enclosure showing our new design; and, FIG. 4 is a bottom plan view of an electronics enclosure showing our new design.

1 Claim, 2 Drawing Sheets

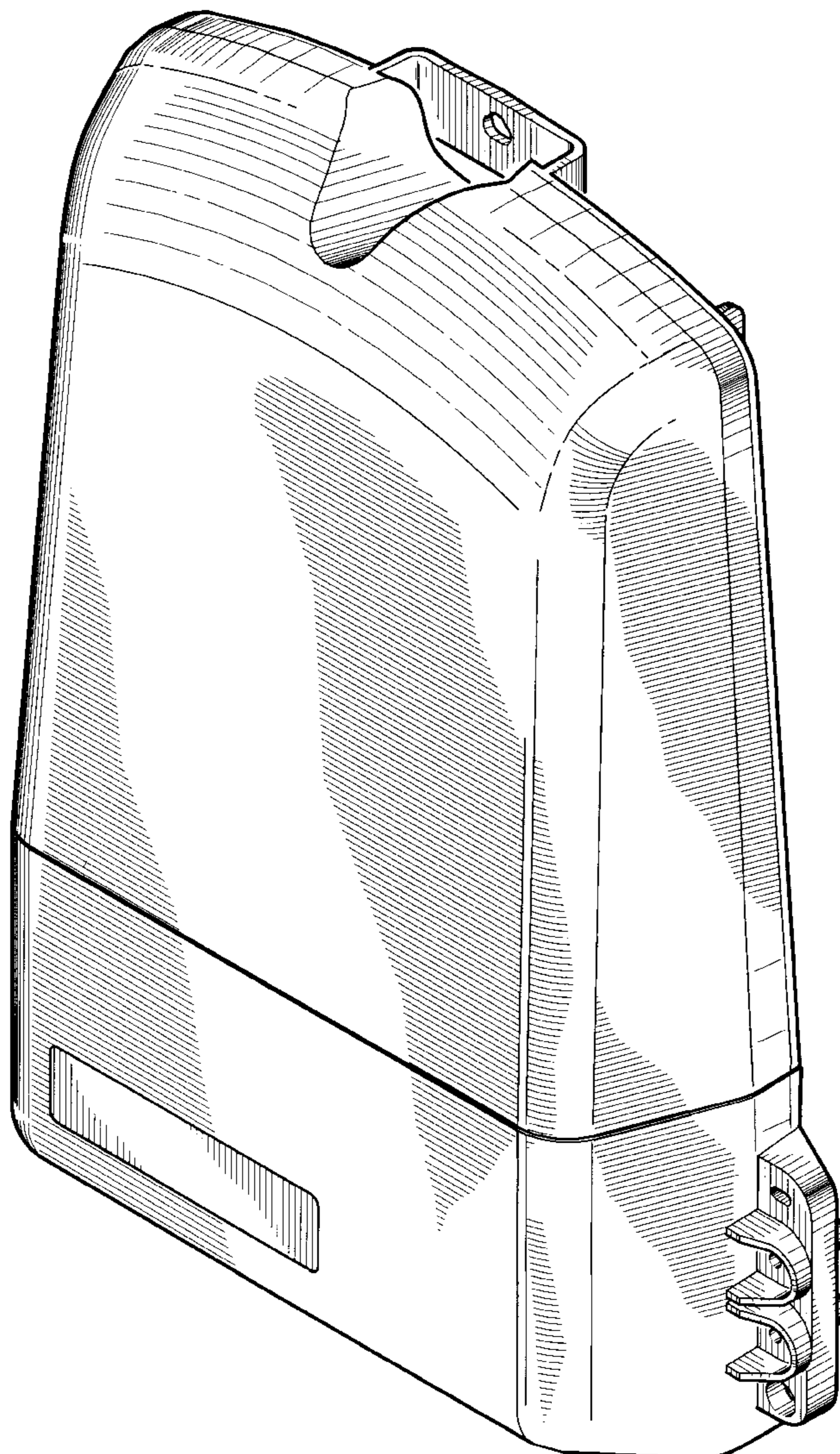


FIG. 1

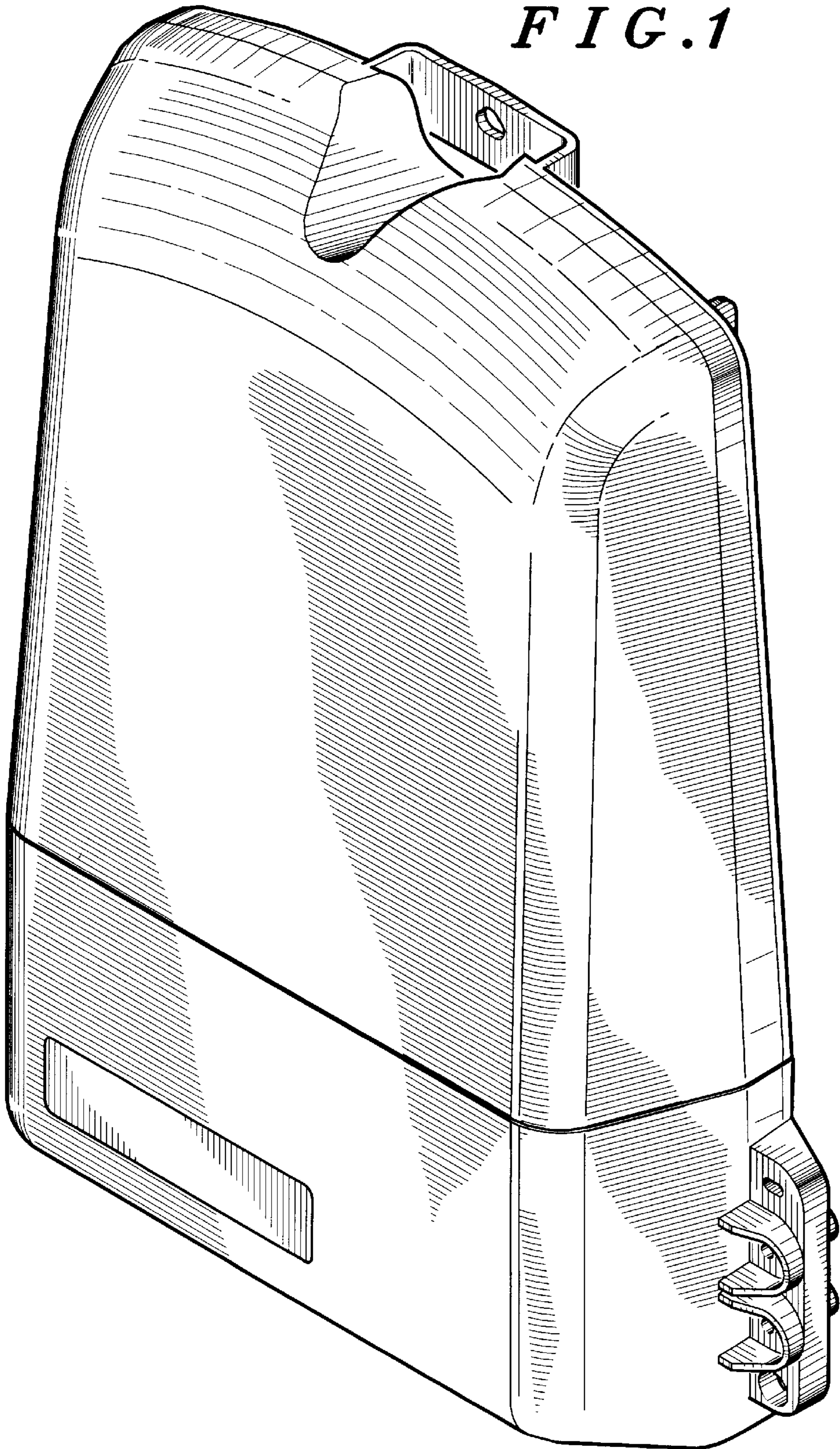


FIG. 3

FIG. 2

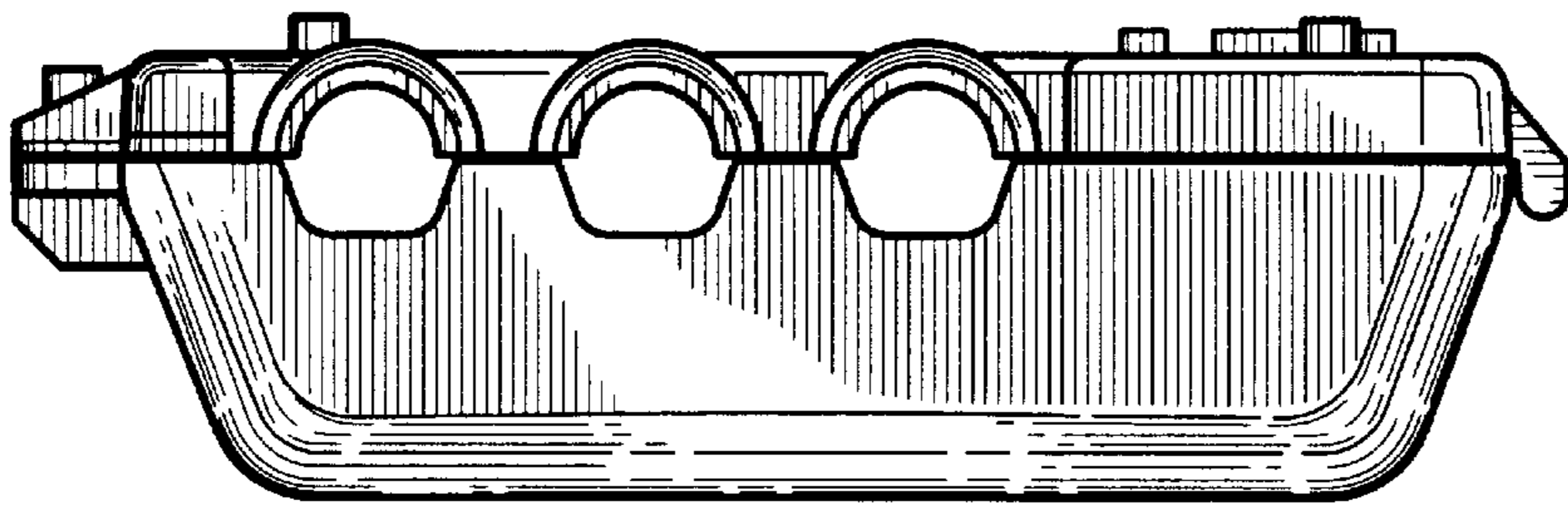
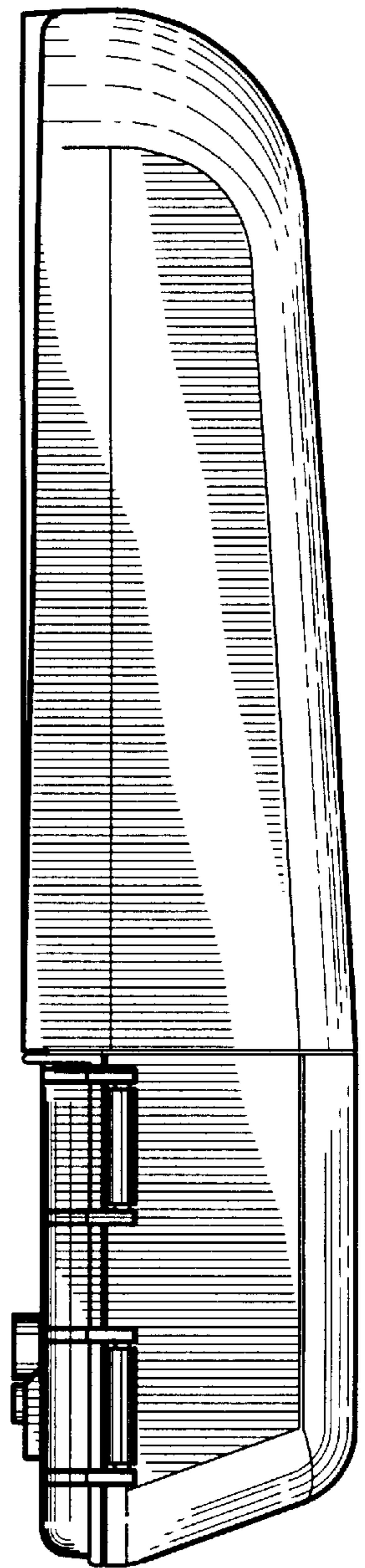
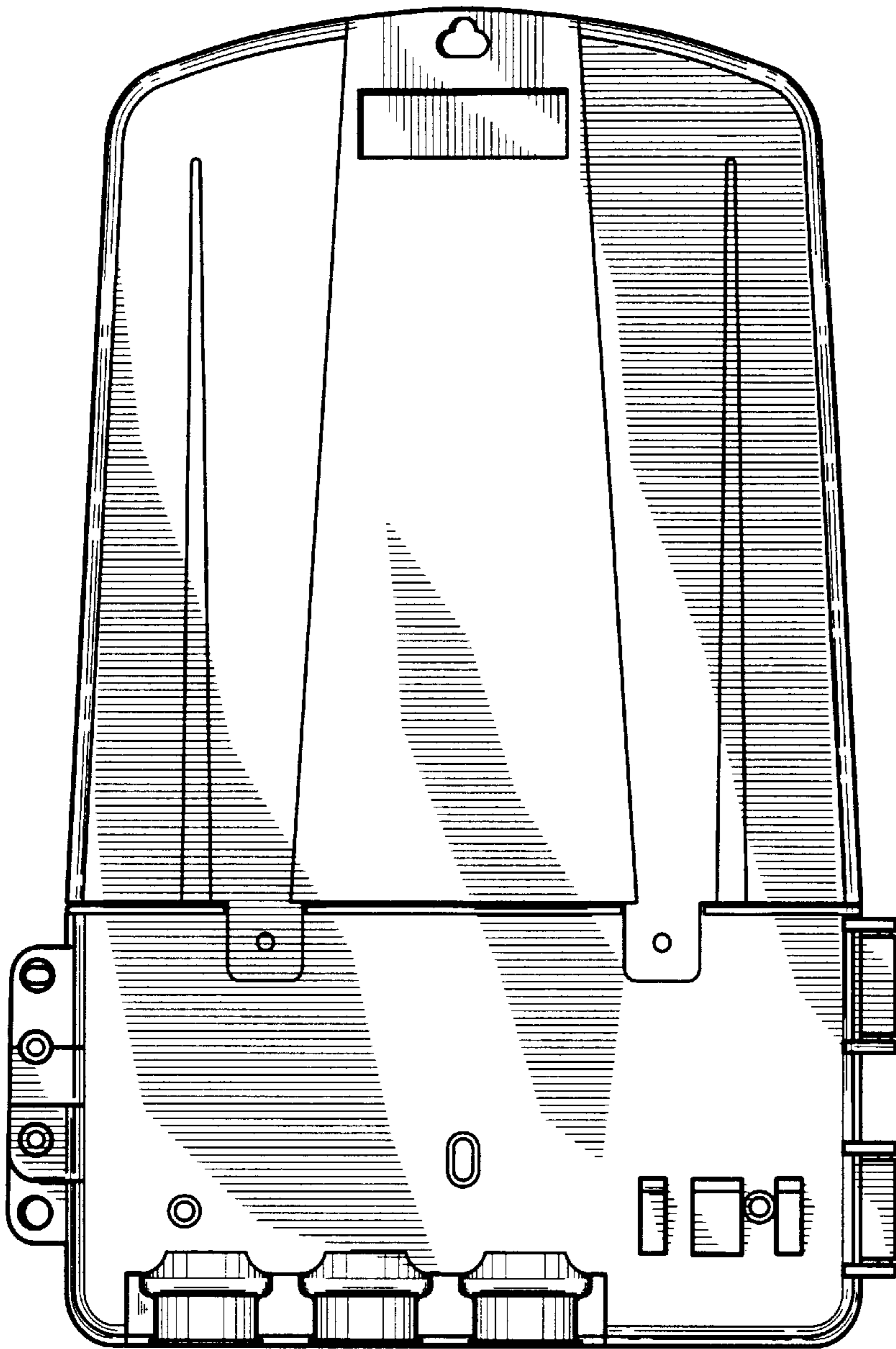


FIG. 4