



US00D420336S

# United States Patent [19]

Tanaka

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[54] **OPERATION CONTROLLER FOR ELECTRONIC COMPUTERS**

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[73] Assignee: Kabushiki Kaisha Toshiba, Kawasaki, Japan

[\*\*] Term: 14 Years

[21] Appl. No.: 29/096,215

[22] Filed: Nov. 6, 1998

[30] **Foreign Application Priority Data**

May 20, 1998 [JP] Japan ..... 10-14039

[51] LOC (7) Cl. .... 14-02

[52] U.S. Cl. .... D14/107; D14/100; D13/147

[58] Field of Search ..... D14/100, 102, D14/107-109, 240, 114; D13/162, 184, 199, 147; D6/432, 436, 445, 448; 312/194; 360/97.01, 97.04, 98.01, 99.01, 99.12; 361/690-696; 369/34, 36

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[57] **CLAIM**

The ornamental design for an operation controller for electronic computers, as shown and described.

**DESCRIPTION**

FIG. 1 is a top, front and right side perspective view of an operation controller for electronic computers showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a left side elevational view thereof; and,

FIG. 7 is a right side elevational view thereof.

**1 Claim, 3 Drawing Sheets**

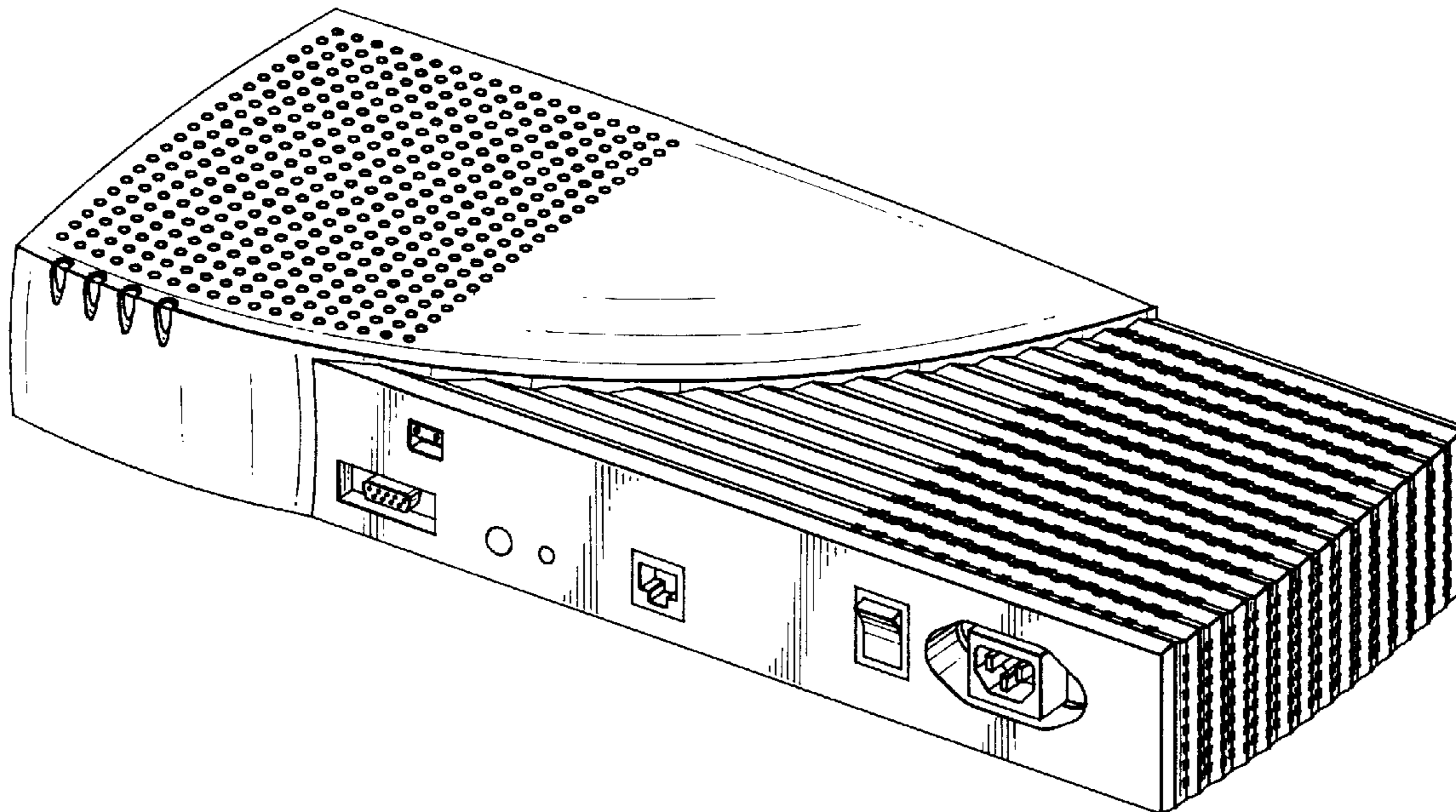


FIG. 1

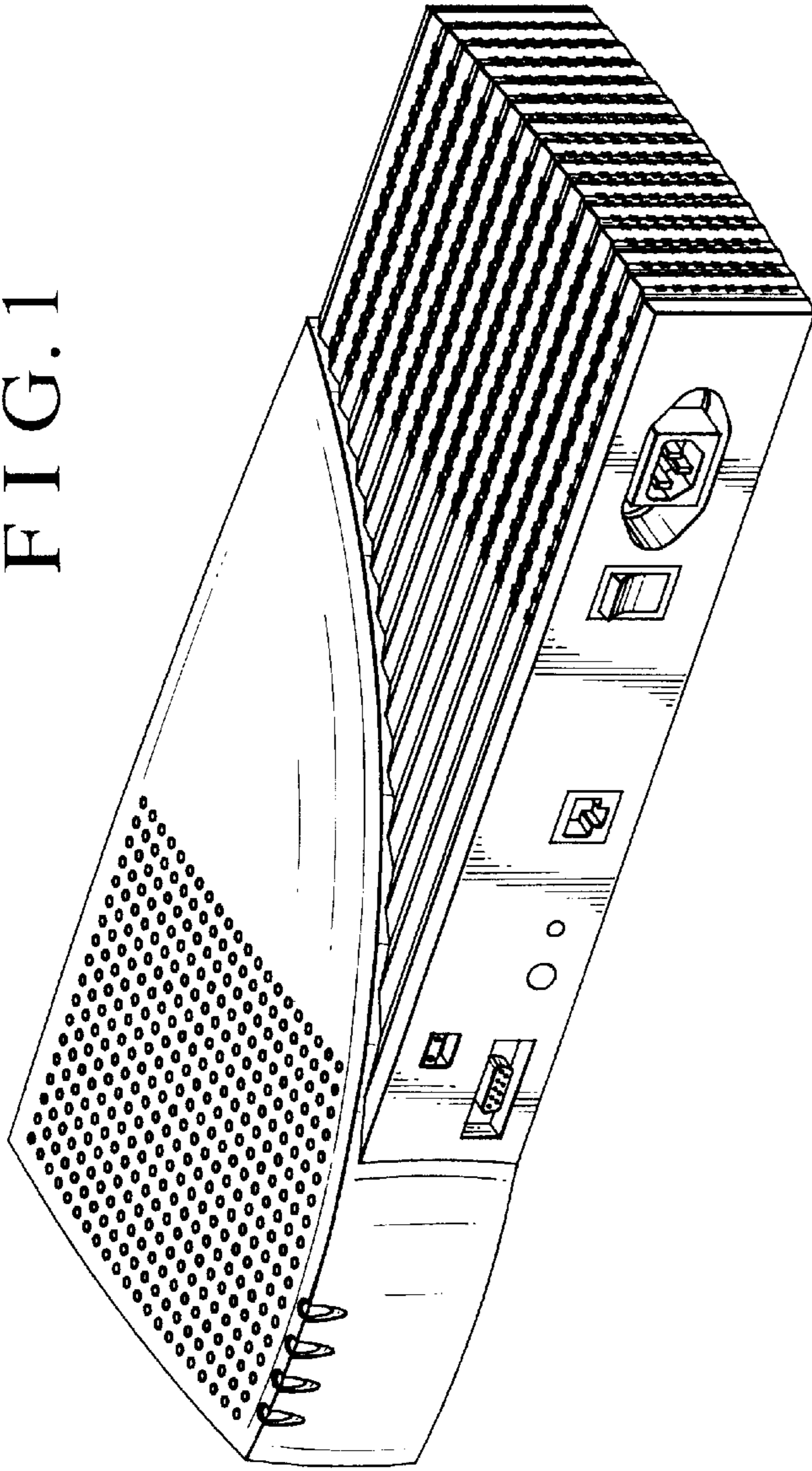


FIG. 2

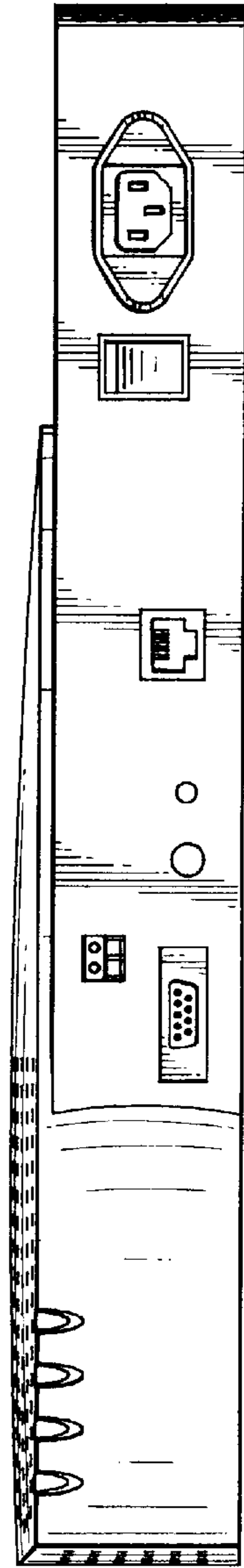


FIG. 3

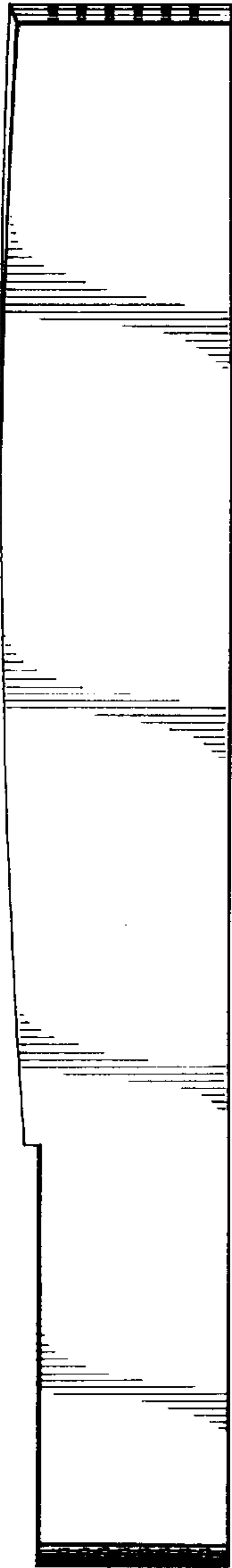


FIG. 4

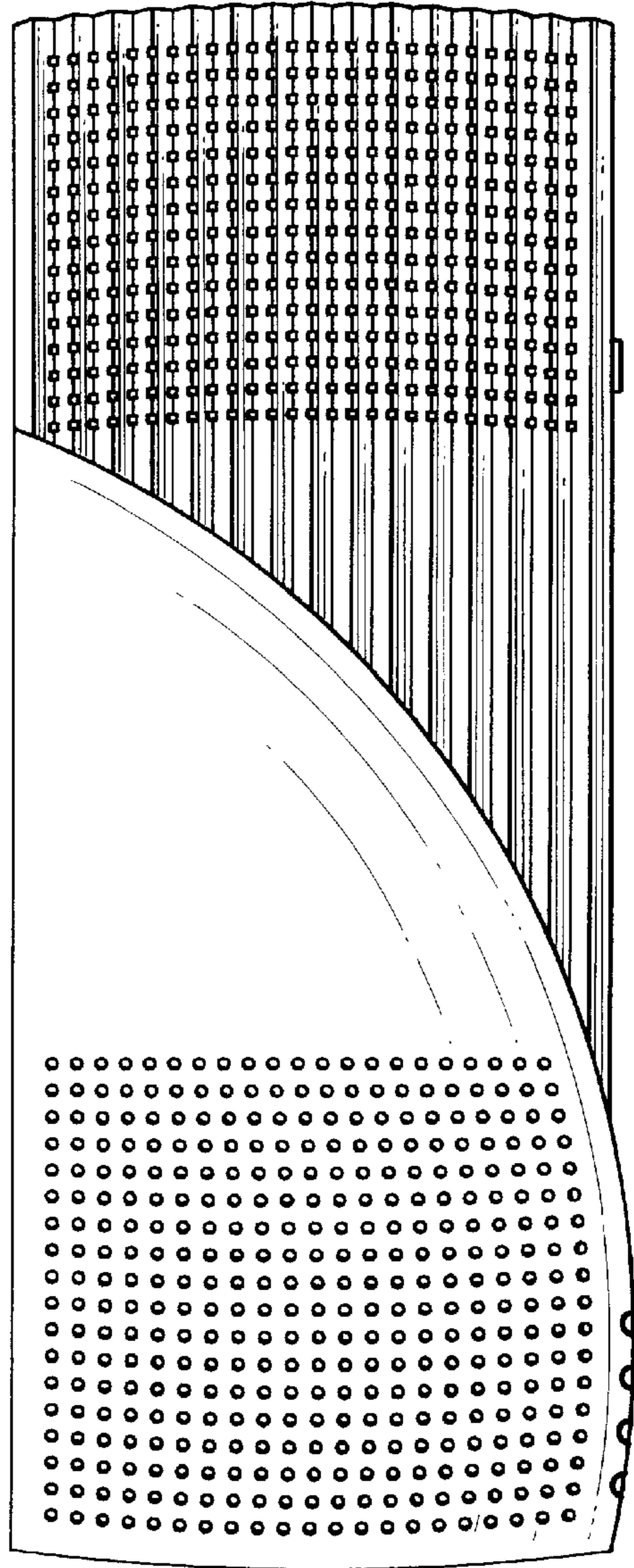


FIG. 5

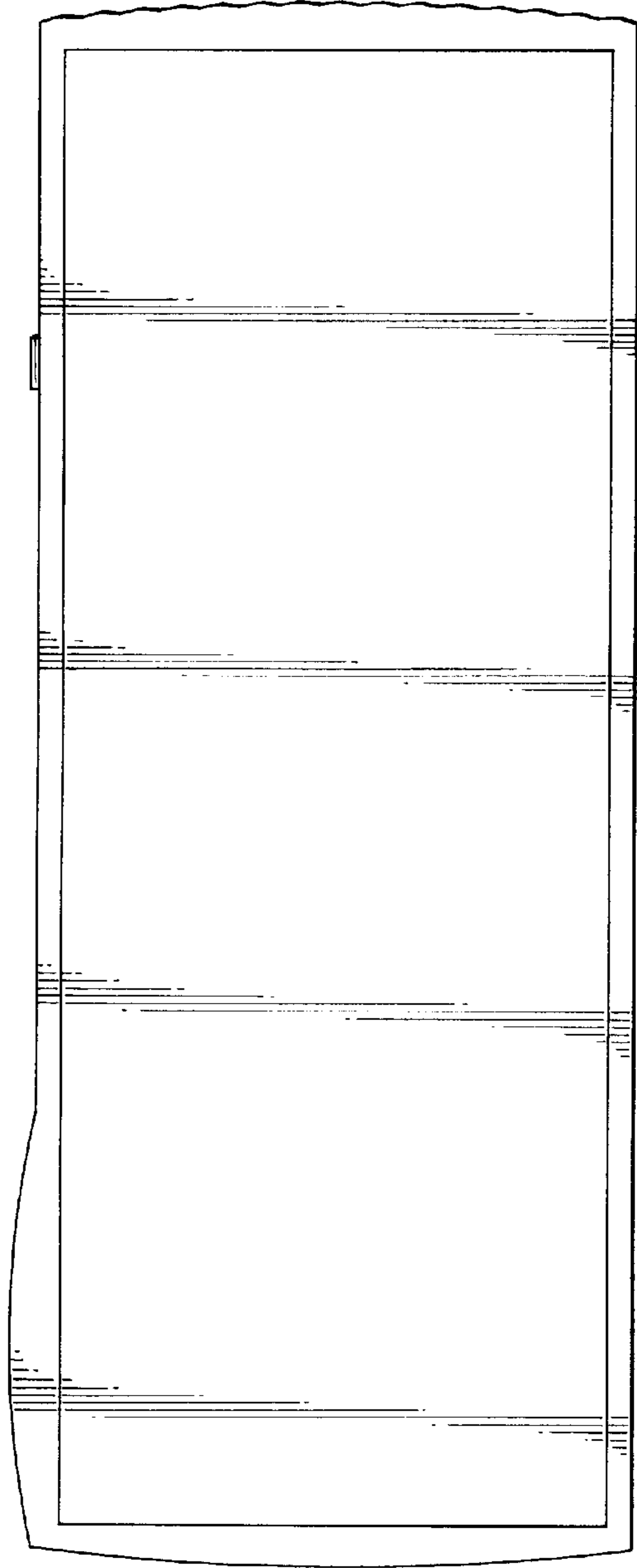


FIG. 6

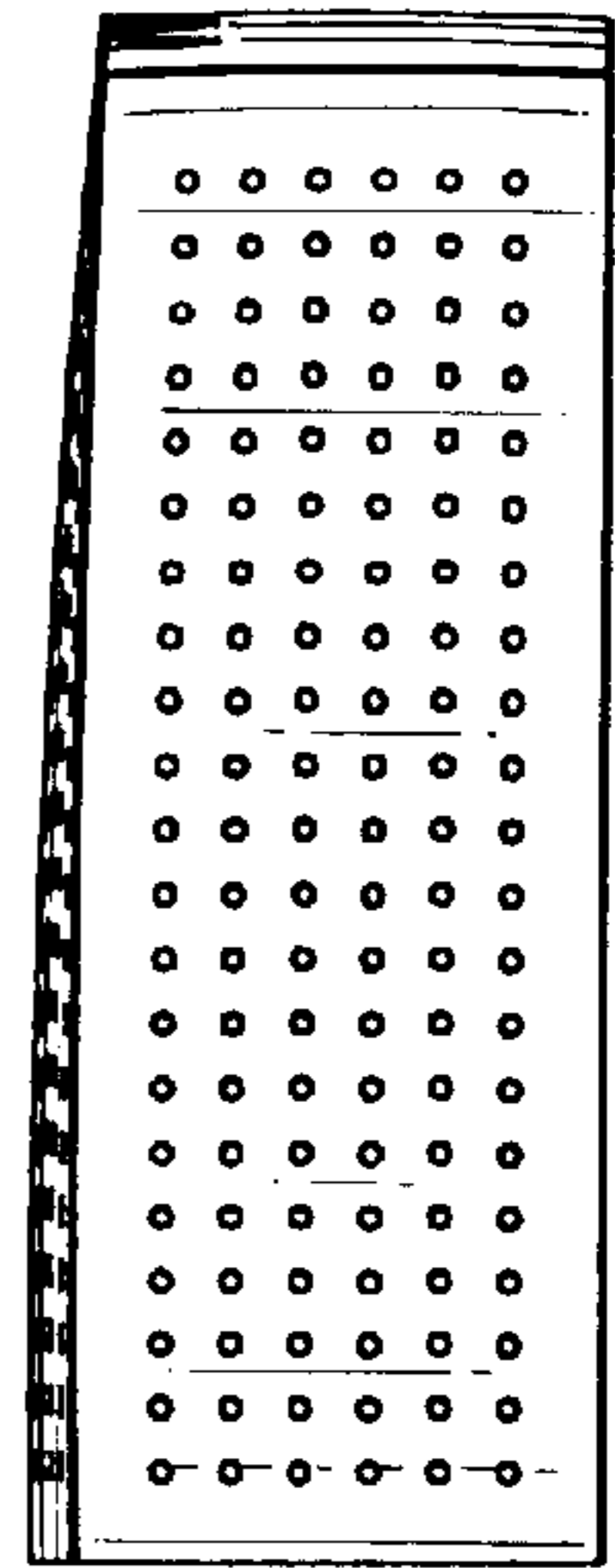


FIG. 7

