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United States Patent [19]

Lee et al.

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[45] **Date of Patent: ** Feb. 1, 2000**

[54] **CYCLE COMPUTER**

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[73] Assignee: **Chun-Mu Huang**, Taiwan

[**] Term: **14 Years**

[21] Appl. No.: **29/099,770**

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[51] **LOC (7) Cl.** **10-04**

[52] **U.S. Cl.** **D10/98**

[58] **Field of Search** D10/98; 324/160-180; 235/95 R, 95 A, 95 B, 95 C, 196, 197

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 334,720 4/1993 Ueda D10/98

D. 347,591 6/1994 Chan D10/98

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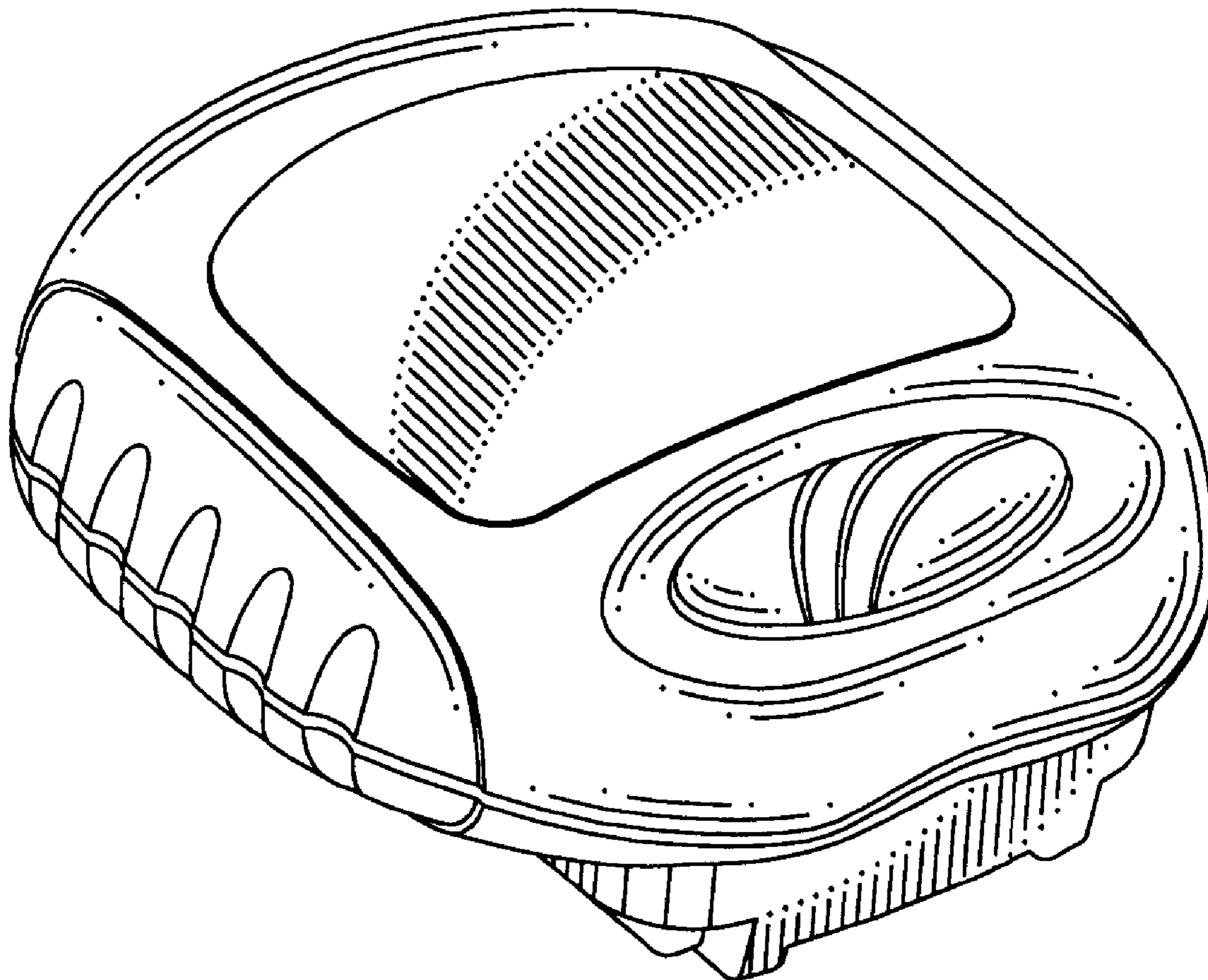
[57] **CLAIM**

The ornamental design for a cycle computer, as shown and described.

DESCRIPTION

FIG. 1 is a top plane view showing a cycle computer of our new design when a receiver is put down; FIG. 2 is a left side view thereof; FIG. 3 is a right side view thereof; FIG. 4 is a bottom plane view thereof; FIG. 5 is a rear elevational view thereof; FIG. 6 is a front elevational view thereof; and, FIG. 7 is a perspective view thereof.

1 Claim, 7 Drawing Sheets



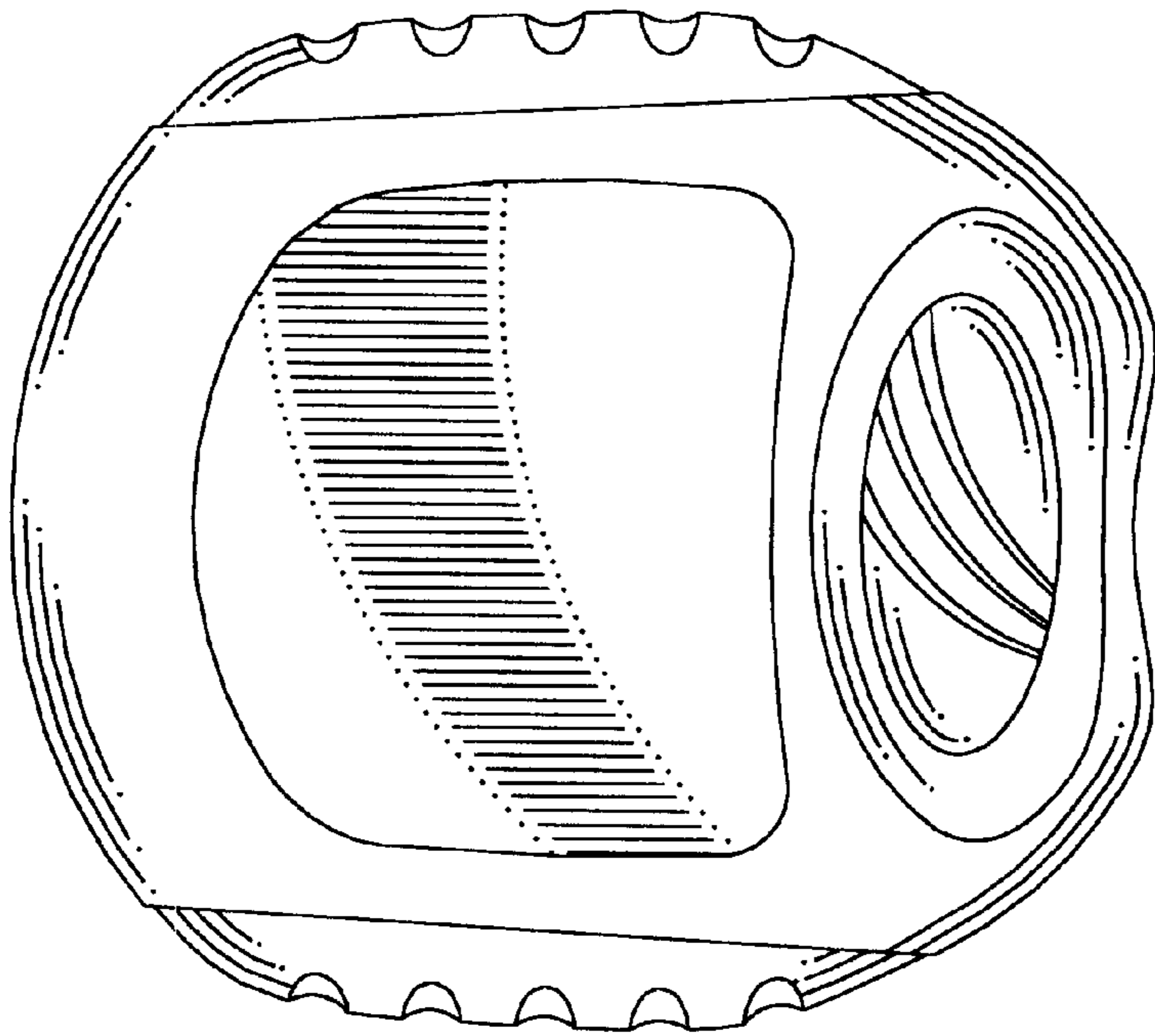


Fig. 1

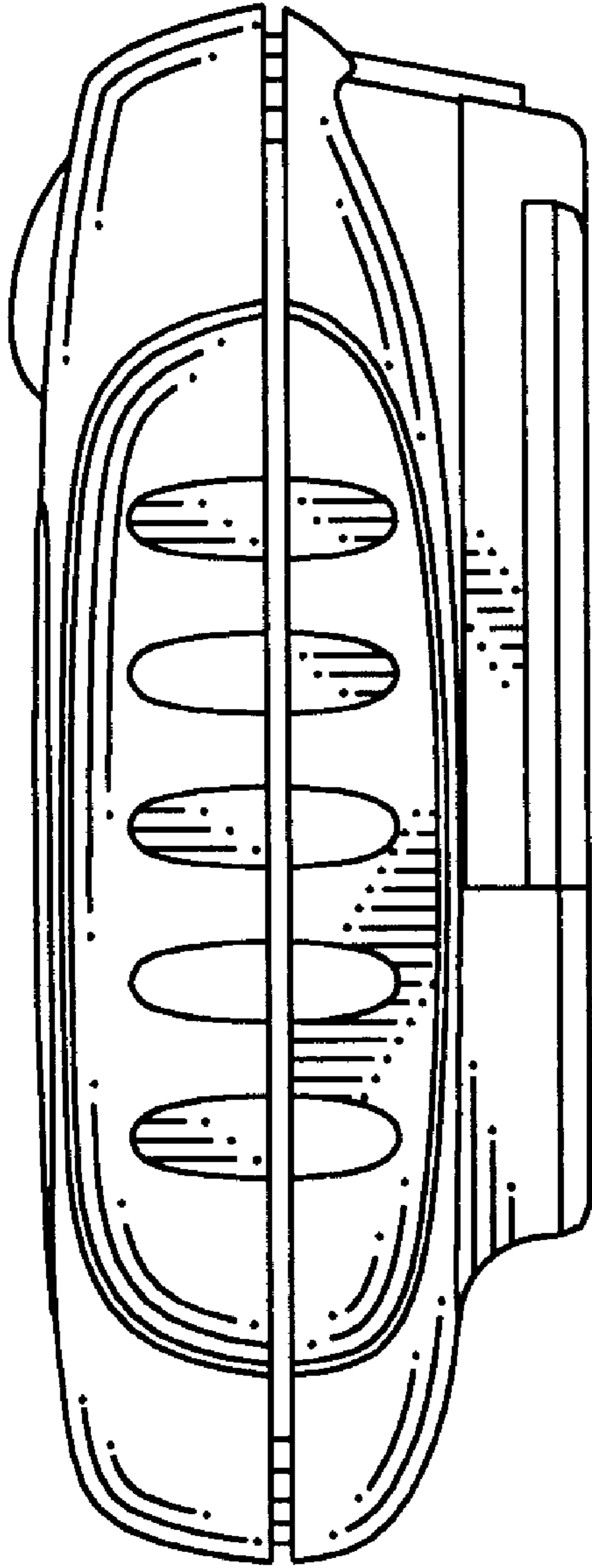


Fig. 2

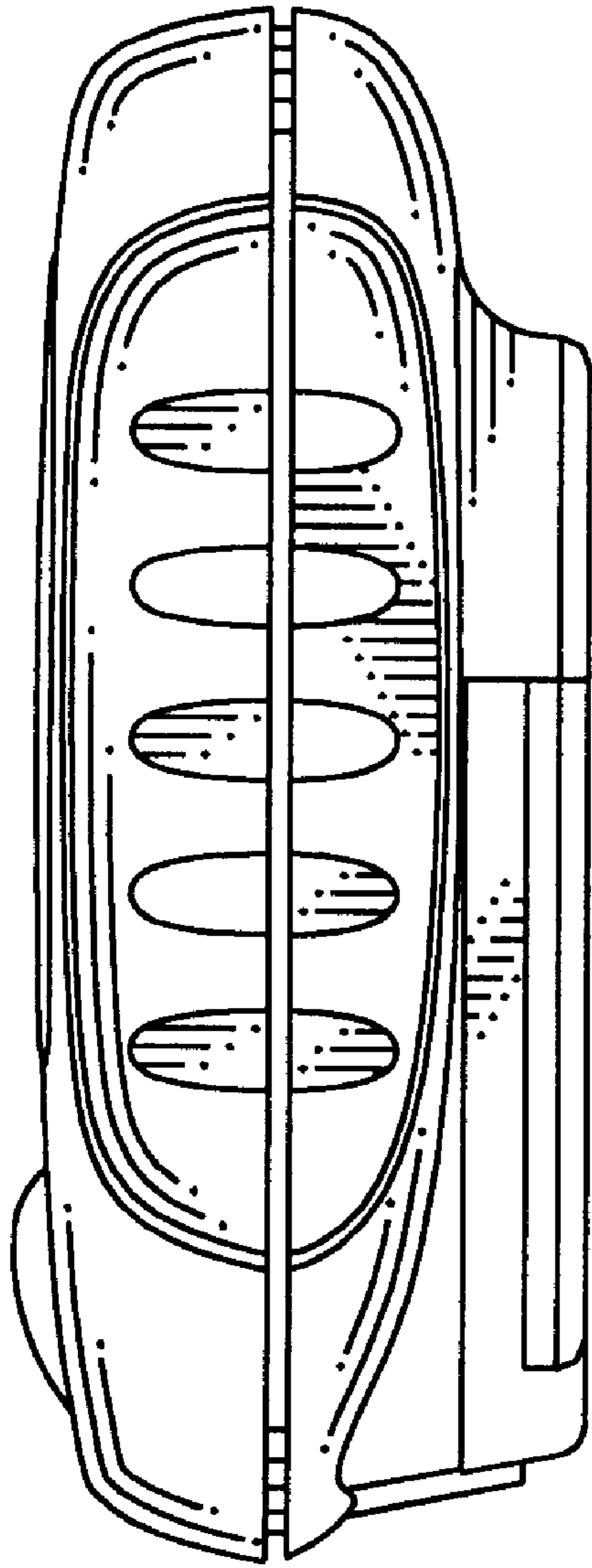


Fig. 3

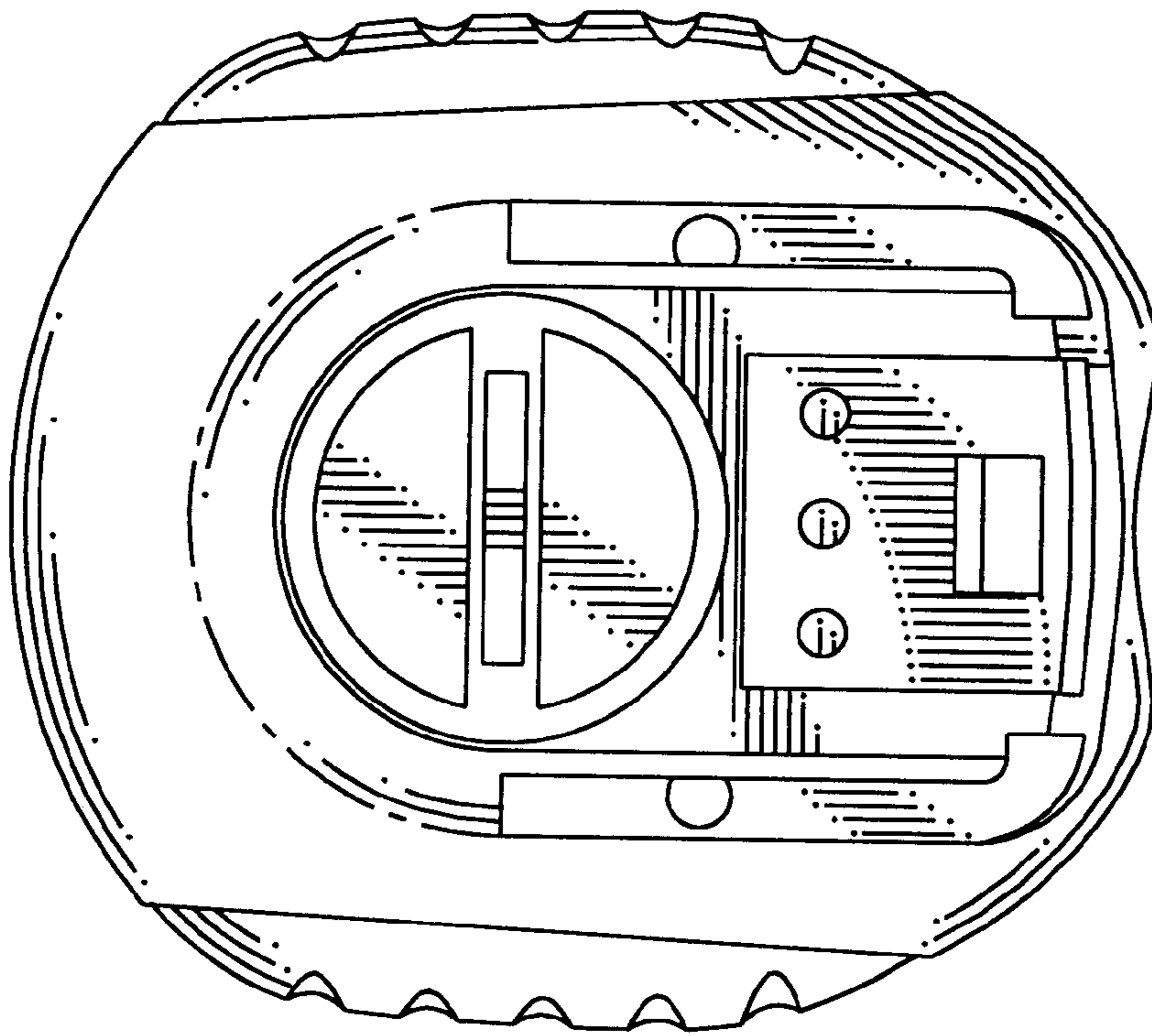


Fig. 4

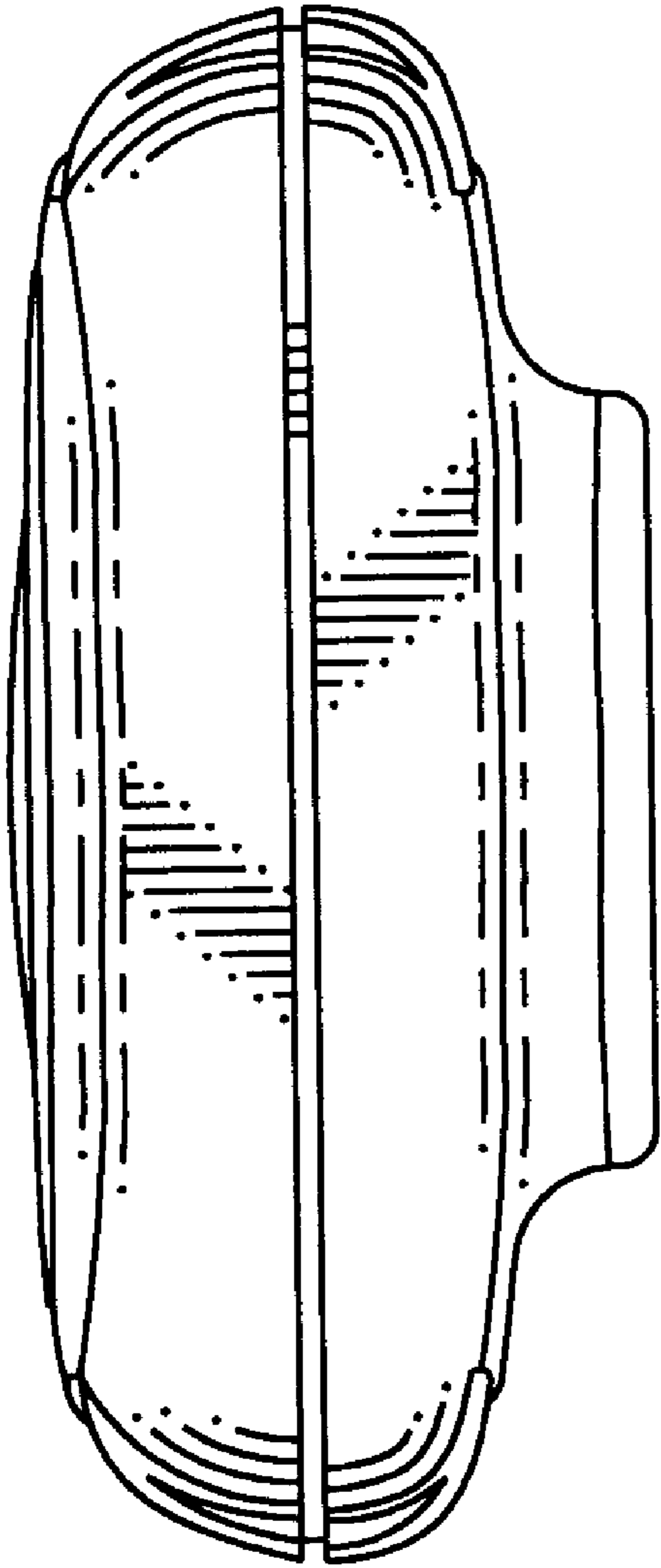


Fig. 5

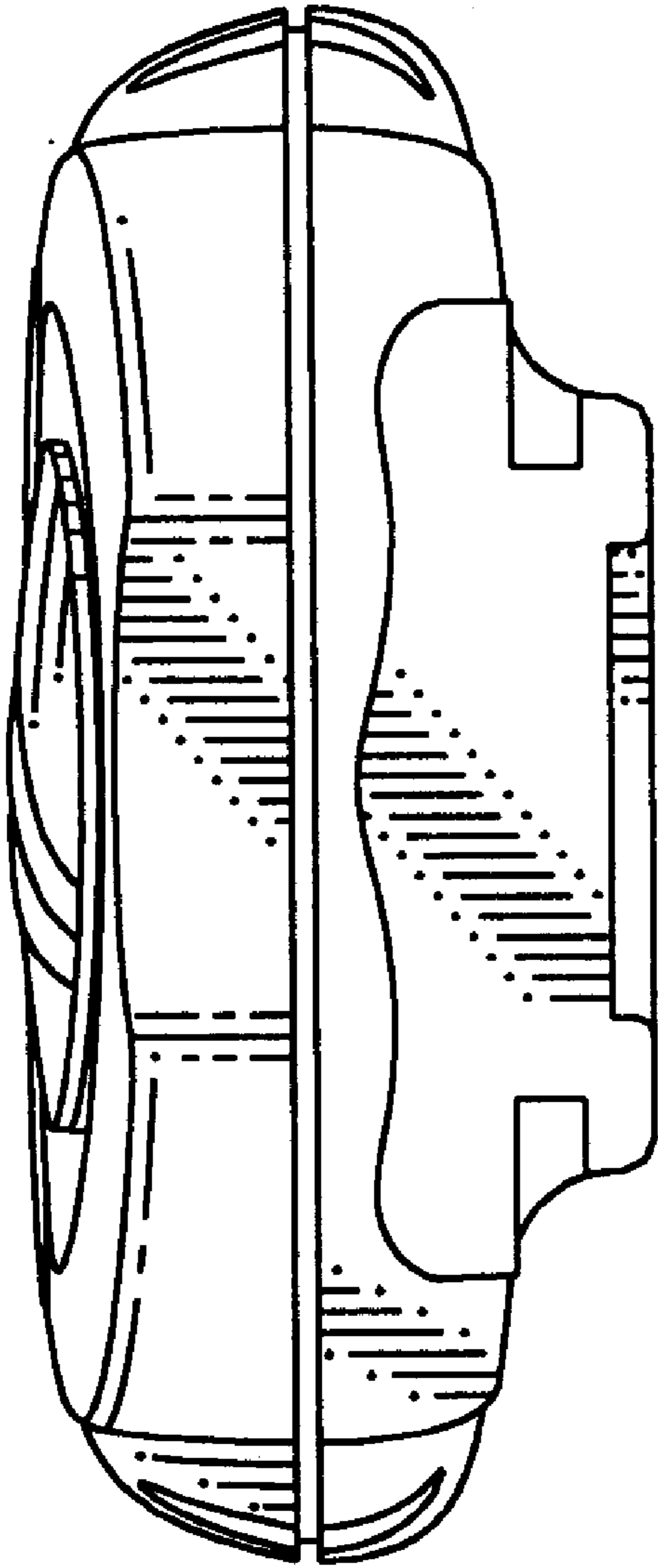


Fig. 6

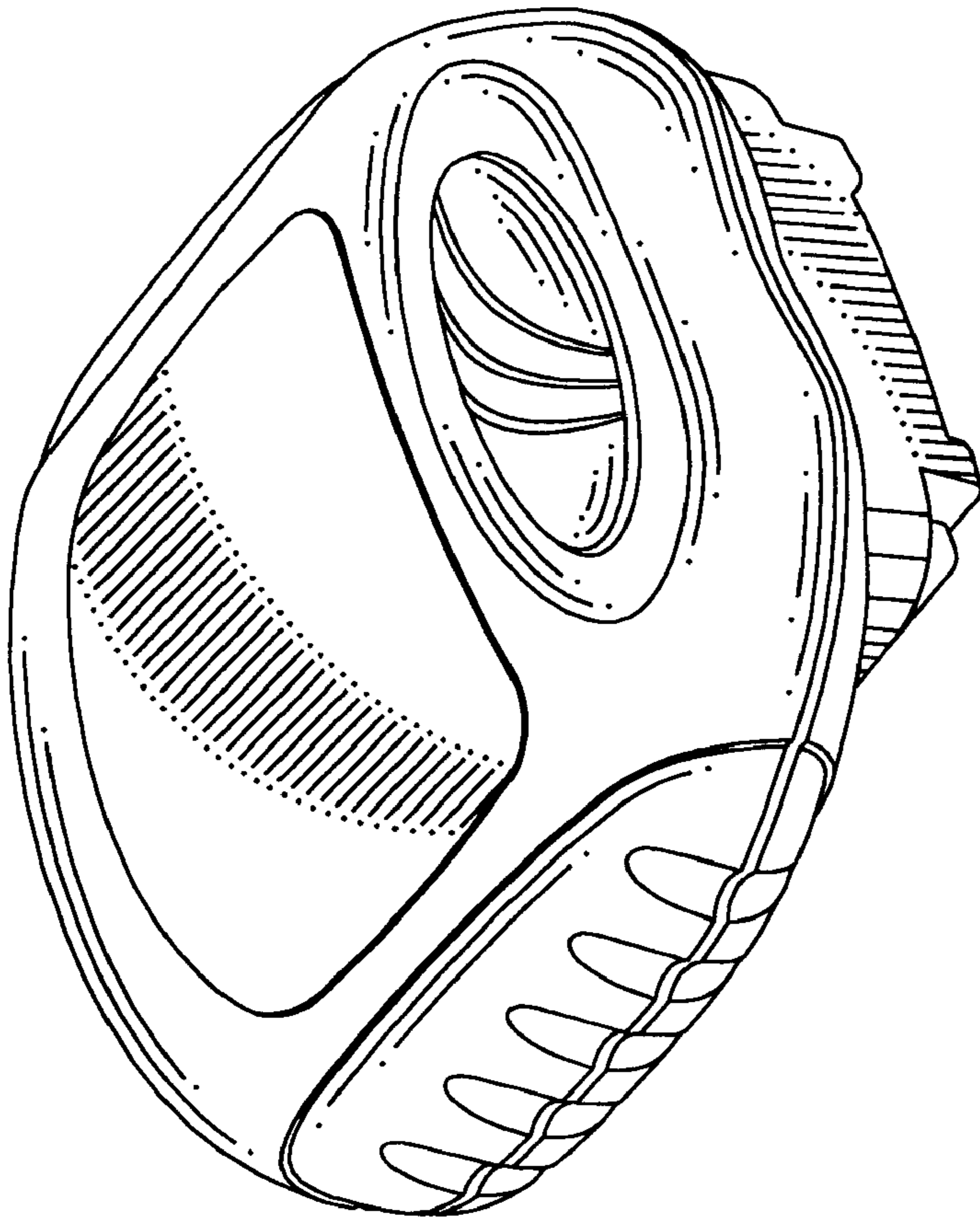


Fig. 7