



US00D418171S

**United States Patent** [19]  
**Robinson et al.**

[11] **Patent Number: Des. 418,171**  
[45] **Date of Patent: \*\* Dec. 28, 1999**

[54] **DISPLAY CHIP**

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[\*\*] Term: **14 Years**

[21] Appl. No.: **29/085,524**

[22] Filed: **Mar. 25, 1998**

[51] **LOC (6) Cl.** ..... **20-02**

[52] **U.S. Cl.** ..... **D20/42; D20/40; D20/22**

[58] **Field of Search** ..... 40/299, 124.09,  
40/124.01, 658, 310, 630, 631, 672, 642.02,  
642.01; D20/10, 27, 42, 43, 40, 19; 206/39;  
283/81

[56] **References Cited**

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Photographs of Display chip 006-003-611.  
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[57] **CLAIM**

The ornamental design for display chip, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevation view of my new design;  
FIG. 2 is a rear elevation view of my new design;  
FIG. 3 is a side elevation view of my new design;  
FIG. 4 is a side elevation view of my new design;  
FIG. 5 is a top plan view of my new design;  
FIG. 6 is bottom plan view of my new design;  
FIG. 7 is a top plan view of the unfolded display chip;  
FIG. 8 is a bottom plan view of the unfolded display chip;  
and,  
FIG. 9 is a perspective view of my new design.

**1 Claim, 5 Drawing Sheets**

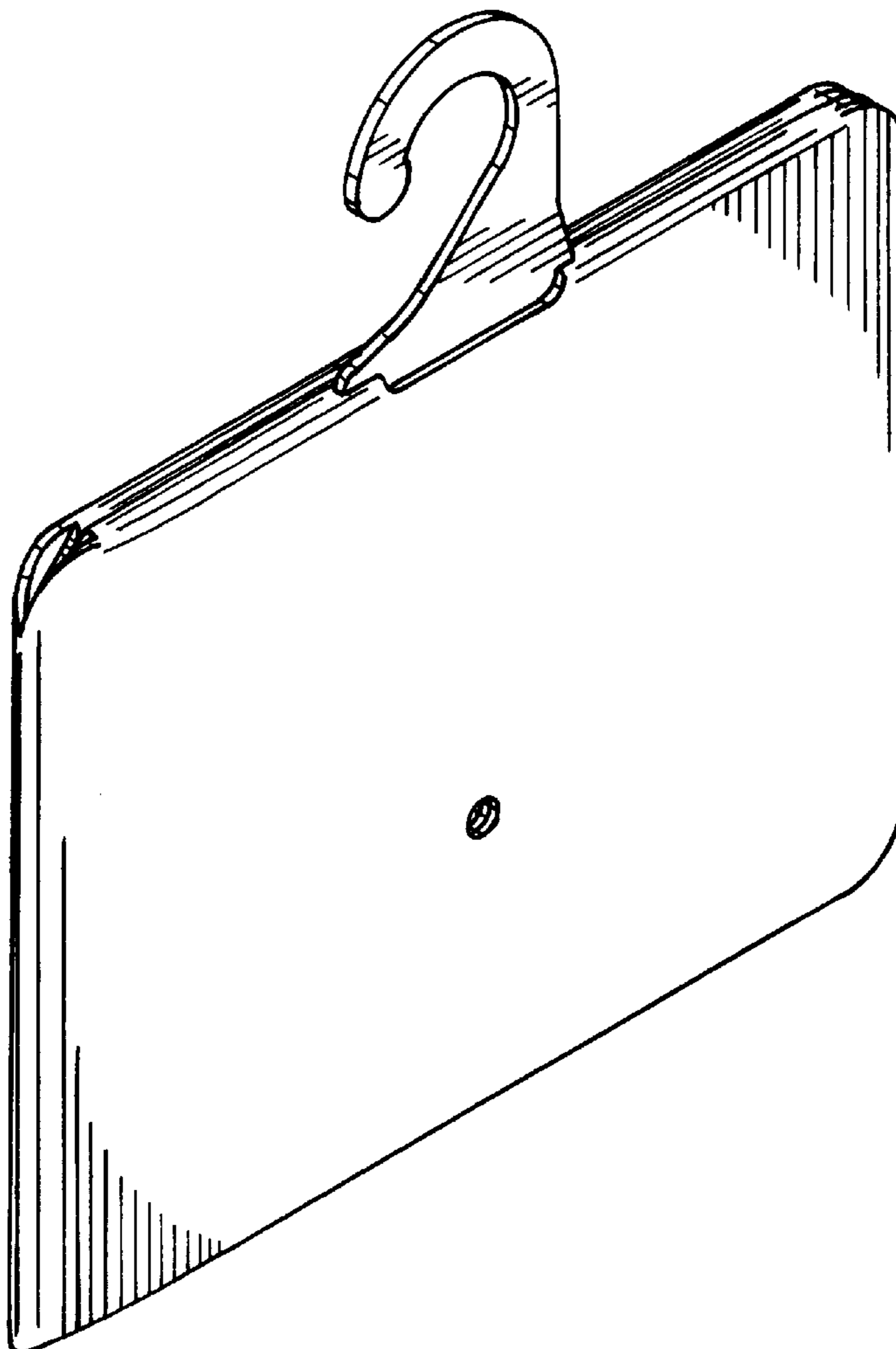


FIG. 2

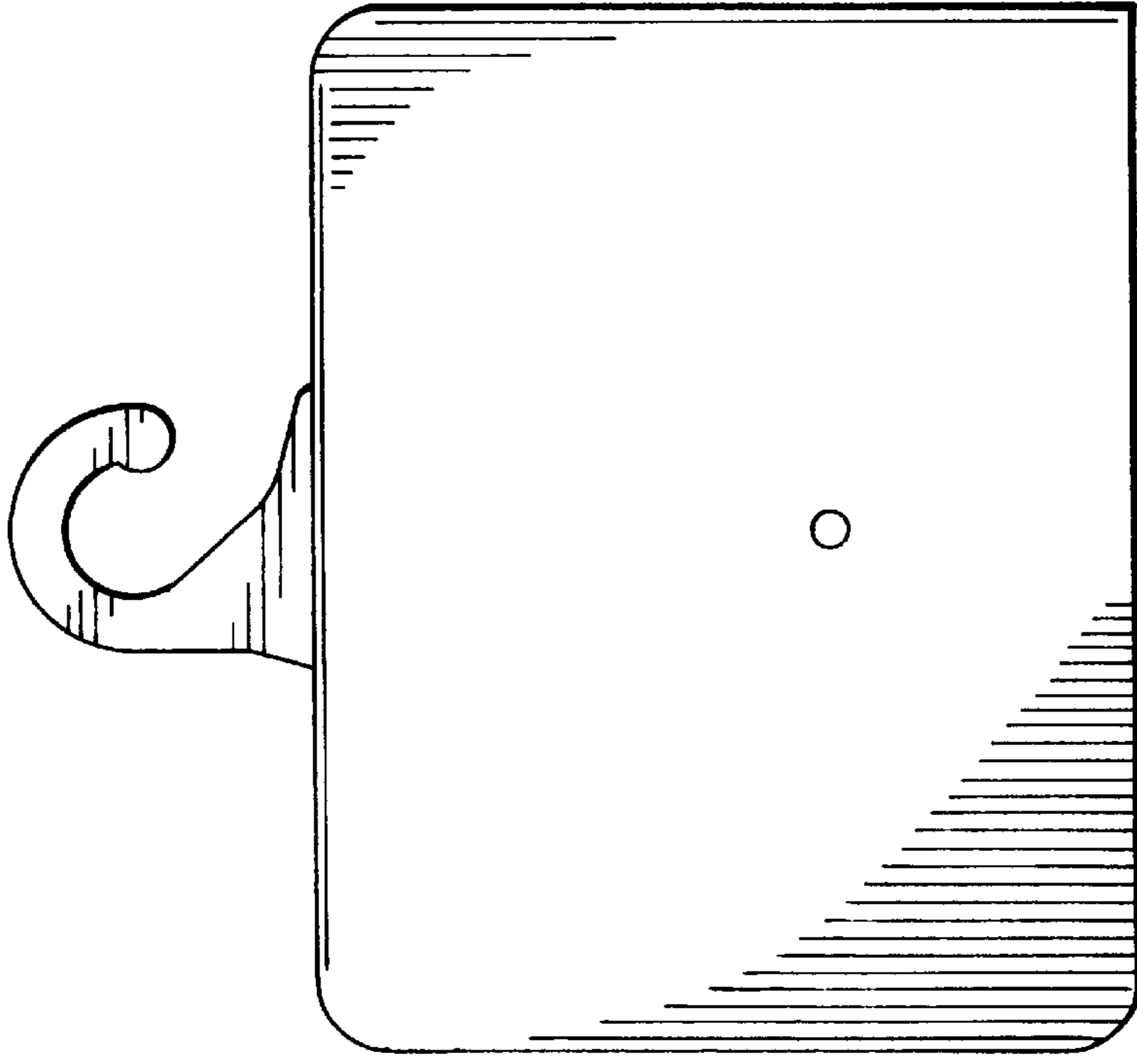


FIG. 1

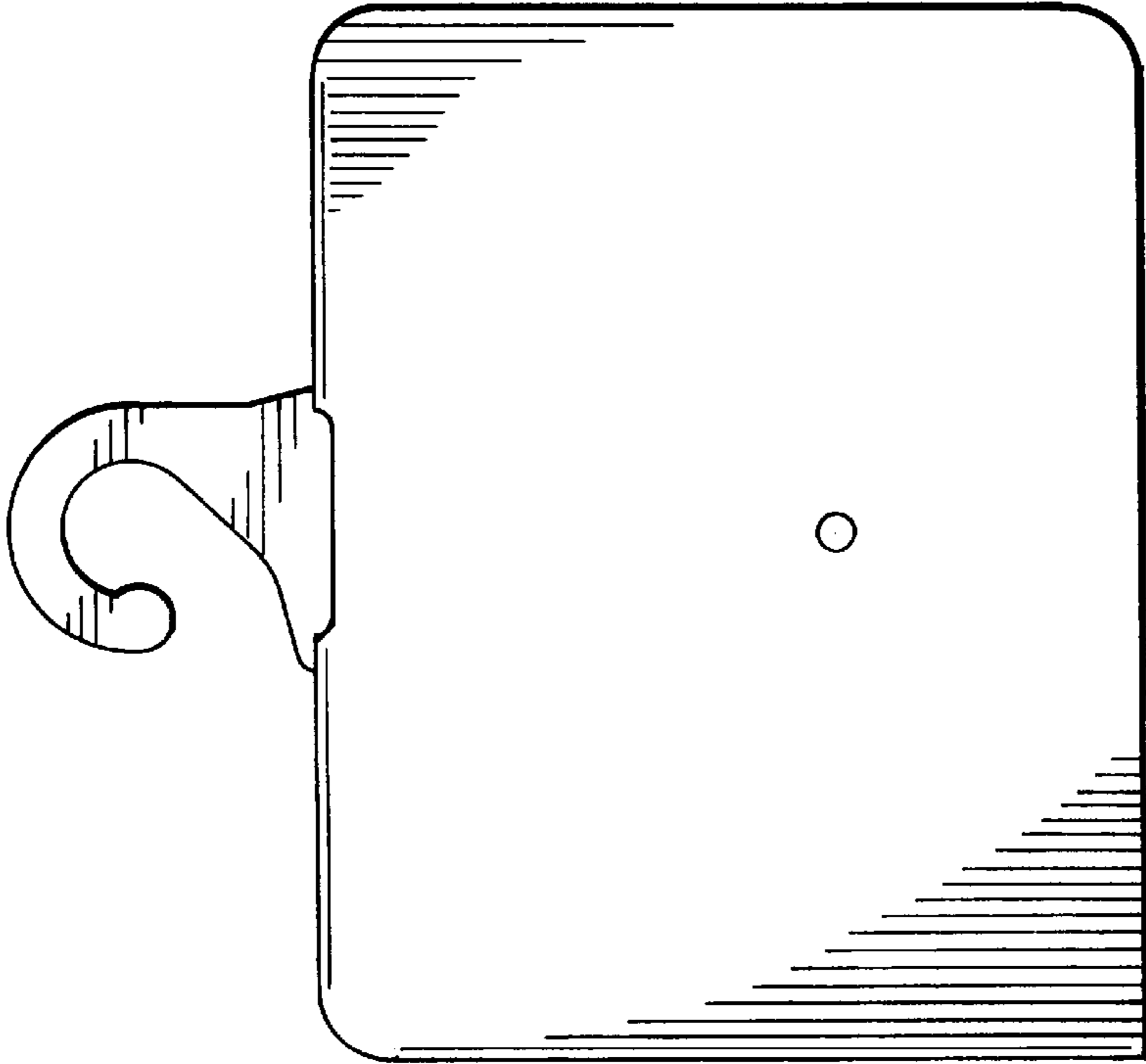


FIG. 3

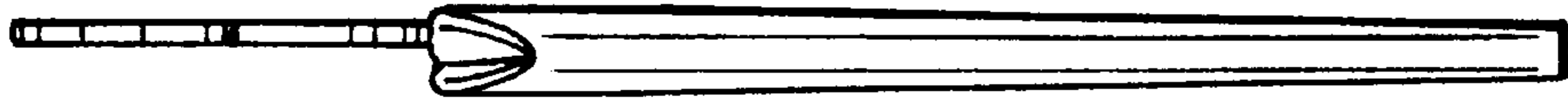


FIG. 4

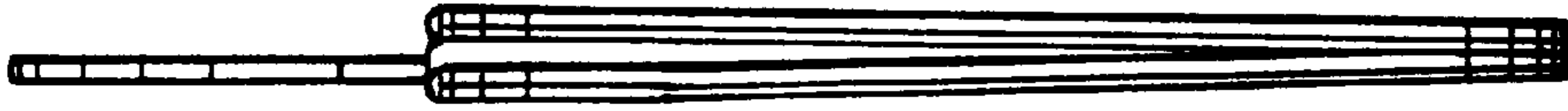


FIG. 5



FIG. 6



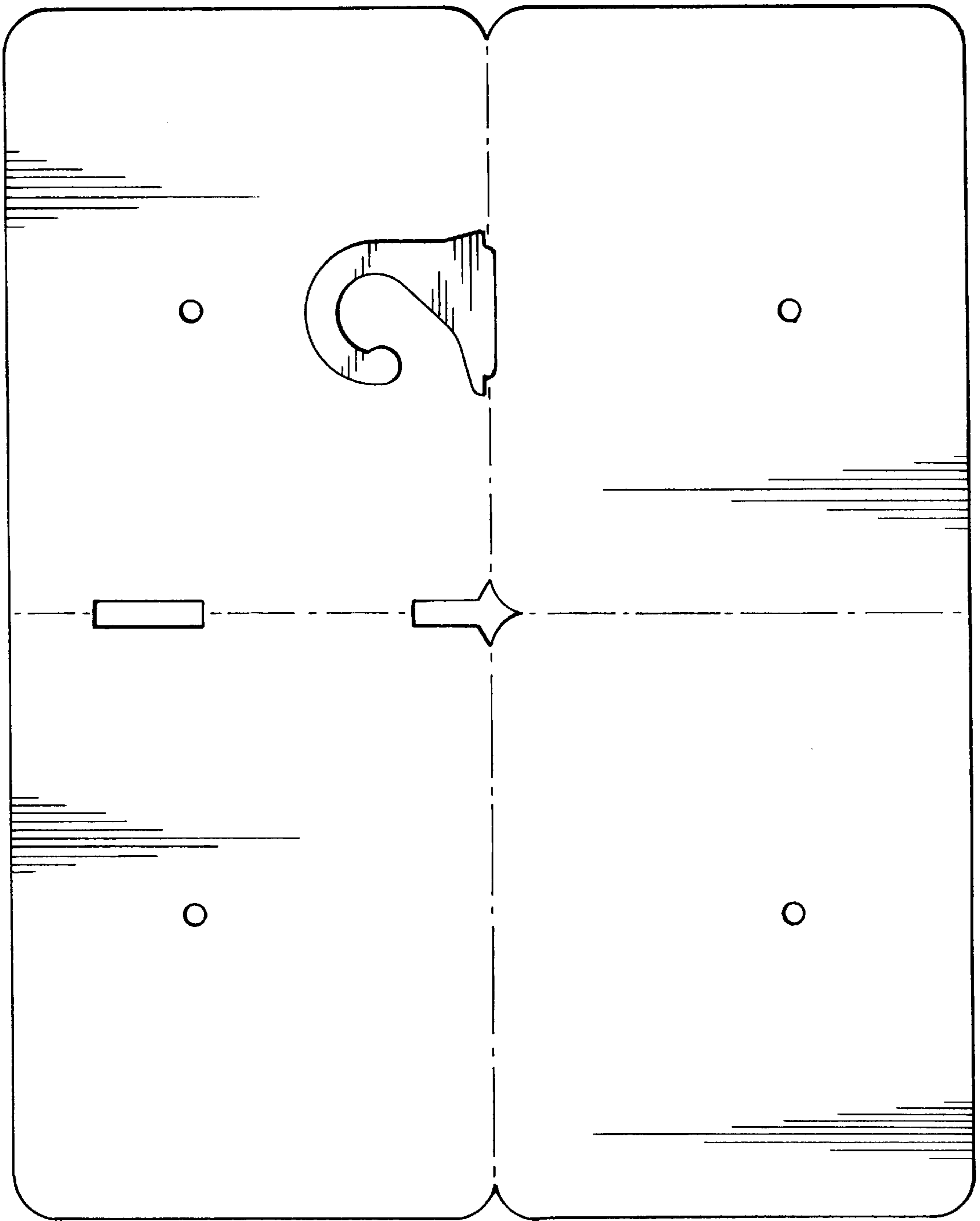


FIG. 7

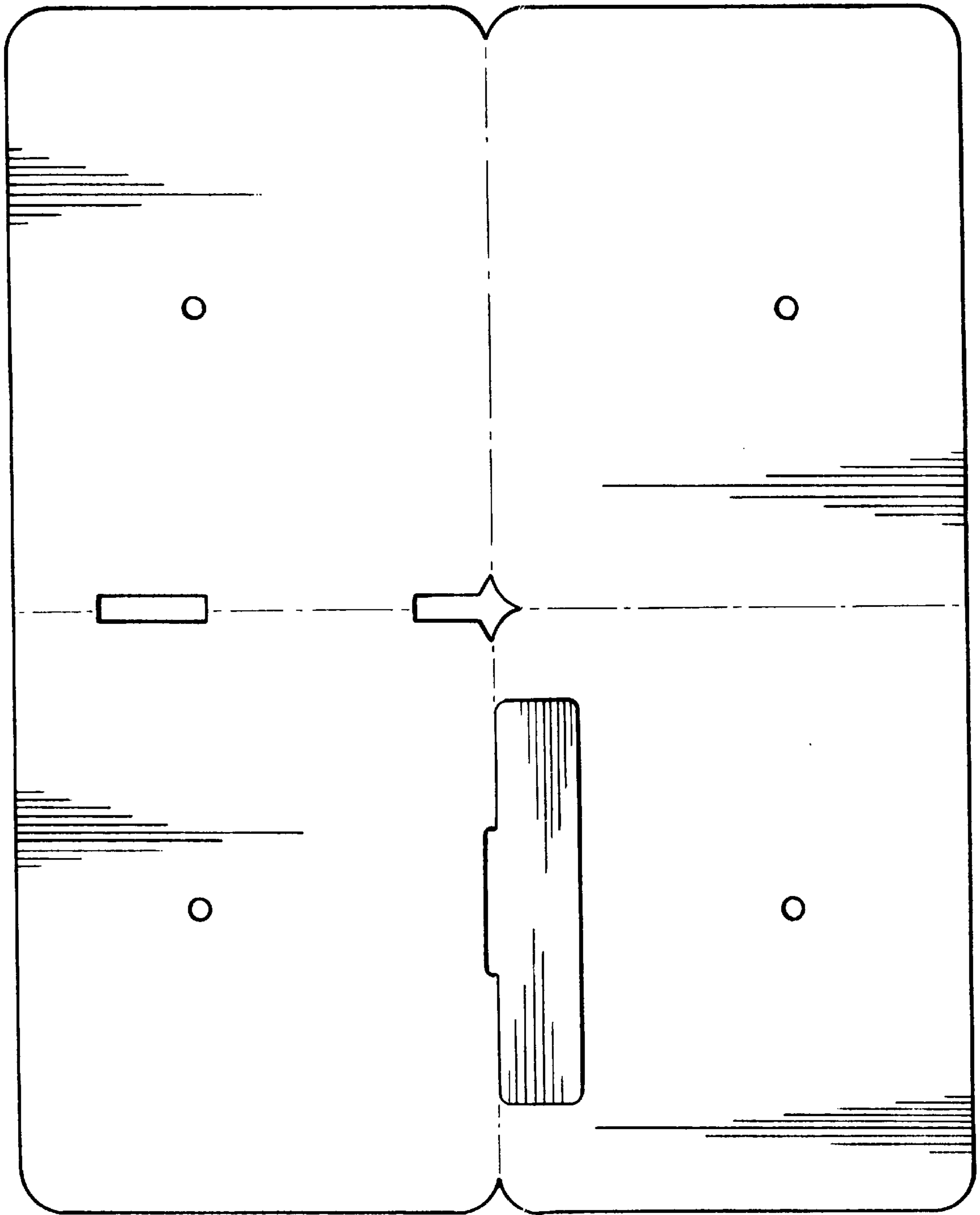


FIG. 8

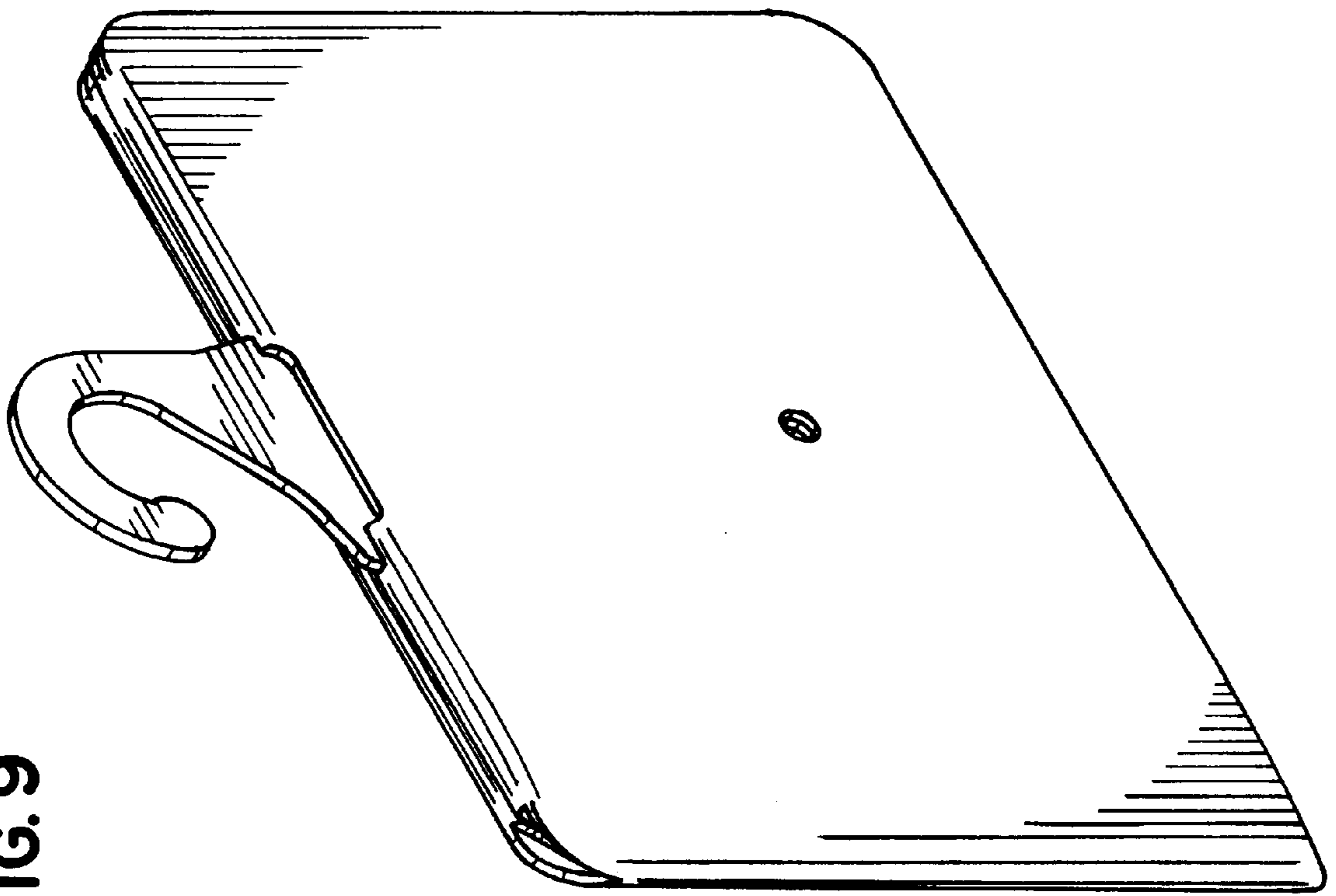


FIG. 9