



US00D417869S

United States Patent [19]
Yasutomi

[11] **Patent Number: Des. 417,869**

[45] **Date of Patent: ** Dec. 21, 1999**

[54] **ELECTRONIC DEVICE**

[75] **Inventor: Hiroshi Yasutomi, Tokyo, Japan**

[73] **Assignee: Sony Corporation, Tokyo, Japan**

[**] **Term: 14 Years**

[21] **Appl. No.: 29/076,590**

[22] **Filed: Sep. 11, 1997**

[51] **LOC (6) Cl. 14-01**

[52] **U.S. Cl. D14/168; D14/192**

[58] **Field of Search** D10/119; D11/79-81,
D11/85; D14/124, 137, 157, 160, 163-165,
167, 168, 188, 189, 192, 195-198, 223;
D21/113; 381/73.1; 455/344, 347-351

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Primary Examiner—Nanda Bondade
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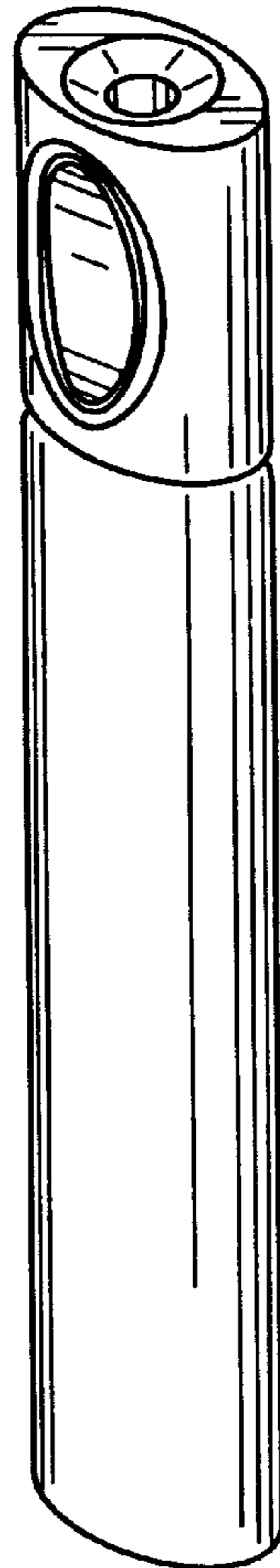
[57] **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device showing my new design;
FIG. 2 is a top plan view thereof;
FIG. 3 is a left side elevational view thereof;
FIG. 4 is a front elevational view thereof;
FIG. 5 is a bottom plan view thereof;
FIG. 6 is a right side elevational view thereof; and,
FIG. 7 is a rear elevational view thereof.

1 Claim, 2 Drawing Sheets



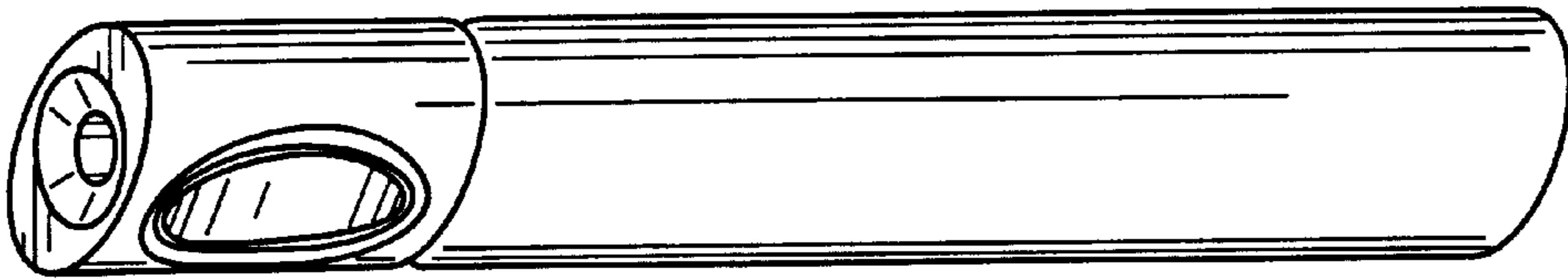


FIG. 1



FIG. 2

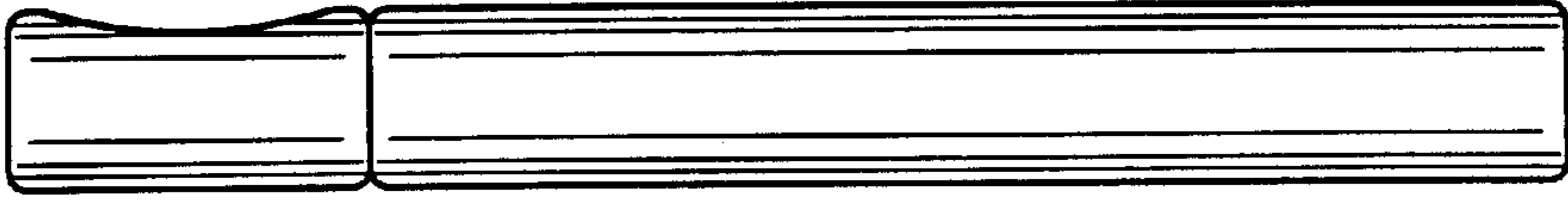


FIG. 3

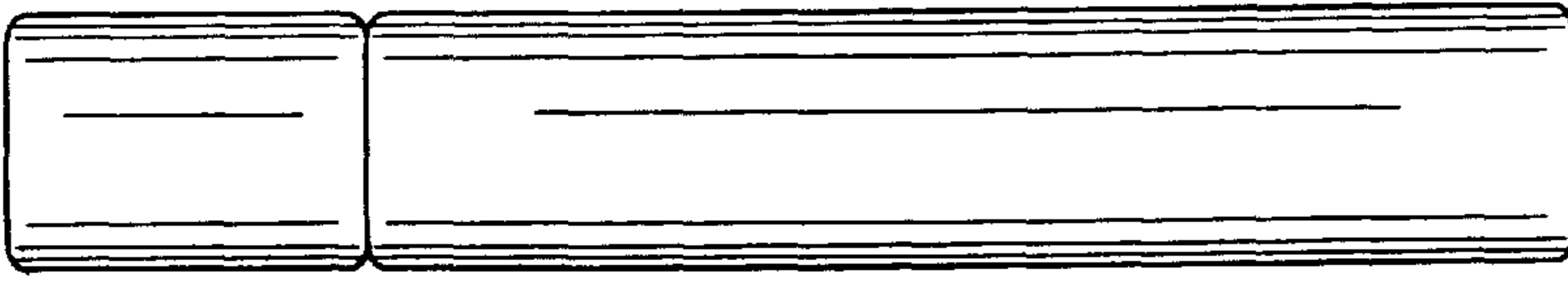


FIG. 7

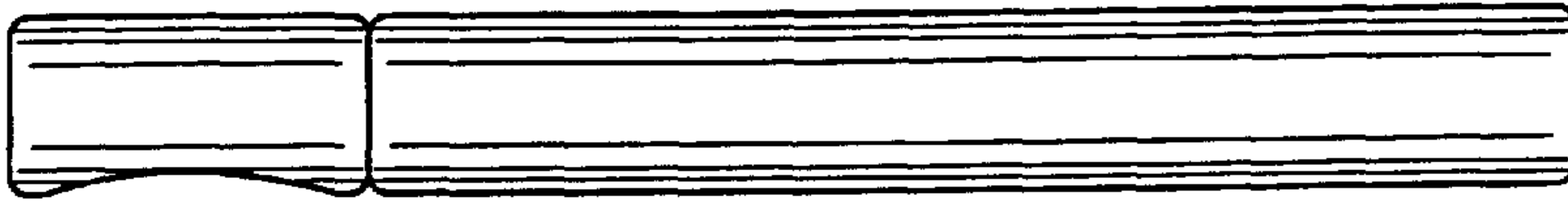


FIG. 6

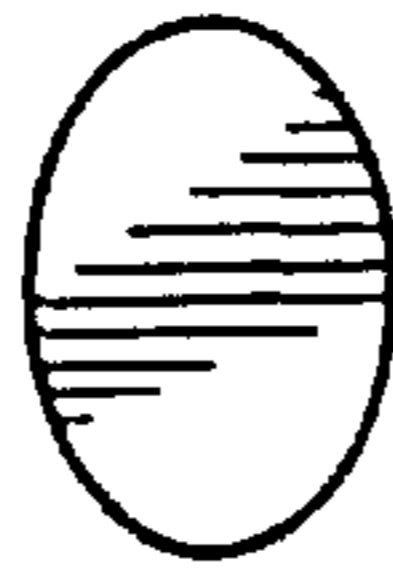


FIG. 5

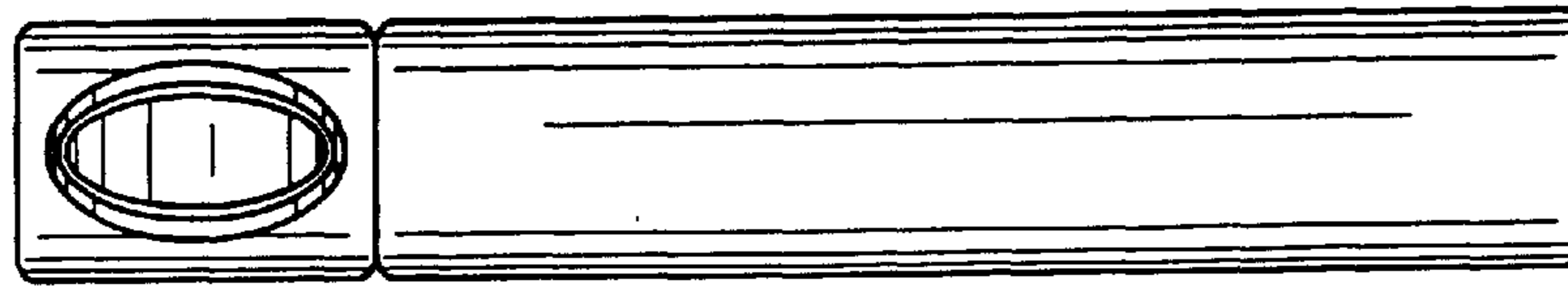


FIG. 4