



US00D416002S

United States Patent [19]

[11] **Patent Number: Des. 416,002**

Tucker et al.

[45] **Date of Patent: ** Nov. 2, 1999**

[54] **ENCLOSURE FOR ELECTRONIC COMPONENTS**

D. 344,073 2/1994 Cipolla et al. D14/102
D. 387,336 12/1997 Ono et al. D14/100

[75] Inventors: **Paul A. Tucker**, Richardson; **Gary Nightingale**; **Brian Spate**, both of Dallas, all of Tex.

Primary Examiner—Freda Nunn
Attorney, Agent, or Firm—Locke Liddell & Sapp LLP

[73] Assignee: **DSC Telecom L.P.**, Plano, Tex.

[57] **CLAIM**

[**] Term: **14 Years**

The ornamental design for an enclosure for electronic components, as shown and described.

[21] Appl. No.: **29/086,406**

DESCRIPTION

[22] Filed: **Apr. 13, 1998**

FIG. 1 is a perspective view of an enclosure for electronic components showing our new design;

[51] **LOC (6) Cl.** **14-02**

[52] **U.S. Cl.** **D14/102**

FIG. 2 is a side elevational view thereof, the opposite side being a mirror image thereof;

[58] **Field of Search** D14/100, 102, D14/107-109; D13/162, 184, 199; D6/432, 436, 445, 448; 312/208.1, 223.2, 223.3; 360/99.12; 361/690-696

FIG. 3 is a front elevational view thereof, the rear being a mirror image thereof;

FIG. 4 is a top plan view thereof; and,

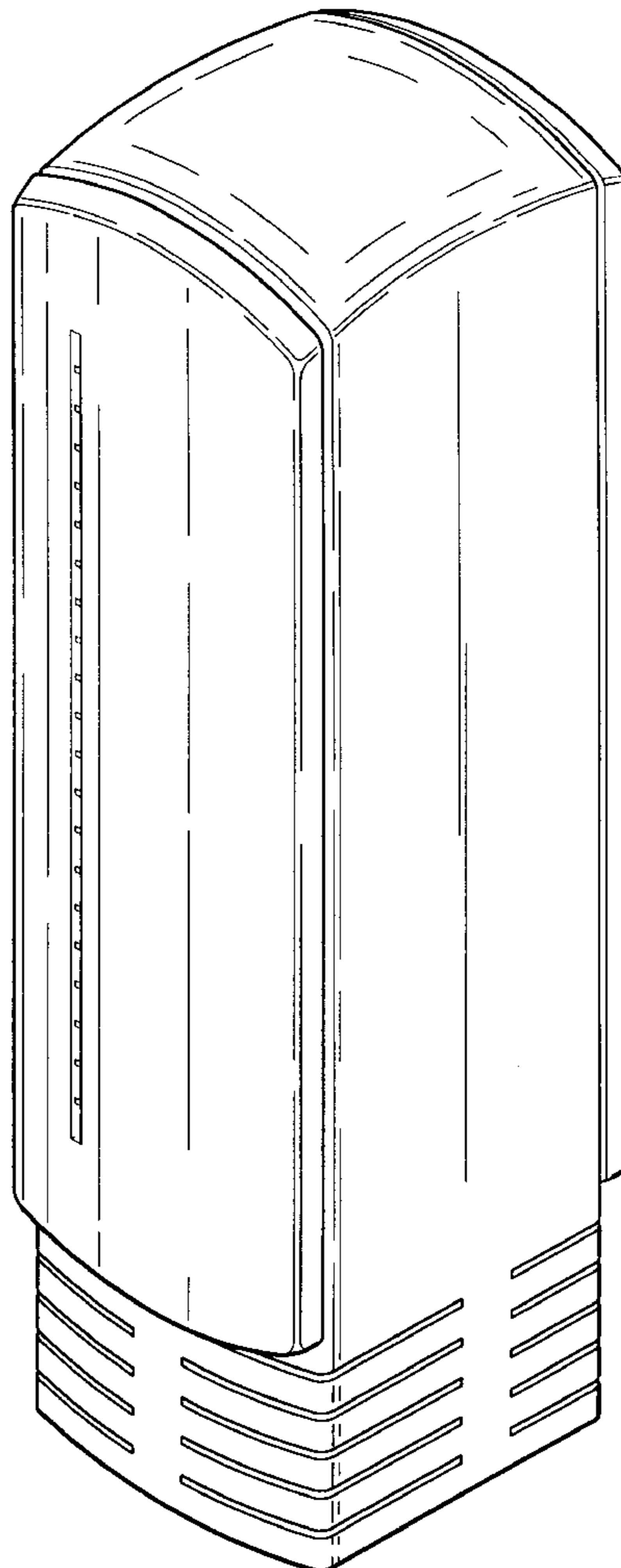
FIG. 5 is a bottom plan view thereof.

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 316,849 5/1991 Houghton D14/102

1 Claim, 1 Drawing Sheet



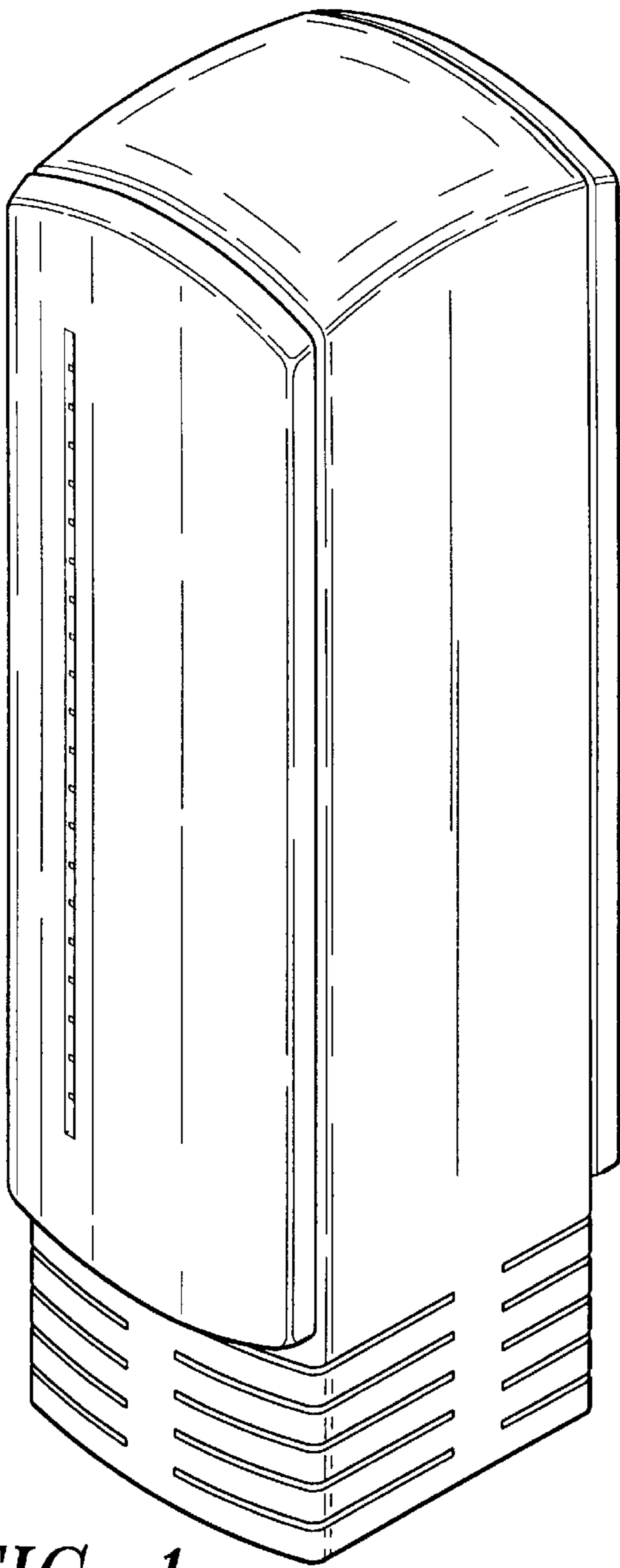


FIG. 1

FIG. 2

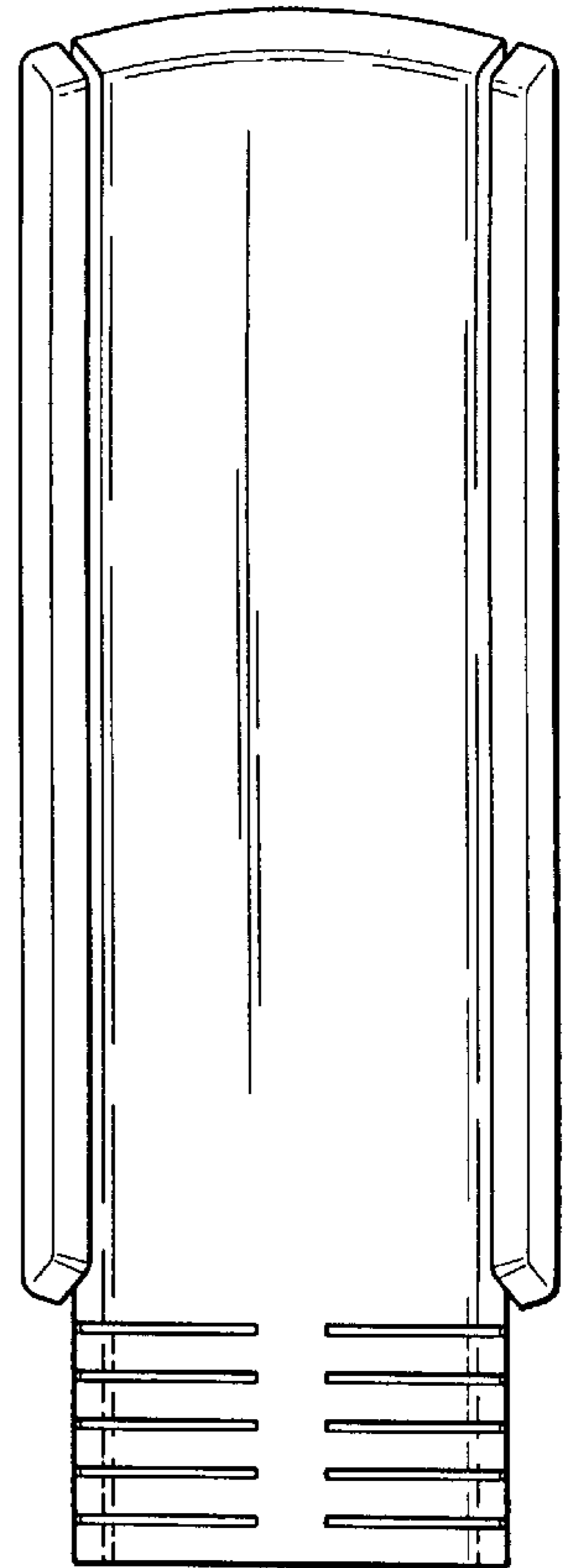


FIG. 3

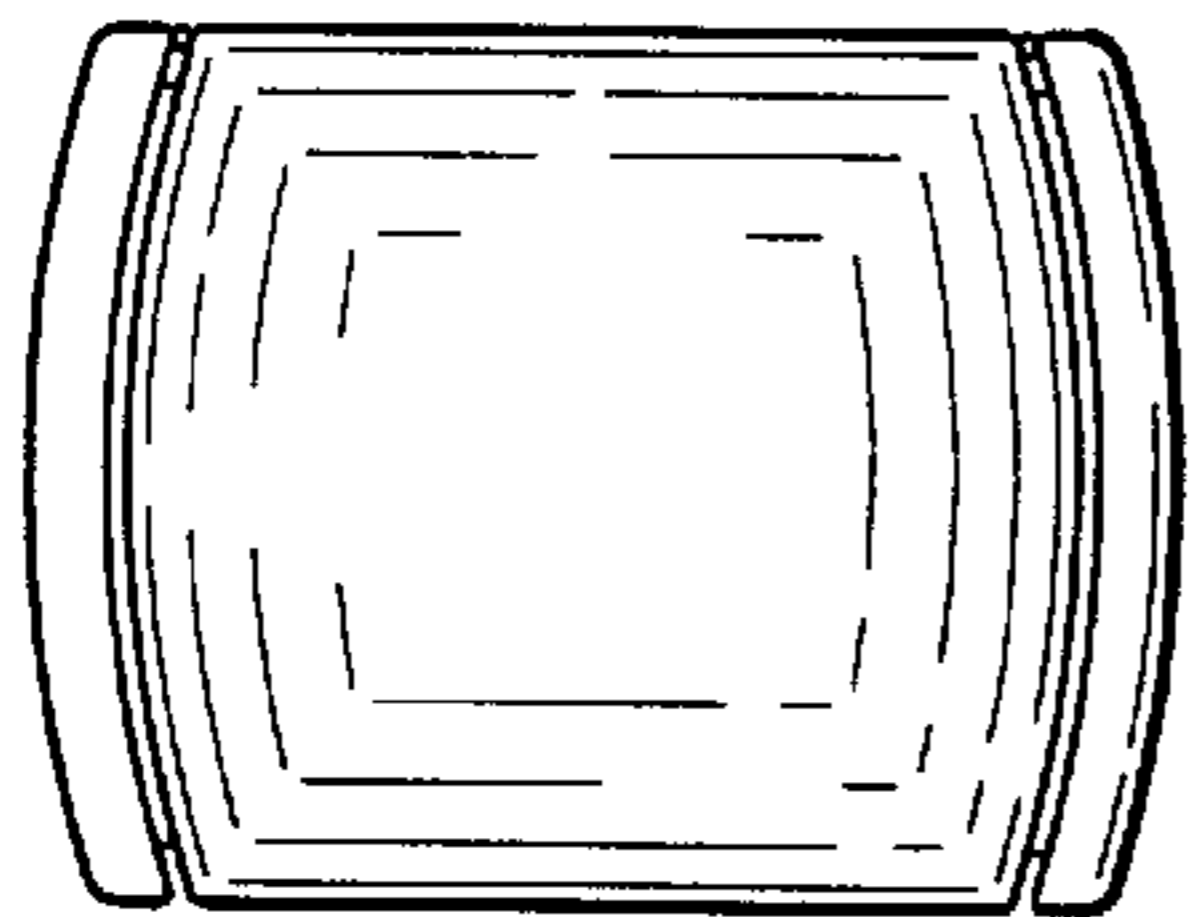
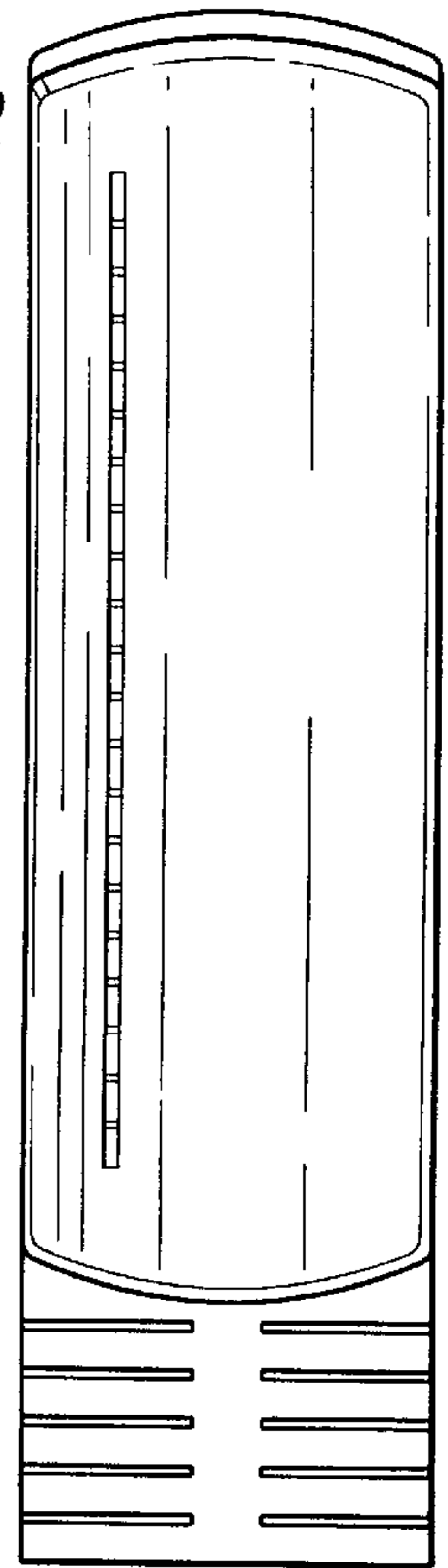


FIG. 4

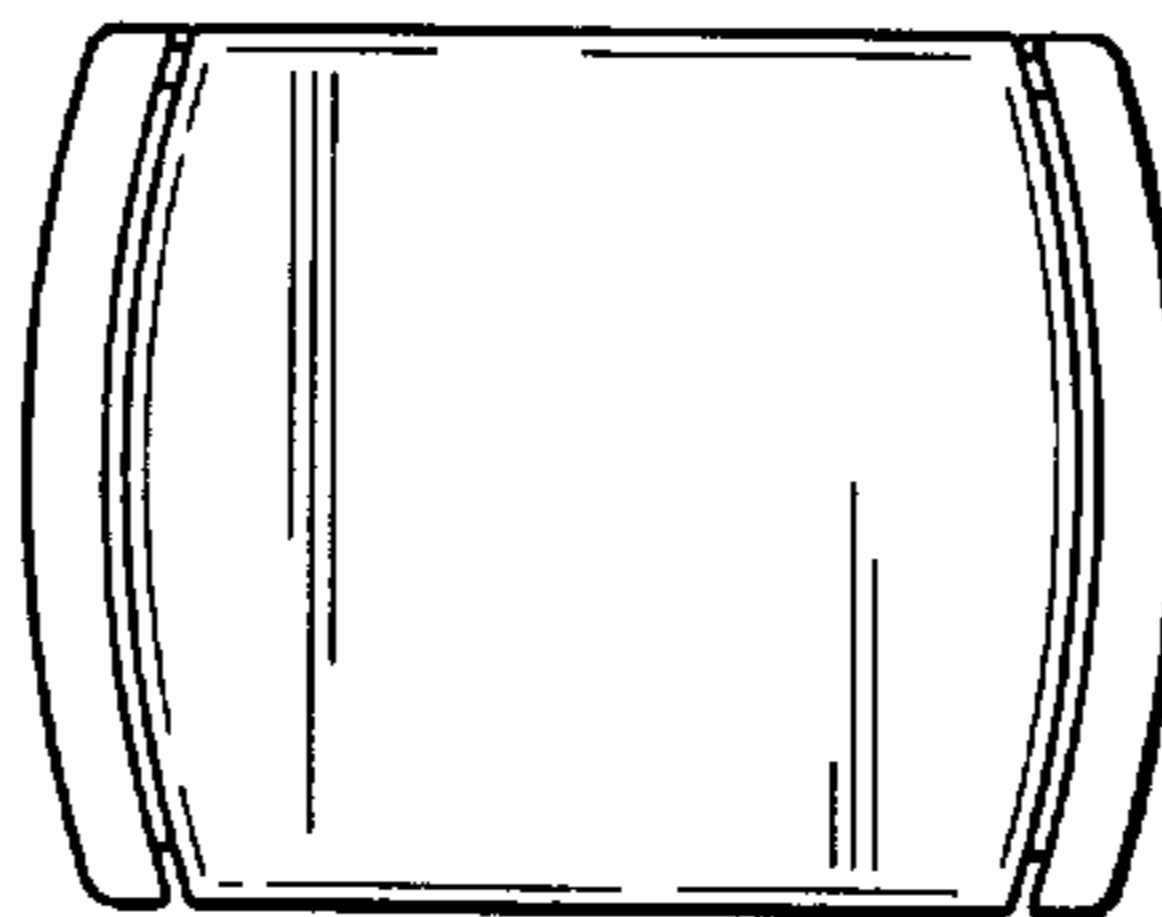


FIG. 5