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# United States Patent [19]

Isonaga

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[45] Date of Patent: **\*\* Oct. 26, 1999**

[54] **COMBINED RADIO RECEIVER AND CLOCK**

[75] Inventor: **Yasuaki Isonaga**, Tokyo, Japan

[73] Assignee: **Sony Corporation**, Tokyo, Japan

[\*\*] Term: **14 Years**

[21] Appl. No.: **29/079,154**

[22] Filed: **Nov. 17, 1997**

[30] **Foreign Application Priority Data**

May 21, 1997 [JP] Japan ..... 9-55002

[51] **LOC (6) Cl.** ..... **14-01**

[52] **U.S. Cl.** ..... **D14/171**

[58] **Field of Search** ..... D10/1, 2, 15; D14/124, D14/162, 168, 170, 171, 181, 188, 189, 192-198; 312/7.1; 455/90, 100, 128, 344, 347, 350, 351

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[57] **CLAIM**

The ornamental design for a combined radio receiver and clock, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a combined radio receiver and clock showing my new design;  
FIG. 2 is a top plan view thereof;  
FIG. 3 is a left side elevational view thereof;  
FIG. 4 is a front elevational view thereof;  
FIG. 5 is a bottom plan view thereof;  
FIG. 6 is a right side elevational view thereof; and,  
FIG. 7 is a rear elevational view thereof.

**1 Claim, 3 Drawing Sheets**

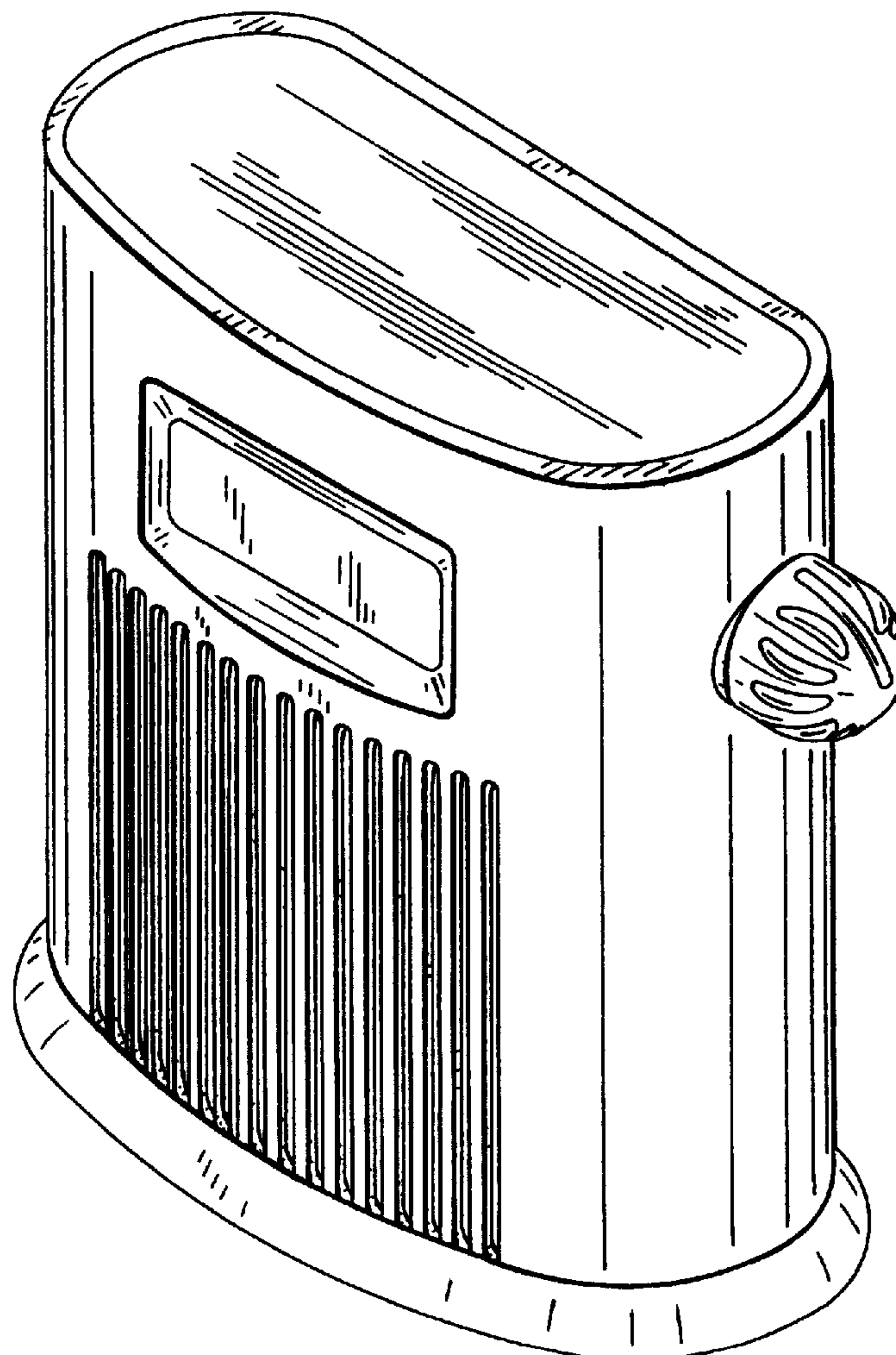


FIG. 1

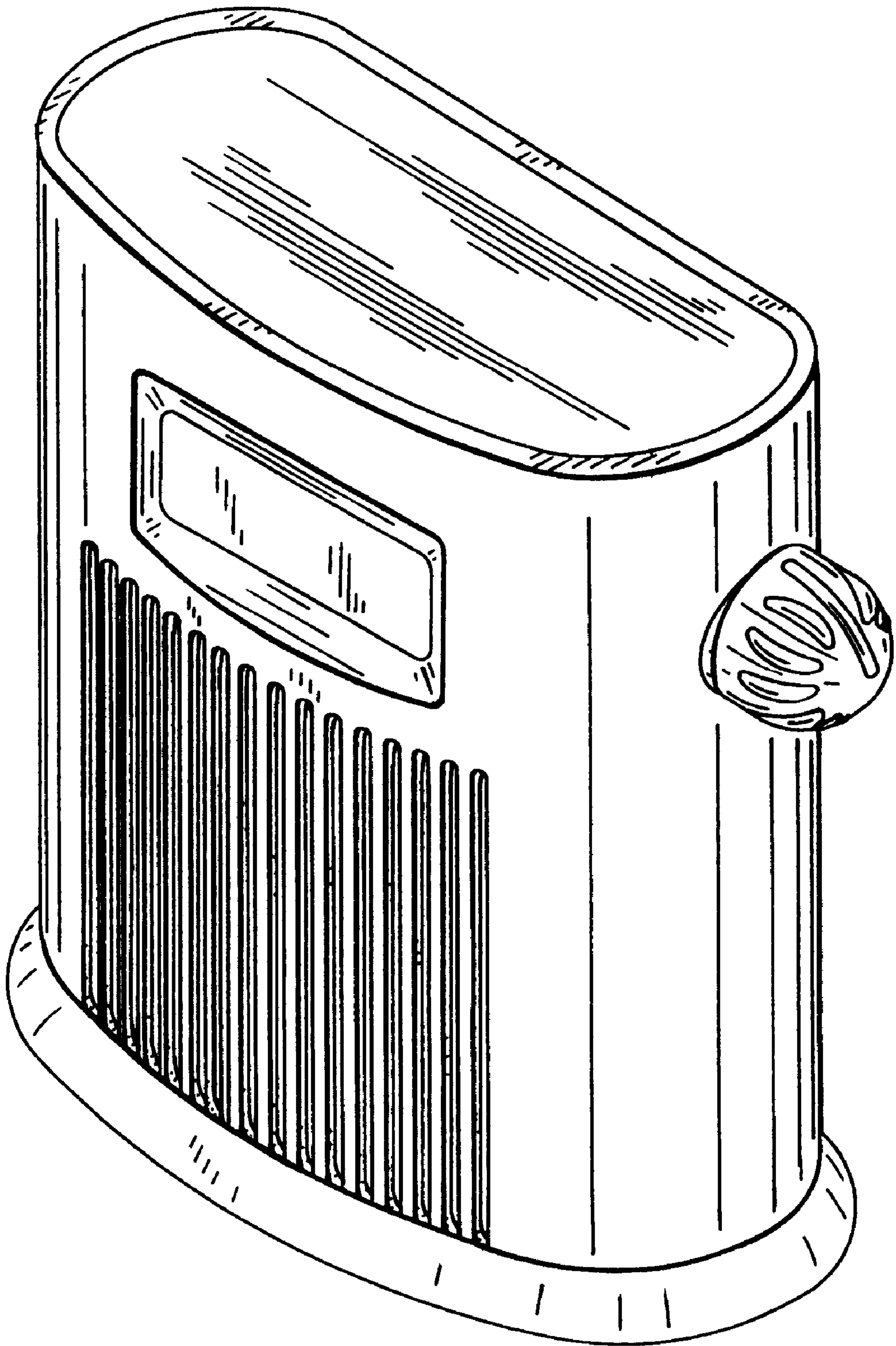


FIG. 2

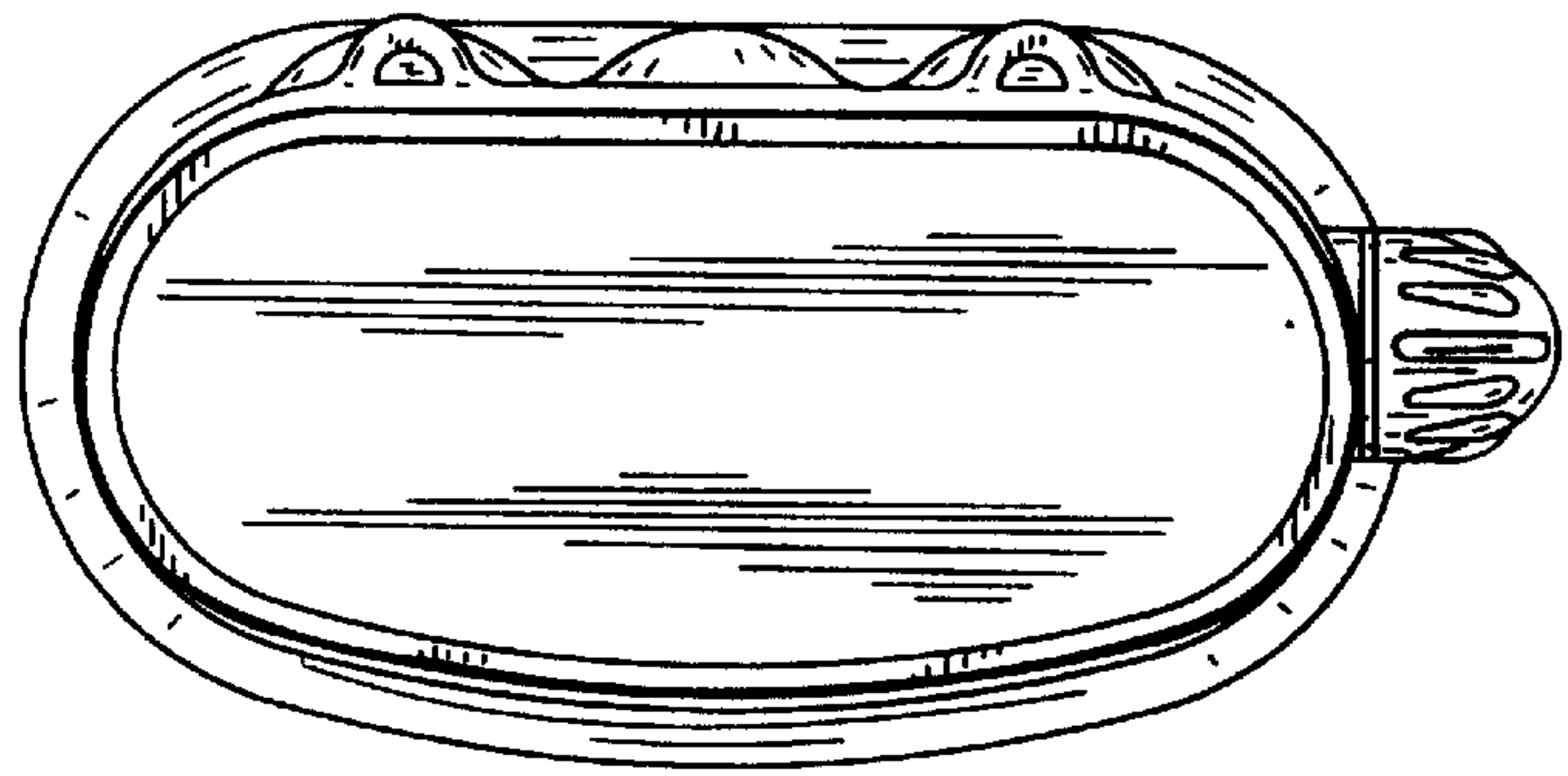


FIG. 3

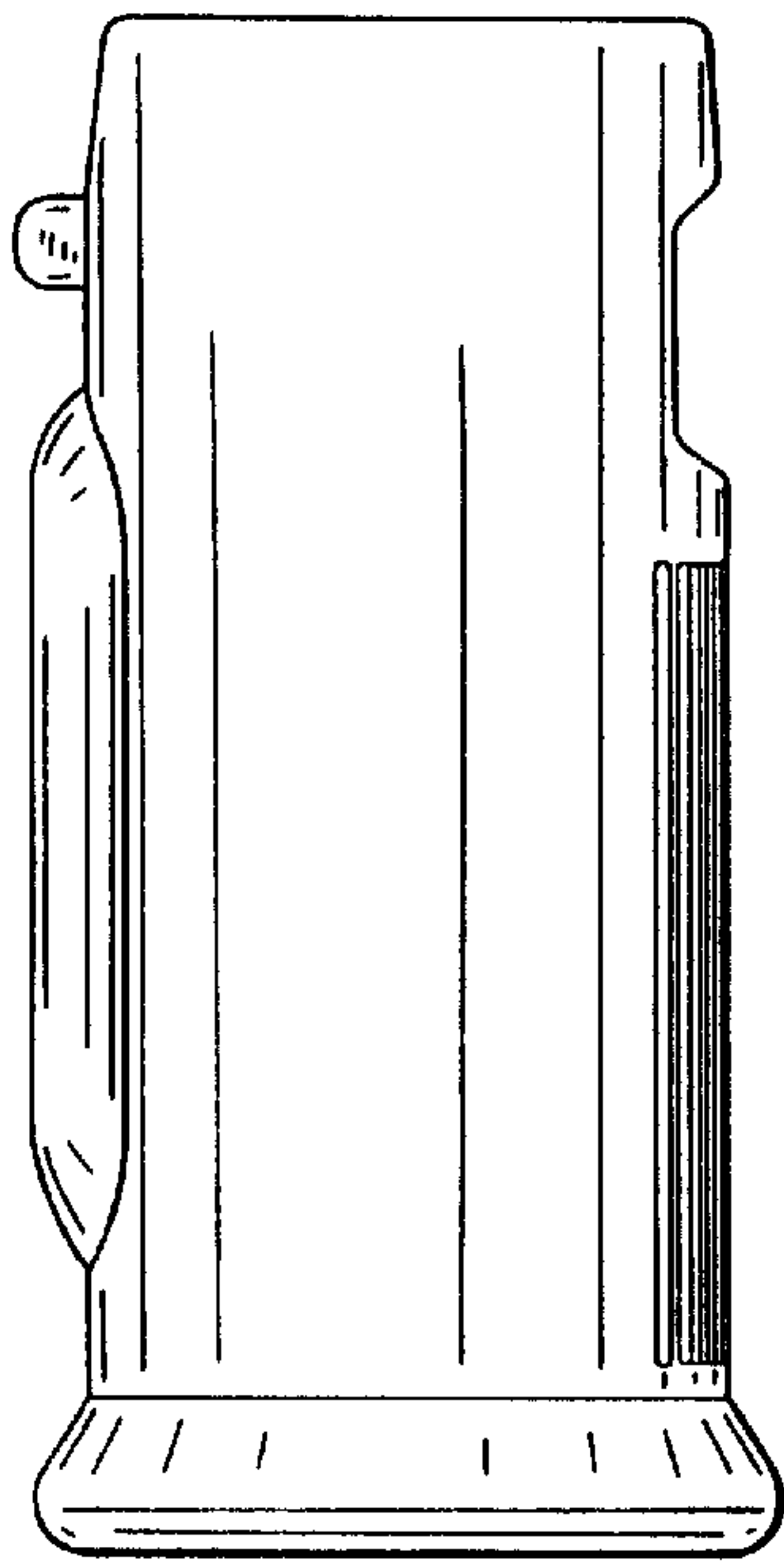


FIG. 4

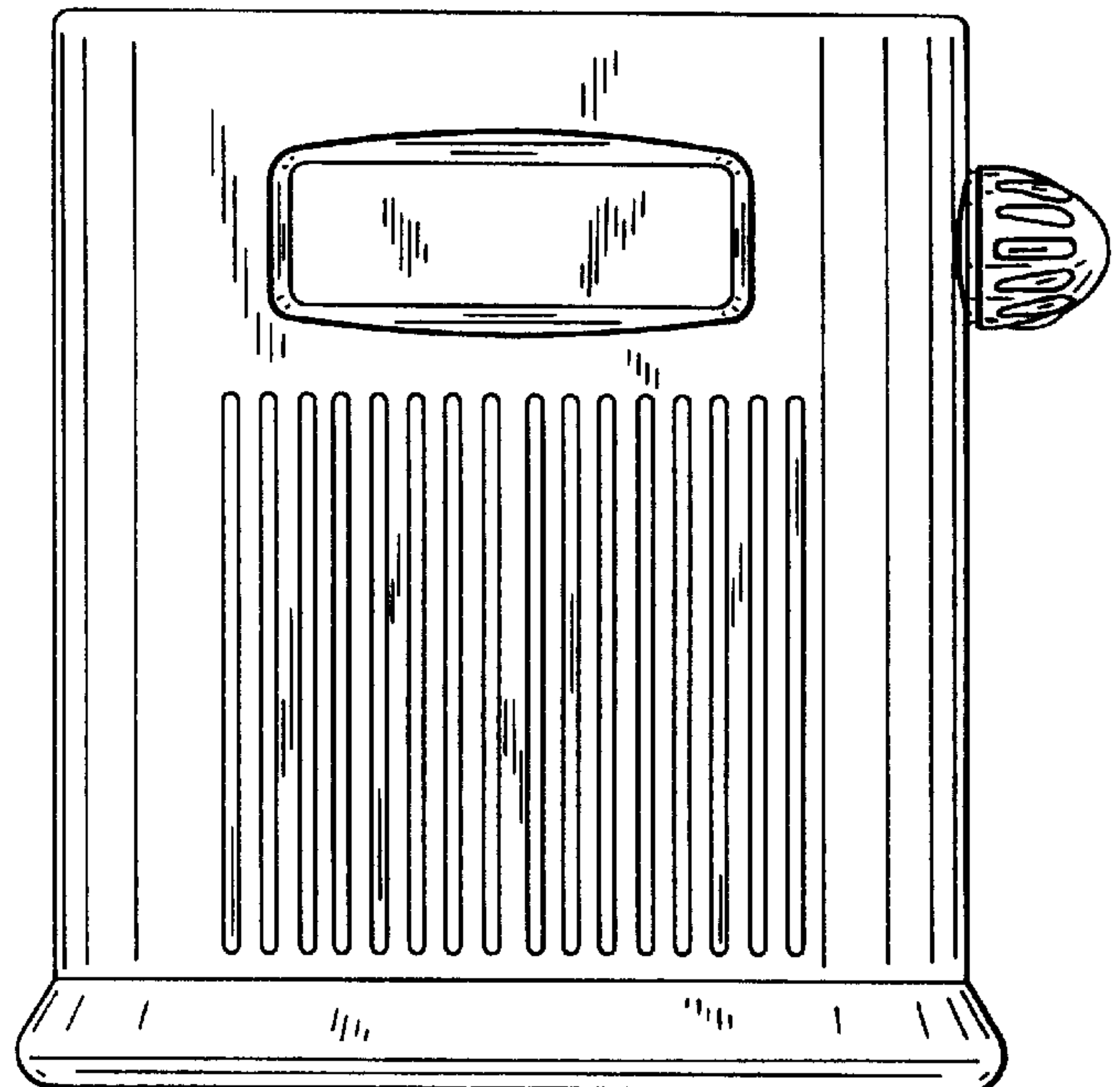


FIG. 6

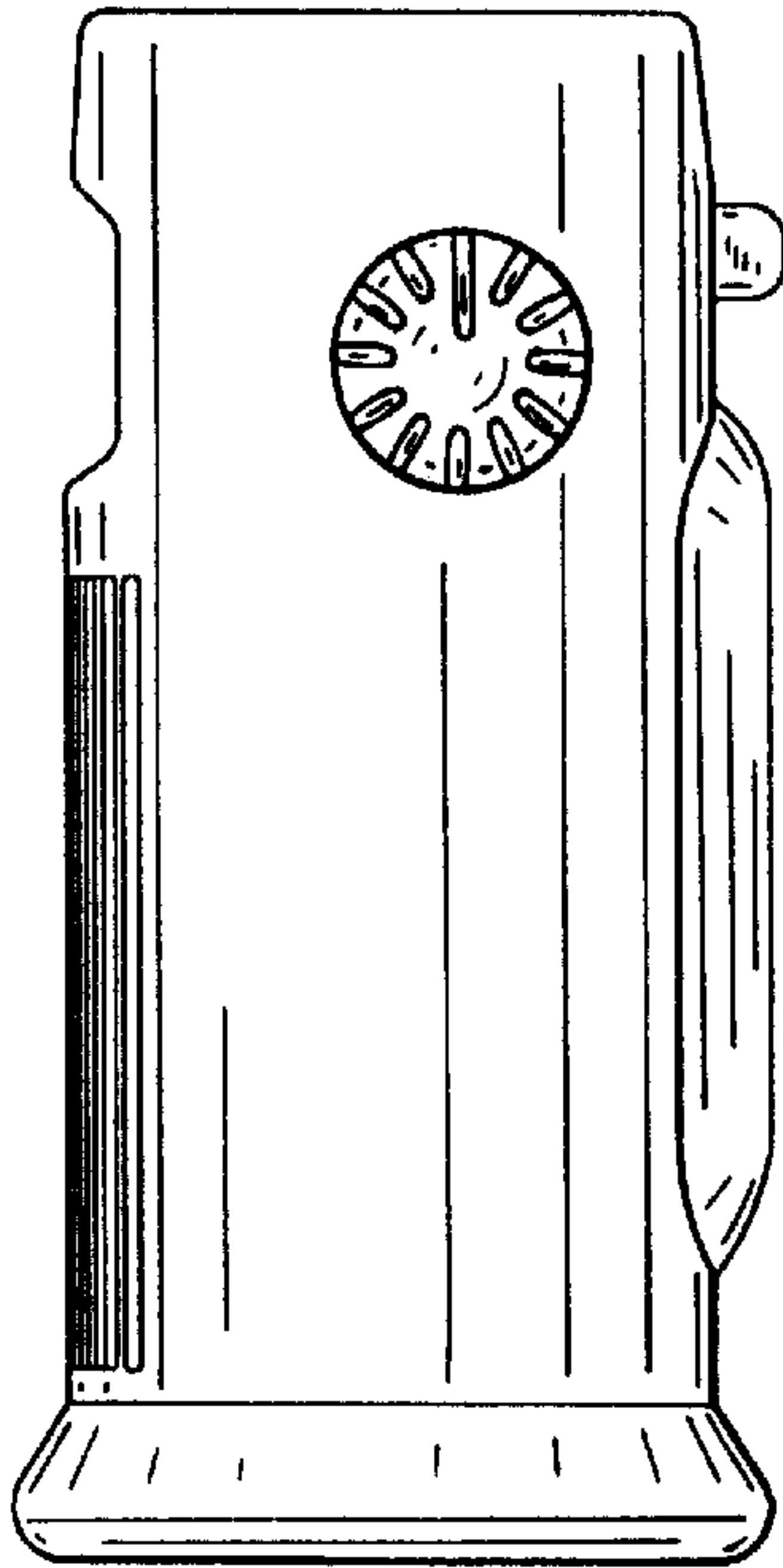


FIG. 7

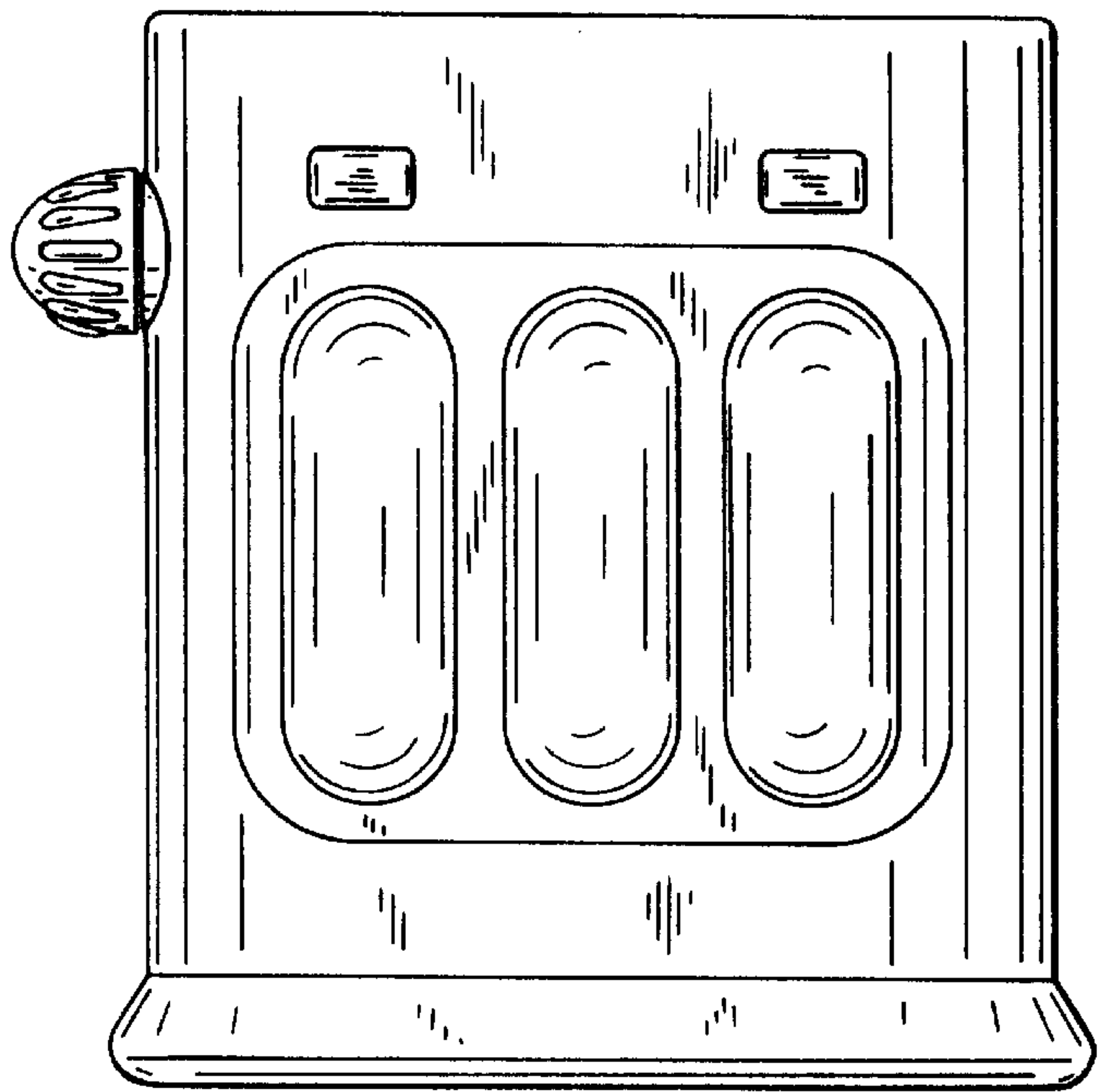


FIG. 5

