



US00D415184S

# United States Patent [19]

Anai et al.

[11] Patent Number: Des. 415,184

[45] Date of Patent: \*\* Oct. 12, 1999

[54] APPARATUS FOR MANUFACTURING A SEMICONDUCTOR FOR A LIQUID CRYSTAL DISPLAY

[75] Inventors: Noriyuki Anai, Goushimachi; Tsutae Omori, Yamanashi-ken; Shinkou Matsumoto, Kumamoto, all of Japan

[73] Assignee: Tokyo Electron Limited, Tokyo, Japan

[\*\*] Term: 14 Years

[21] Appl. No.: 29/090,228

[22] Filed: Jul. 1, 1998

[30] Foreign Application Priority Data

Jan. 30, 1998 [JP] Japan ..... 10-2557

[51] LOC (6) Cl. .... 15-09

[52] U.S. Cl. .... D15/199

[58] Field of Search ..... D15/122, 199; 257/57, 59, 66, 53, 72, 659; 349/141, 139, 43, 38, 122, 145, 146, 138; 438/162, 166, 482, 30, 149, 158, 763

[56] References Cited

U.S. PATENT DOCUMENTS

- D. 309,472 7/1990 Nishida ..... D15/147
- D. 333,144 2/1993 Koizumi ..... D15/199
- D. 365,584 12/1995 Nakagome et al. .... D15/199

Primary Examiner—Antoine Duval Davis  
Attorney, Agent, or Firm—Oblon, Spivak, McClelland, Maier & Neustadt, P.C.

[57] CLAIM

The ornamental design for apparatus for manufacturing a semiconductor for a liquid crystal display, as shown.

DESCRIPTION

FIG. 1 is a front elevational view of apparatus for manufacturing a semiconductor for a liquid crystal display, showing our new design;

FIG. 2 is a rear elevational view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof; and,

FIG. 6 is a bottom plan view thereof.

1 Claim, 2 Drawing Sheets

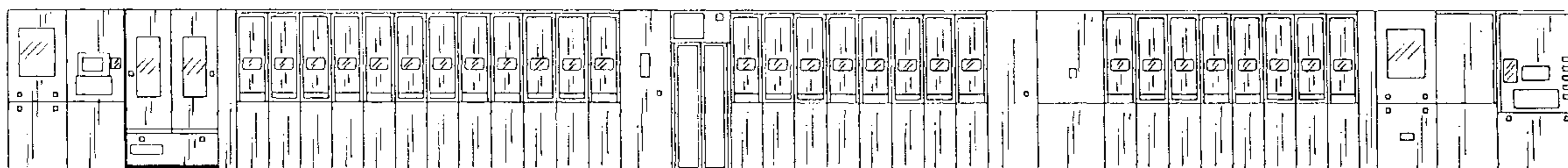


FIG.1

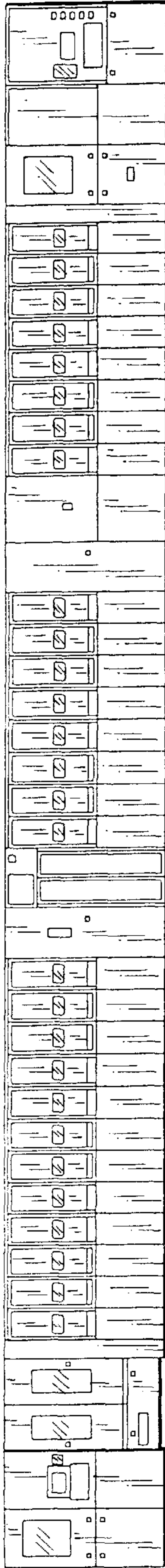


FIG.2

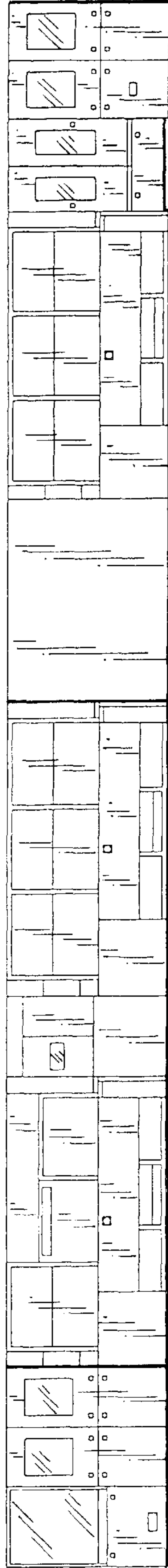


FIG.3

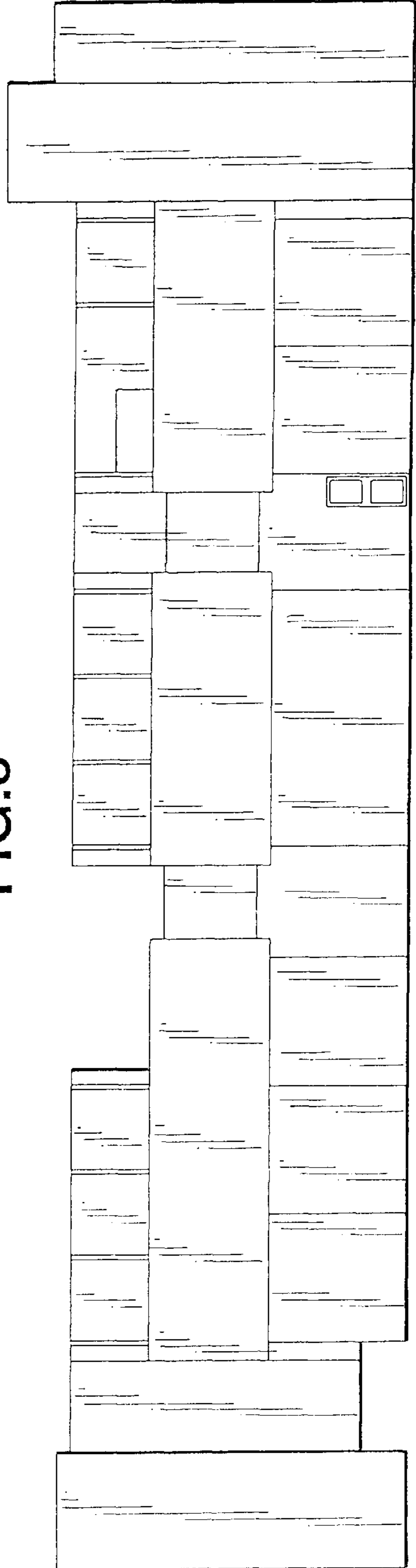


FIG.5

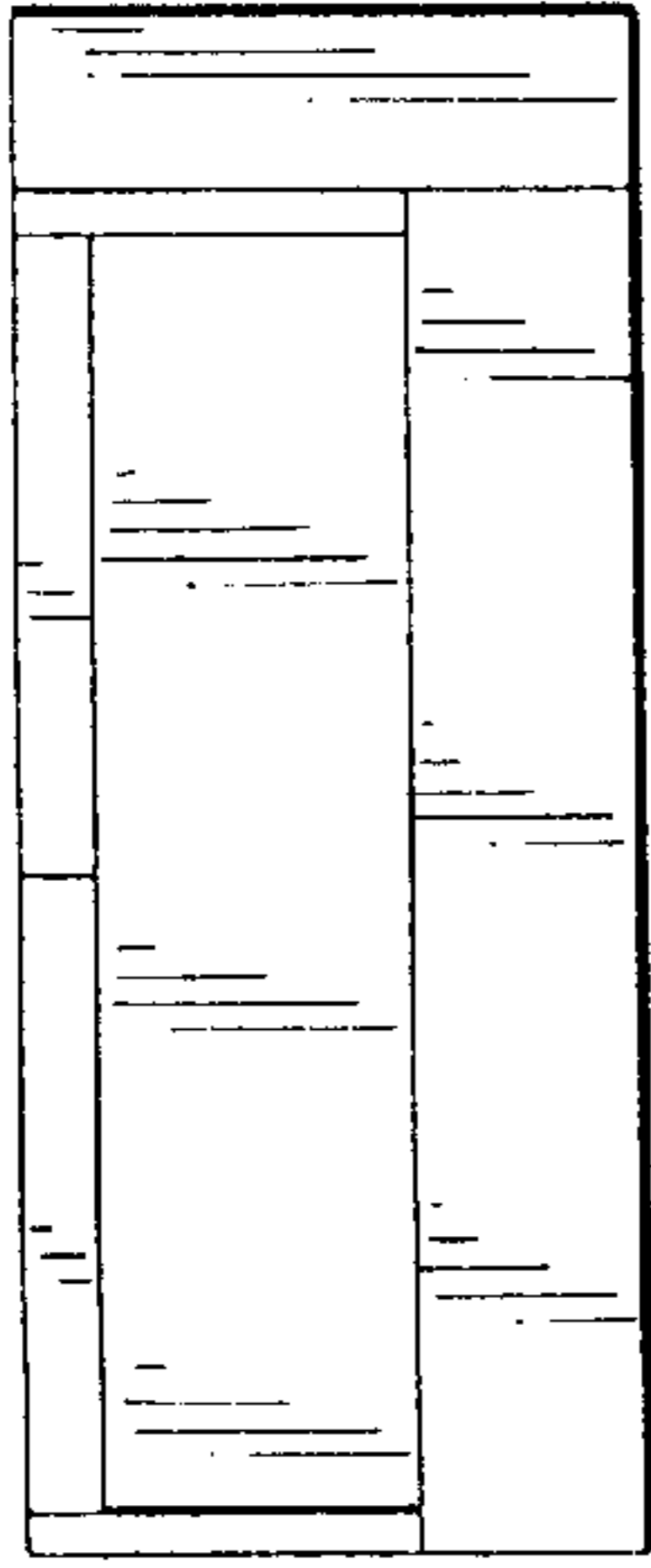


FIG.4

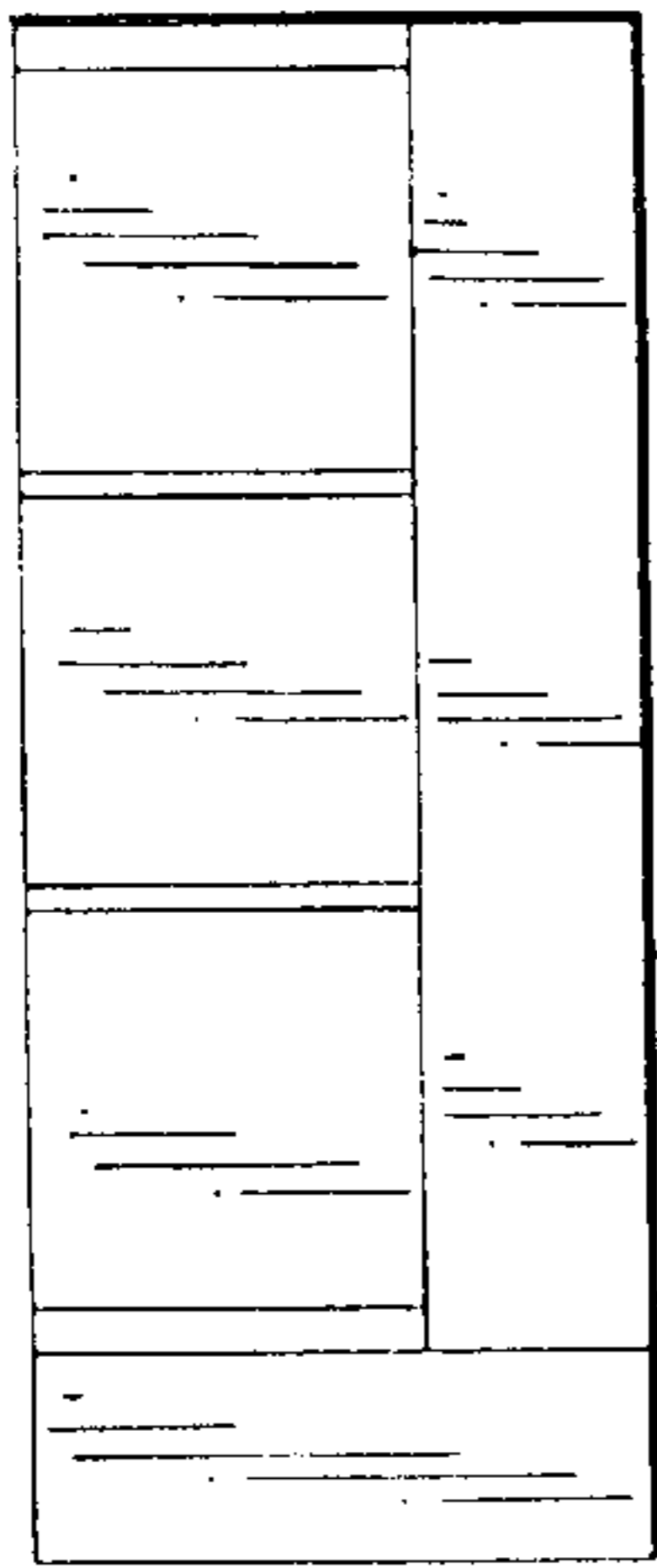


FIG.6

