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United States Patent [19]

Wetherell et al.

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[45] Date of Patent: **** Oct. 5, 1999**

[54] **PROCESSOR**

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[73] Assignee: **Orban, Inc.**, San Leandro, Calif.

[**] Term: **14 Years**

[21] Appl. No.: **29/069,255**

[22] Filed: **Mar. 20, 1997**

[51] **LOC (6) Cl.** **14-03**

[52] **U.S. Cl.** **D14/198**

[58] **Field of Search** D14/124, 137,
D14/156, 157, 162, 164, 168, 188, 193-198,
265; 455/344, 347, 350, 351

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 234,046 1/1975 Taguchi et al. D14/198

D. 327,688 7/1992 Ito et al. D14/188
D. 328,073 7/1992 Yamakawa D14/198 X
D. 349,719 8/1994 Ohta D14/188 X
D. 375,956 11/1996 Shintani D14/198 X

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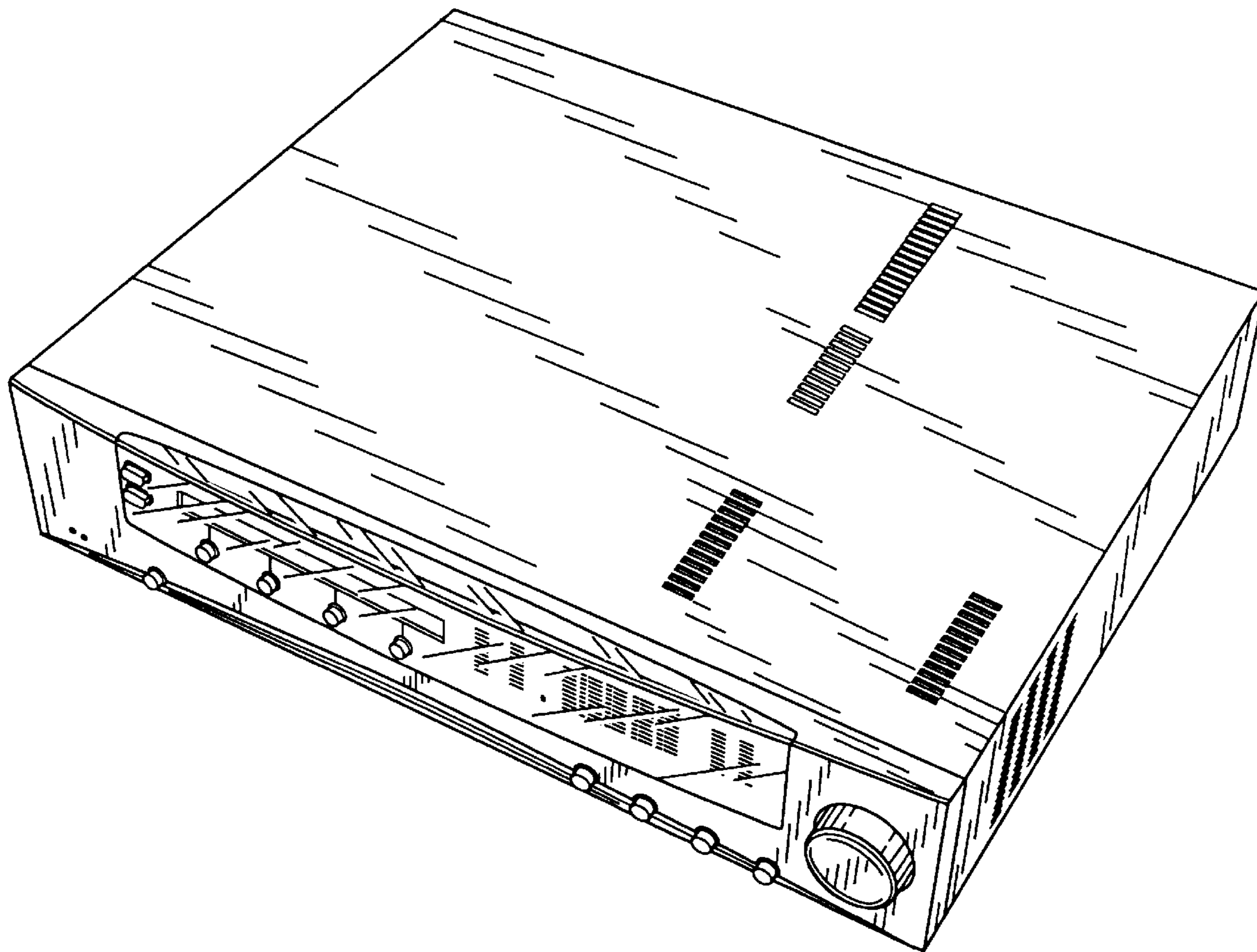
[57] **CLAIM**

The ornamental design for the processor, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a processor showing our new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a bottom plan view thereof;
FIG. 6 is a right side elevational view thereof; and,
FIG. 7 is a left side elevational view thereof.

1 Claim, 7 Drawing Sheets



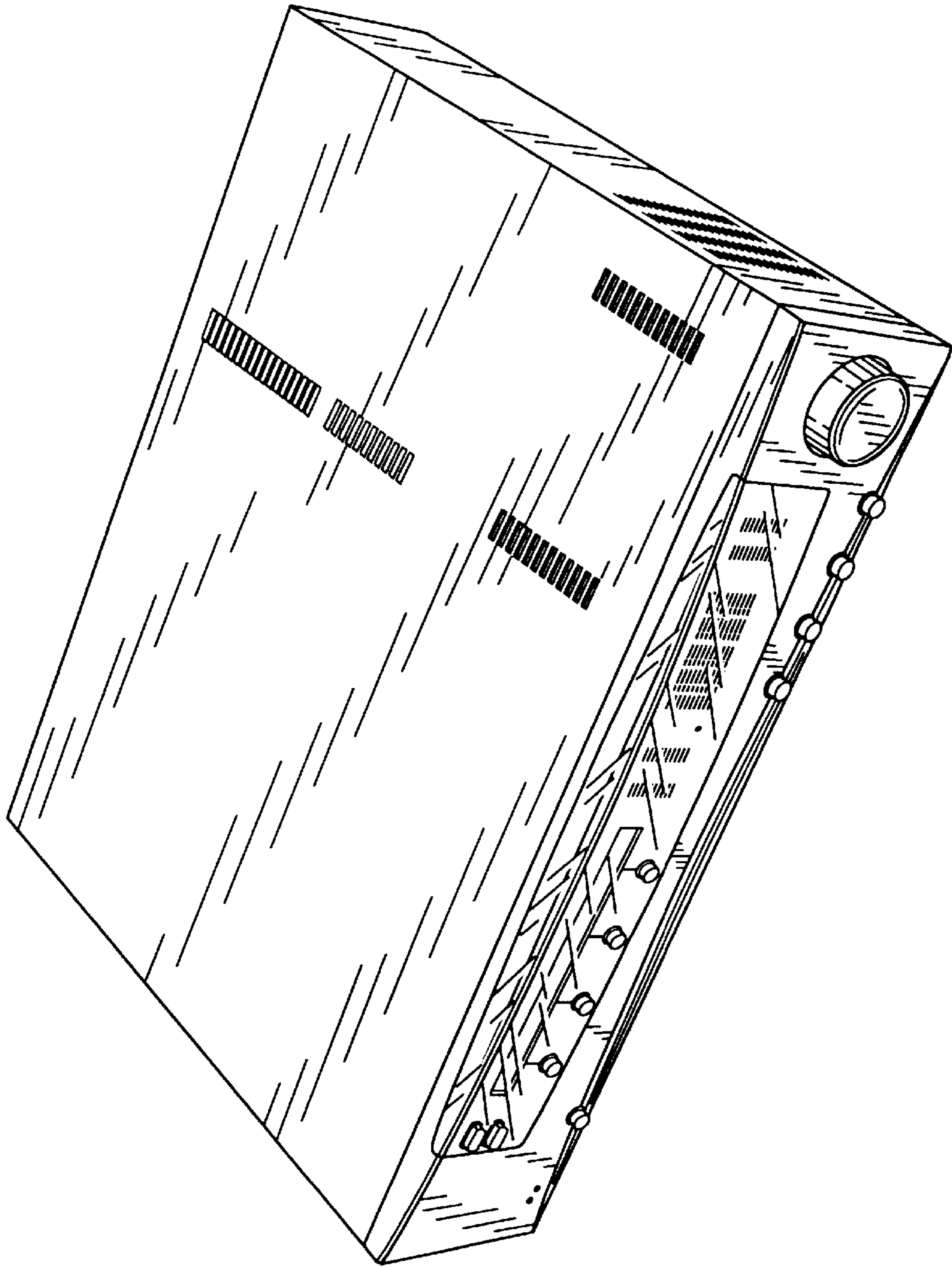


FIG. 1

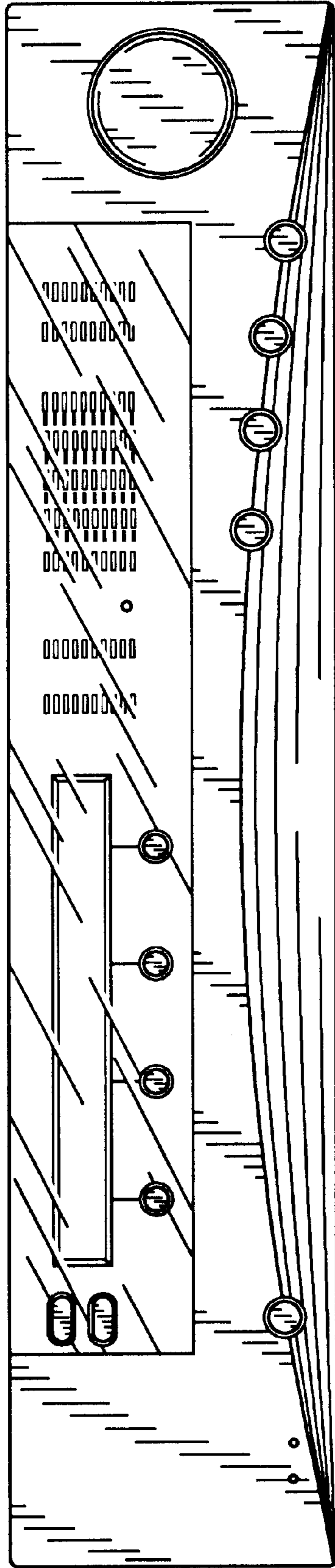


FIG. 2

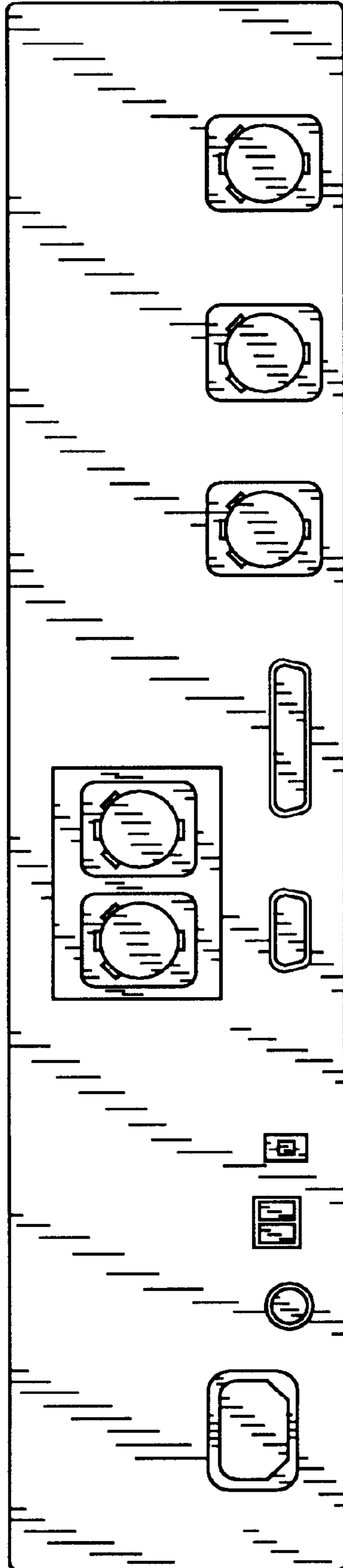


FIG. 3

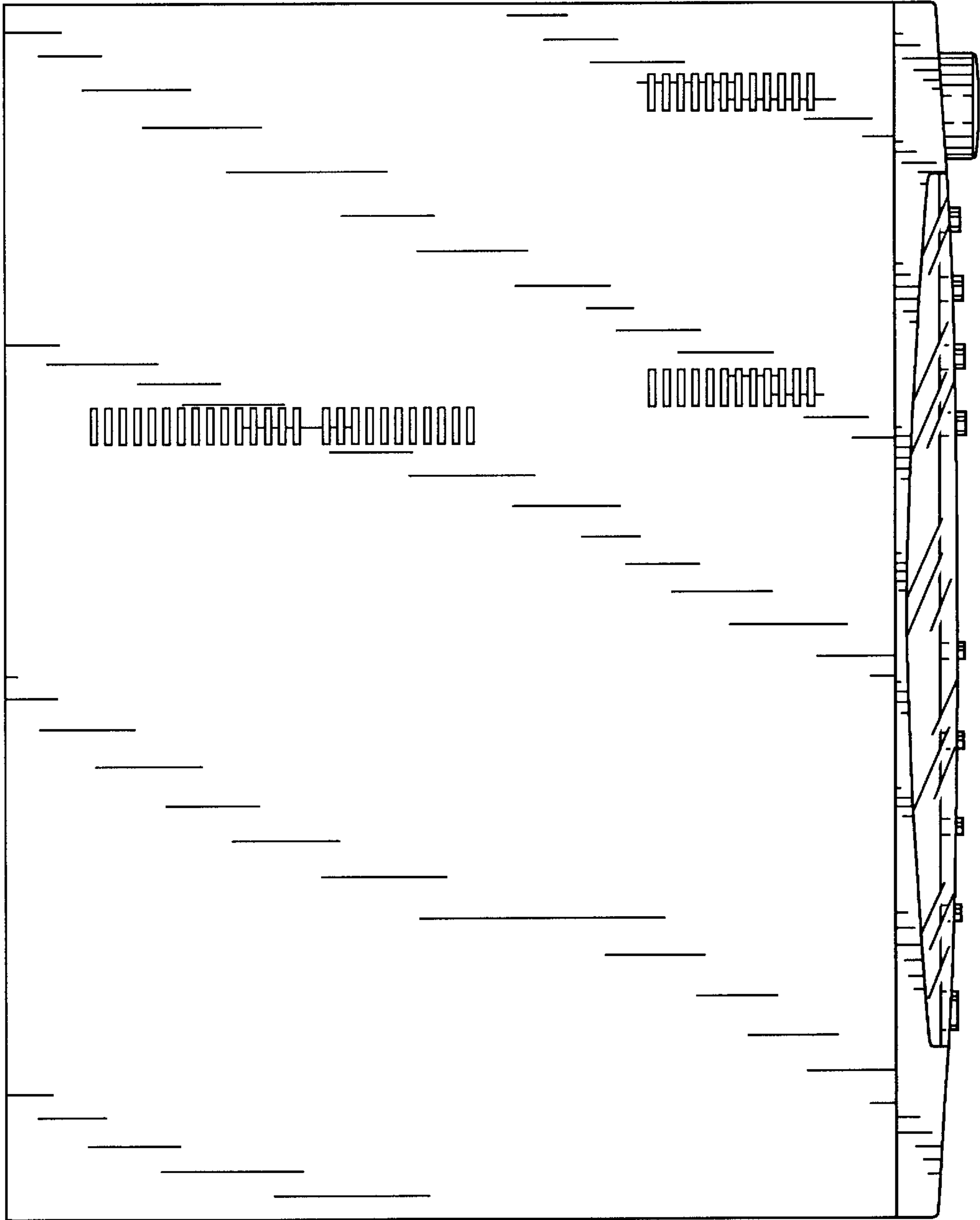


FIG. 4

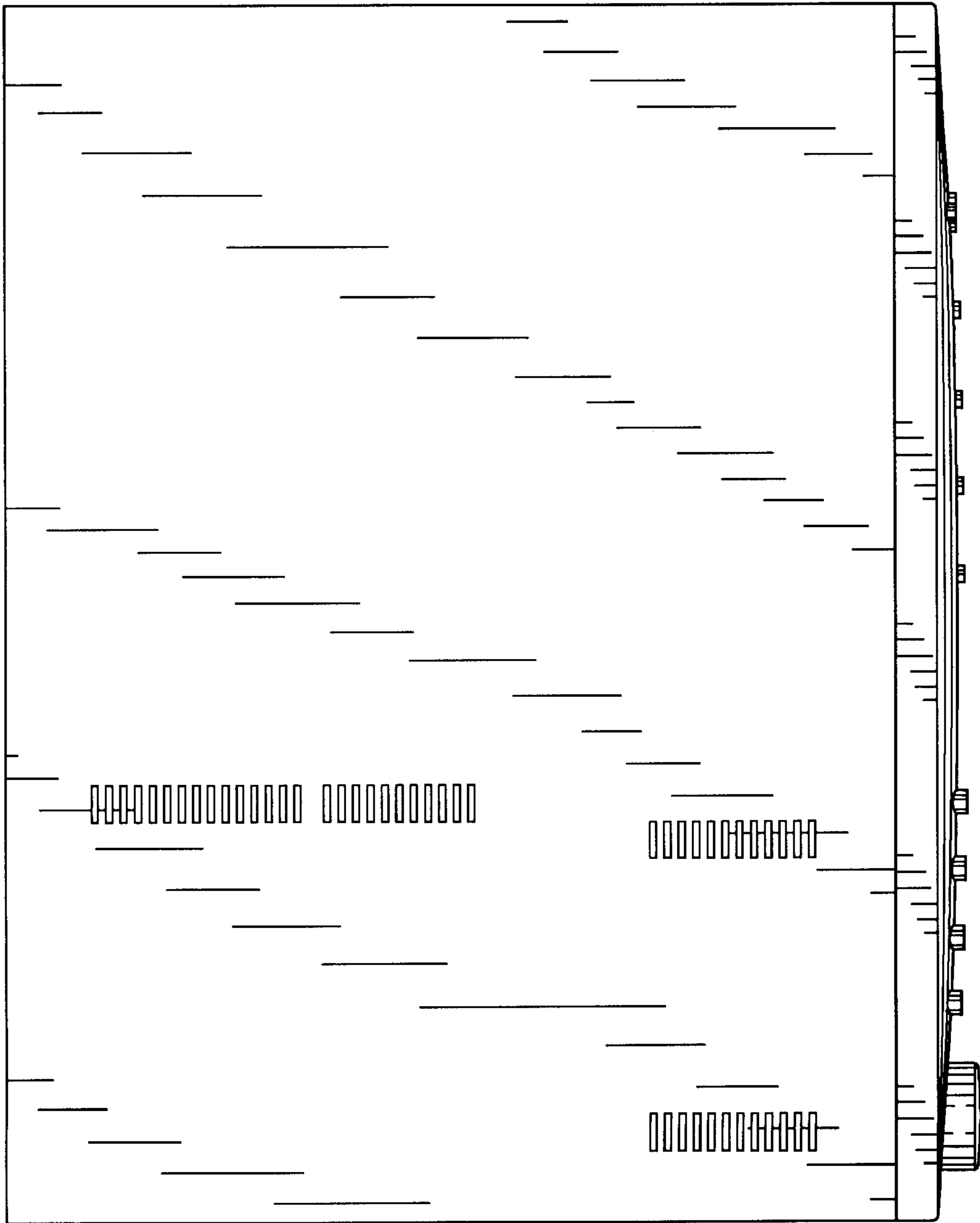


FIG. 5

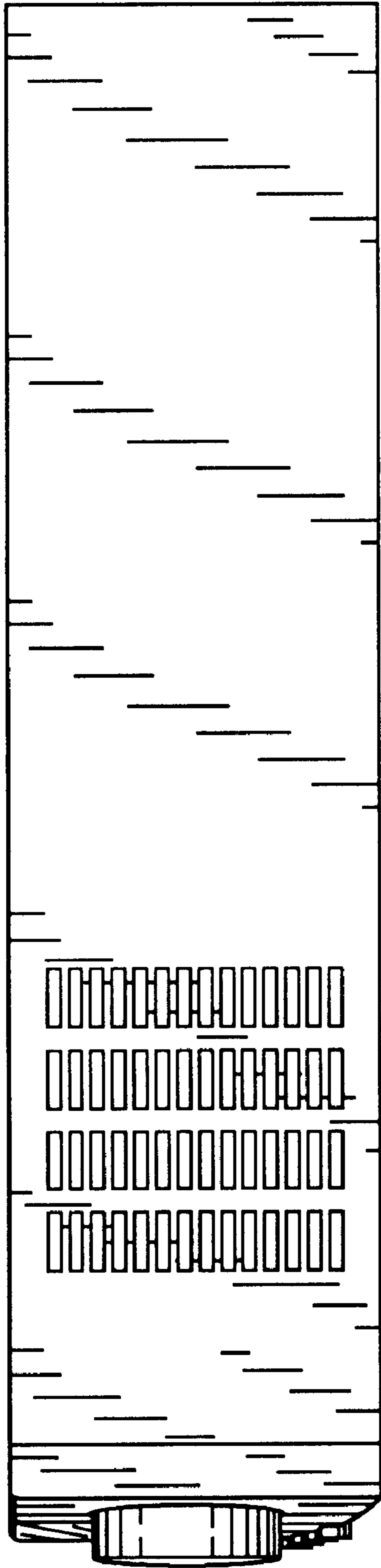


FIG. 6

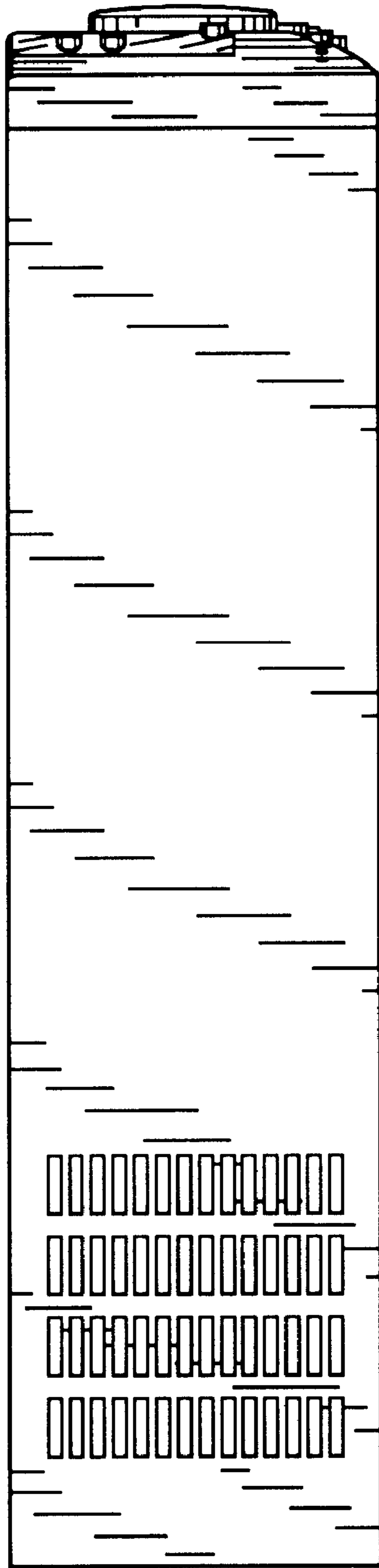


FIG. 7