



US00D413862S

United States Patent [19]

[11] Patent Number: Des. 413,862

Huang et al.

[45] Date of Patent: ** Sep. 14, 1999

[54] GROUND FAULT CIRCUIT INTERRUPTER

[56] References Cited

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U.S. PATENT DOCUMENTS

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[**] Term: **14 Years**

[57] CLAIM

[21] Appl. No.: **29/087,738**

An ornamental design for a ground fault circuit interrupter, as shown and described.

[22] Filed: **May 8, 1998**

[30] Foreign Application Priority Data

DESCRIPTION

Nov. 11, 1997 [CN] China 97329371.3

FIG. 1 is a bottom plan view;
FIG. 2 is a front elevation view;
FIG. 3 is a top plan view;
FIG. 4 is a right side elevation view;
FIG. 5 is a left side elevation view; and,
FIG. 6 is a rear elevation view.

[51] LOC (6) Cl. **13-03**

[52] U.S. Cl. **D13/139.3**

[58] Field of Search 307/125; 250/302.2;
439/538, 106-107, 135, 136, 144, 651,
535, 536, 650; 174/66, 67, 53; 220/3.2;
361/42; D13/139.1-139.8

1 Claim, 2 Drawing Sheets

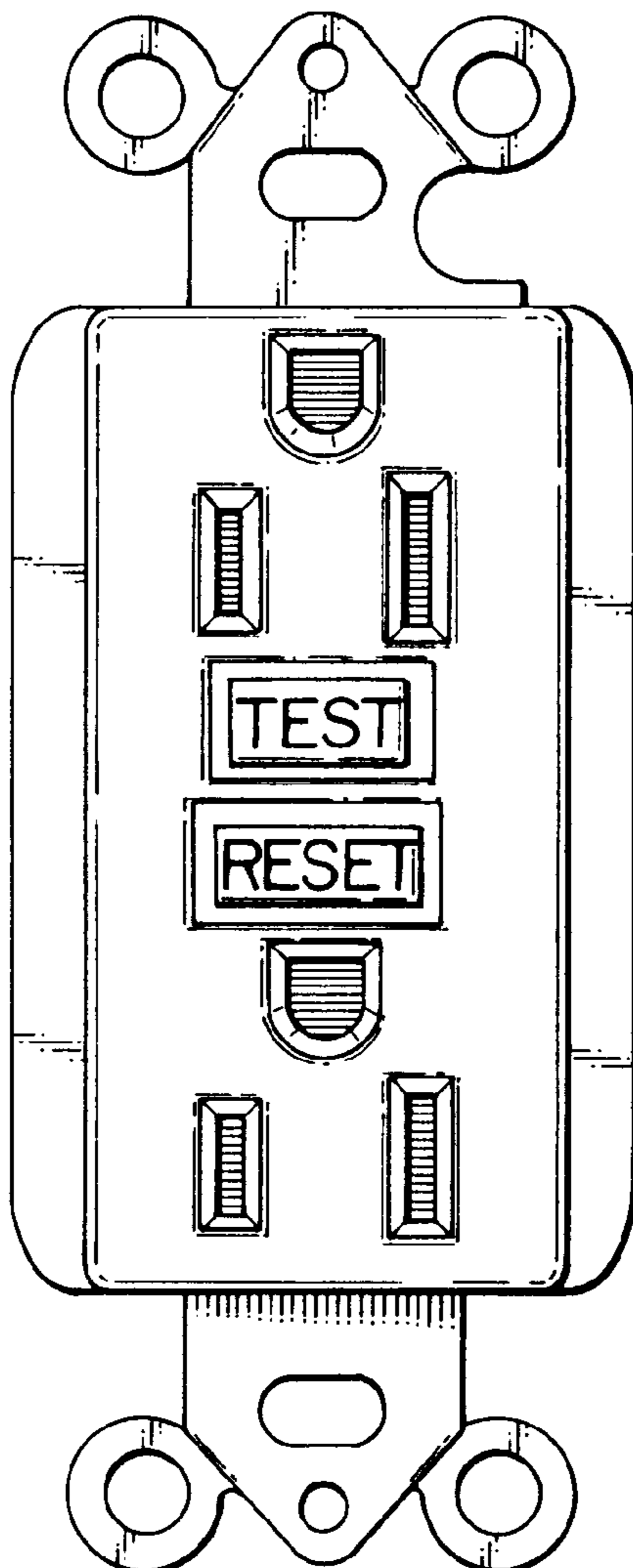


FIG. 1

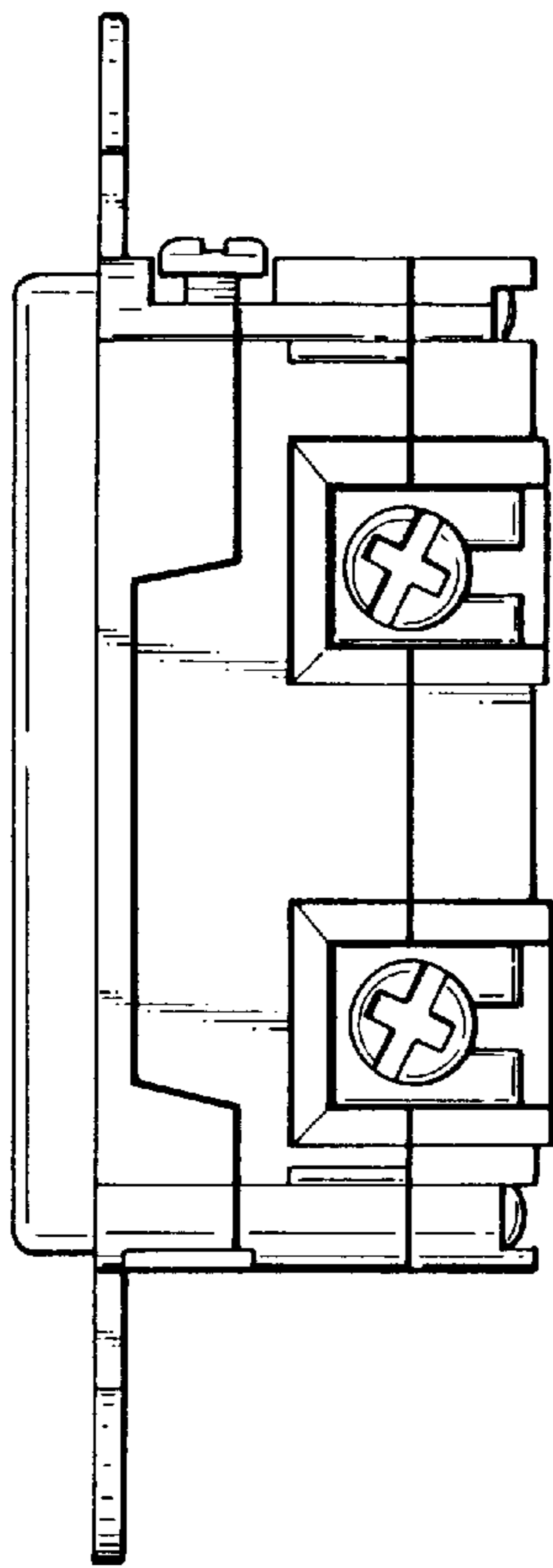
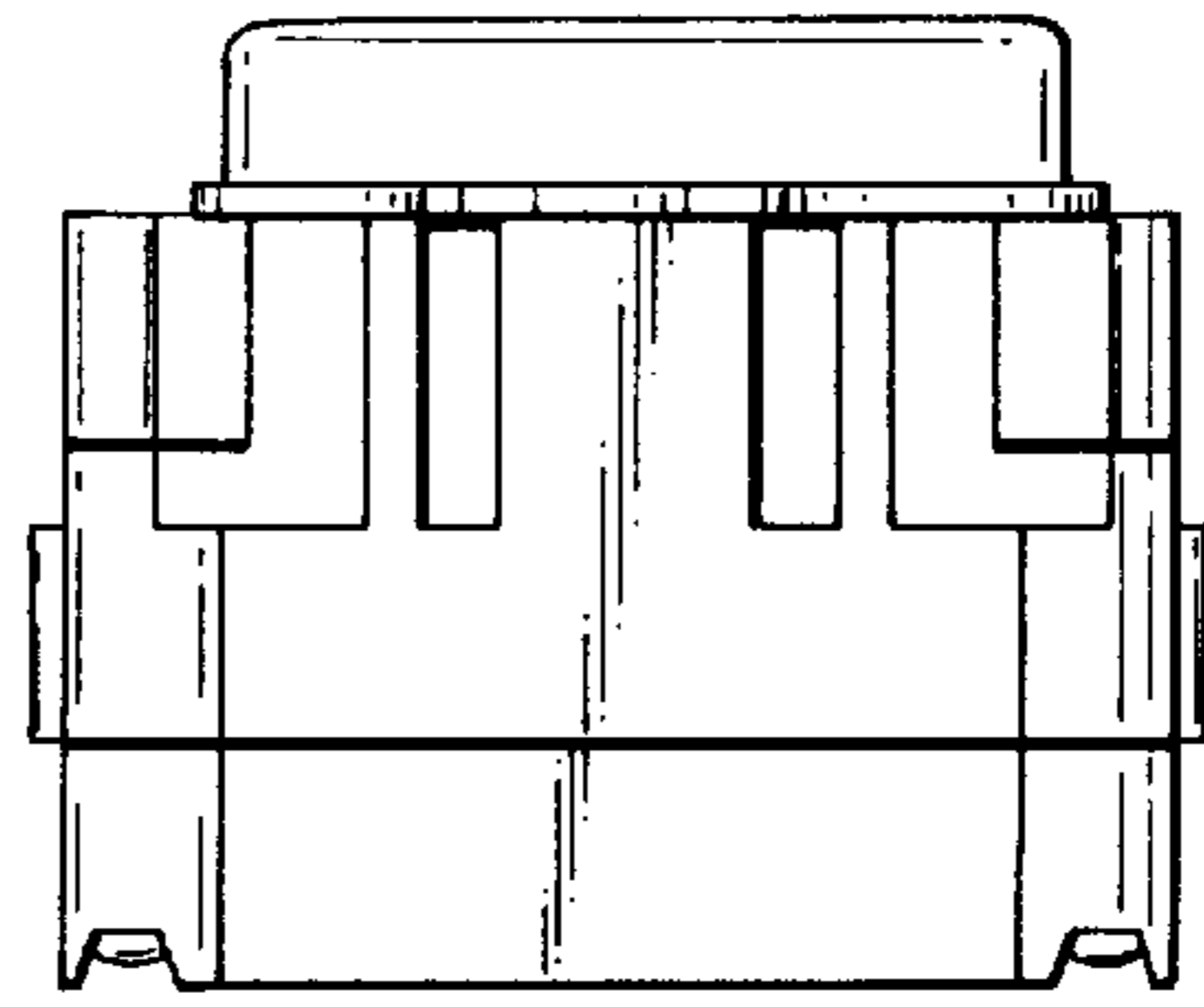


FIG. 4

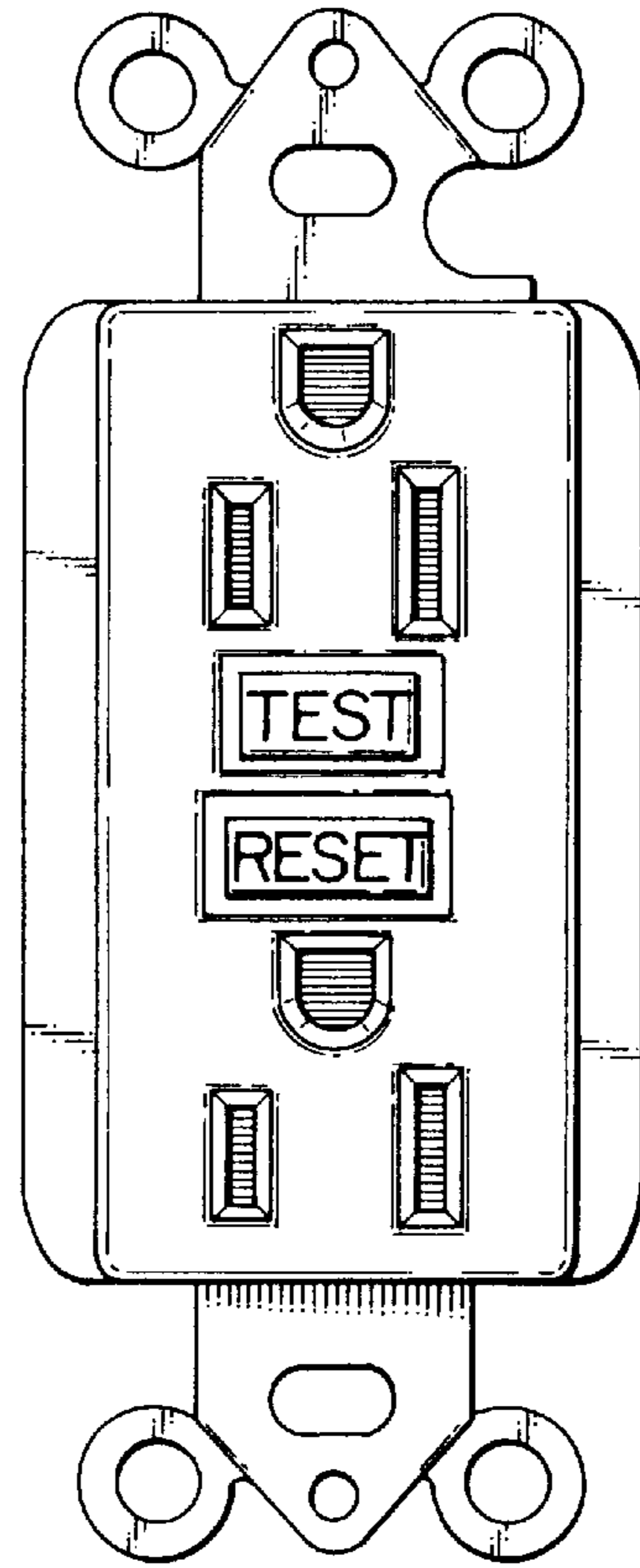
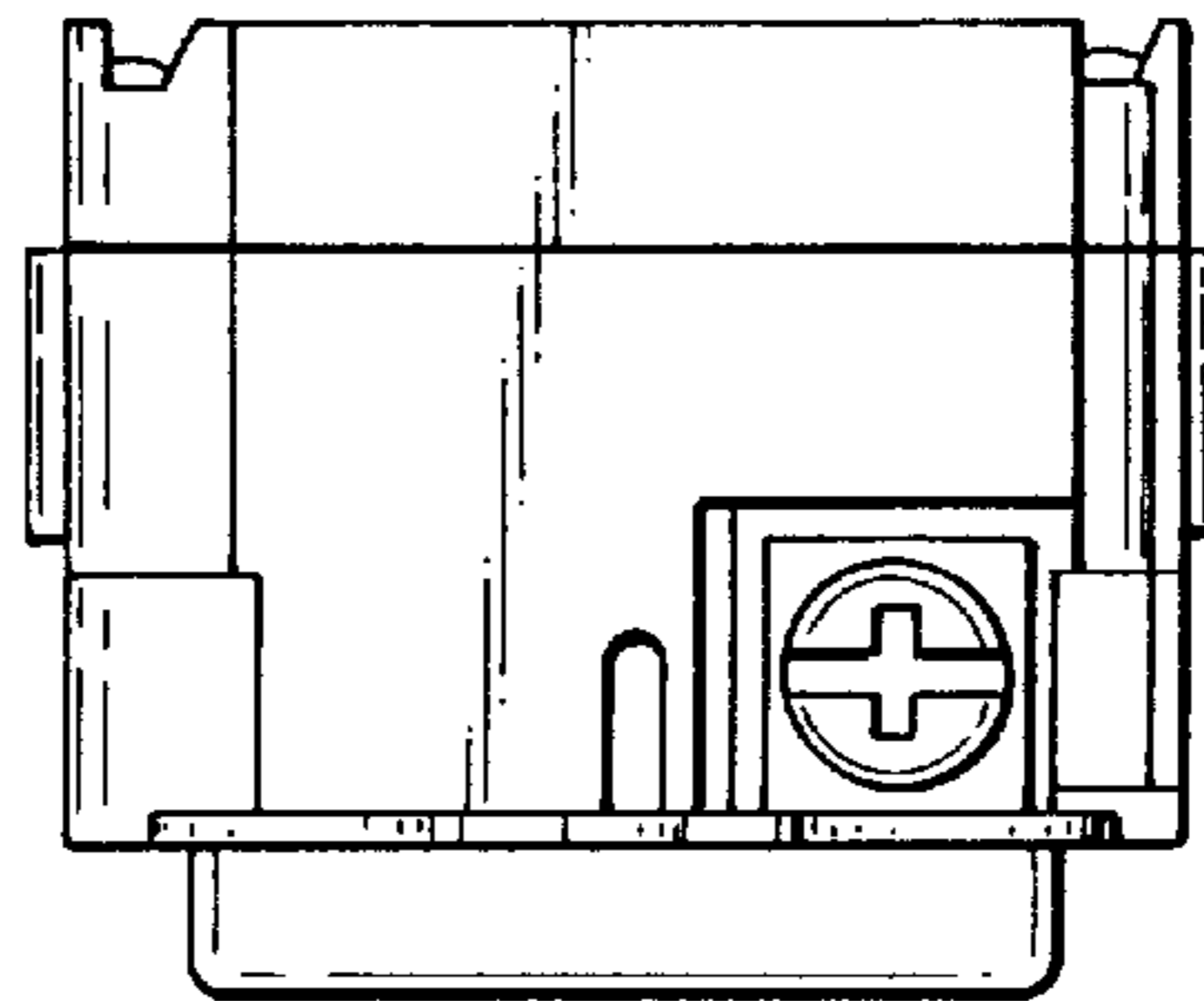


FIG. 2

FIG. 3



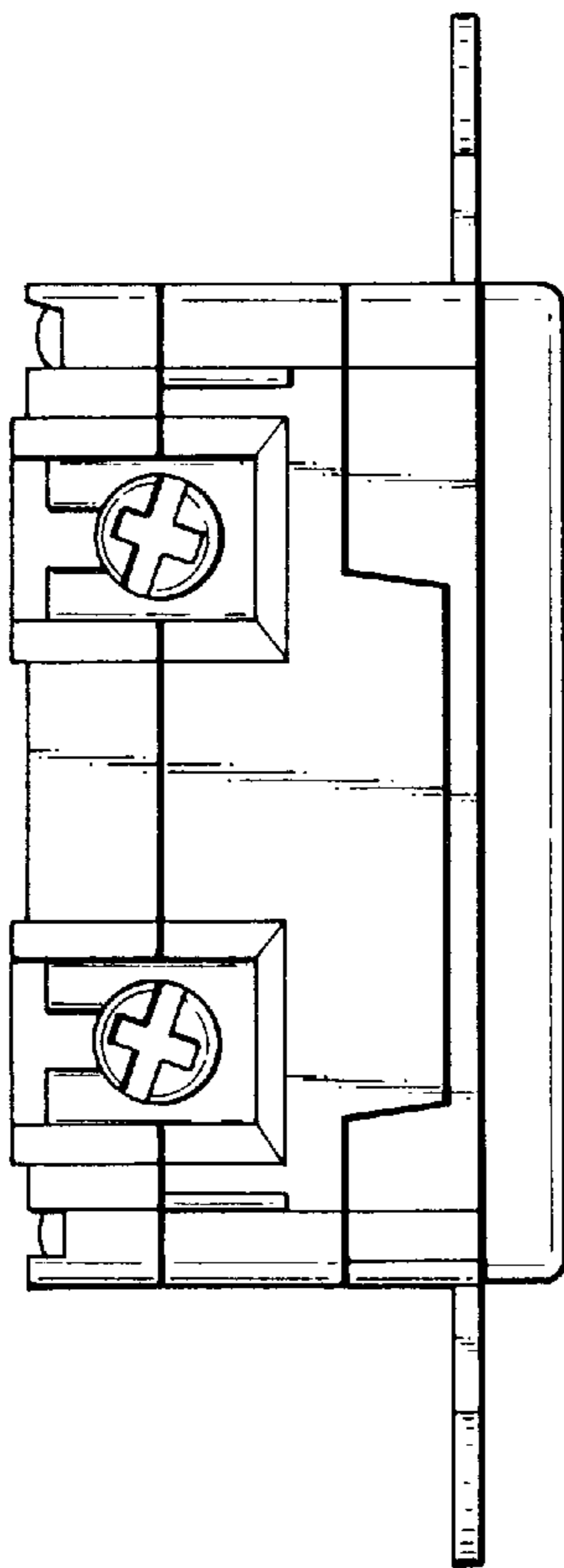


FIG. 5

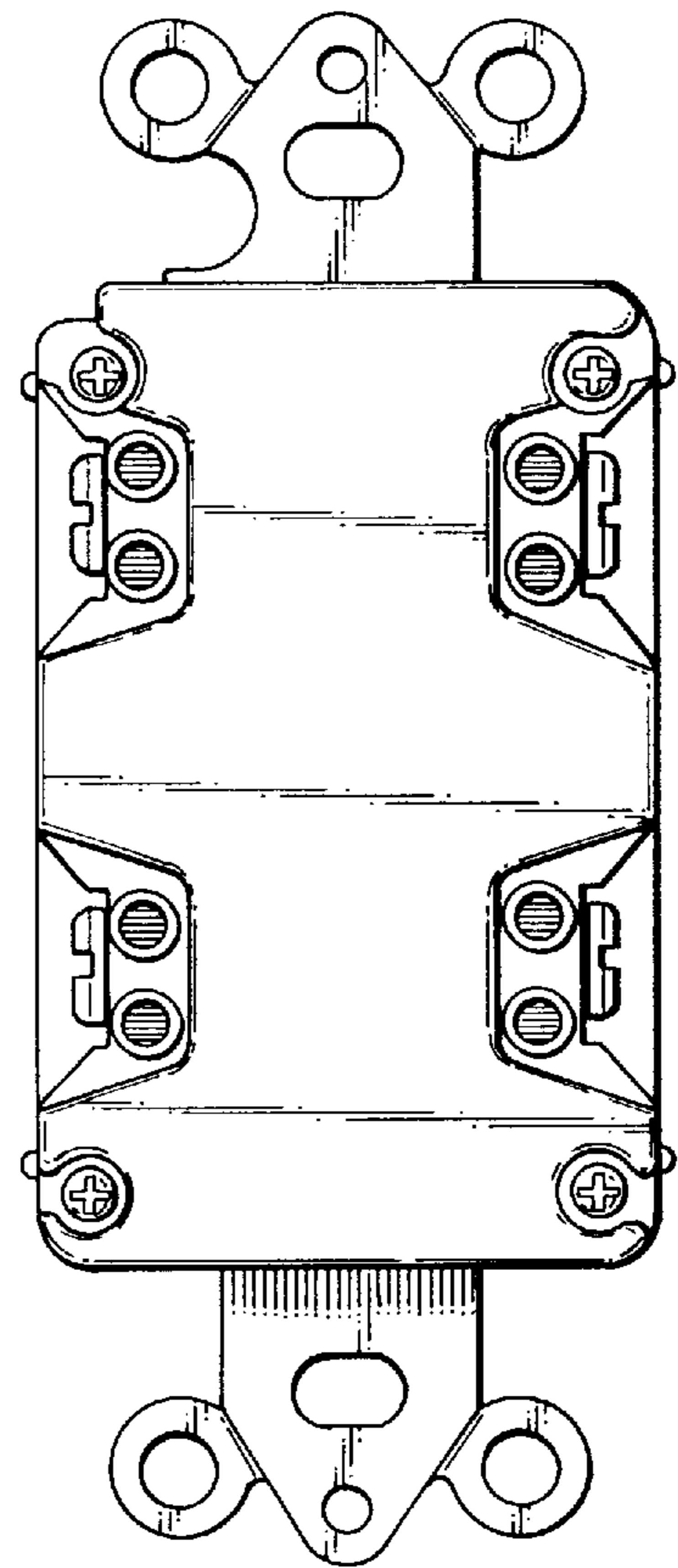


FIG. 6