



US00D413270S

# United States Patent [19] Shun

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[54] SYNCHRONIZED EQUATION CLOCK

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[\*\*] Term: **14 Years**

[21] Appl. No.: **29/083,769**

[22] Filed: **Feb. 18, 1998**

[30] **Foreign Application Priority Data**

Dec. 16, 1997 [CN] China ..... 97330552

[51] LOC (6) Cl. .... **10-01**

[52] U.S. Cl. .... **D10/1; D10/10; D10/6**

[58] Field of Search ..... D10/1-39, 122-126;  
368/285, 10, 276-227, 280-281

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*Attorney, Agent, or Firm*—Armstrong, Westerman, Hattori, McLeland & Naughton

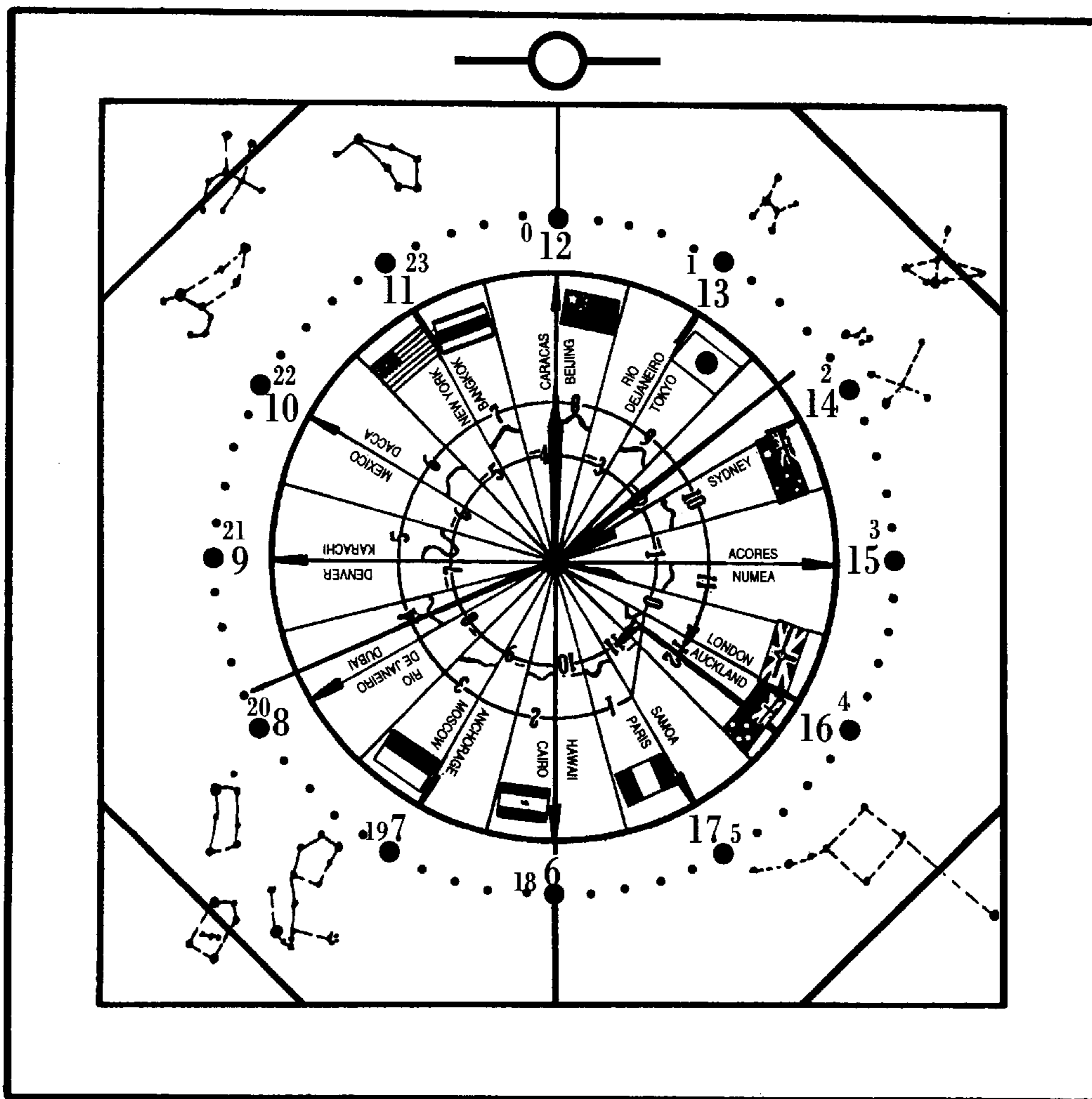
[57] **CLAIM**

I claim the ornamental design for a synchronized equation clock, as shown and described.

**DESCRIPTION**

FIG. 1 is a front view of a synchronized equation clock showing my new design;  
FIG. 2 is a rear view thereof;  
FIG. 3 is a top view thereof;  
FIG. 4 is a bottom view thereof;  
FIG. 5 is a right-side view thereof; and,  
FIG. 6 is a left-side view thereof.

**1 Claim, 4 Drawing Sheets**



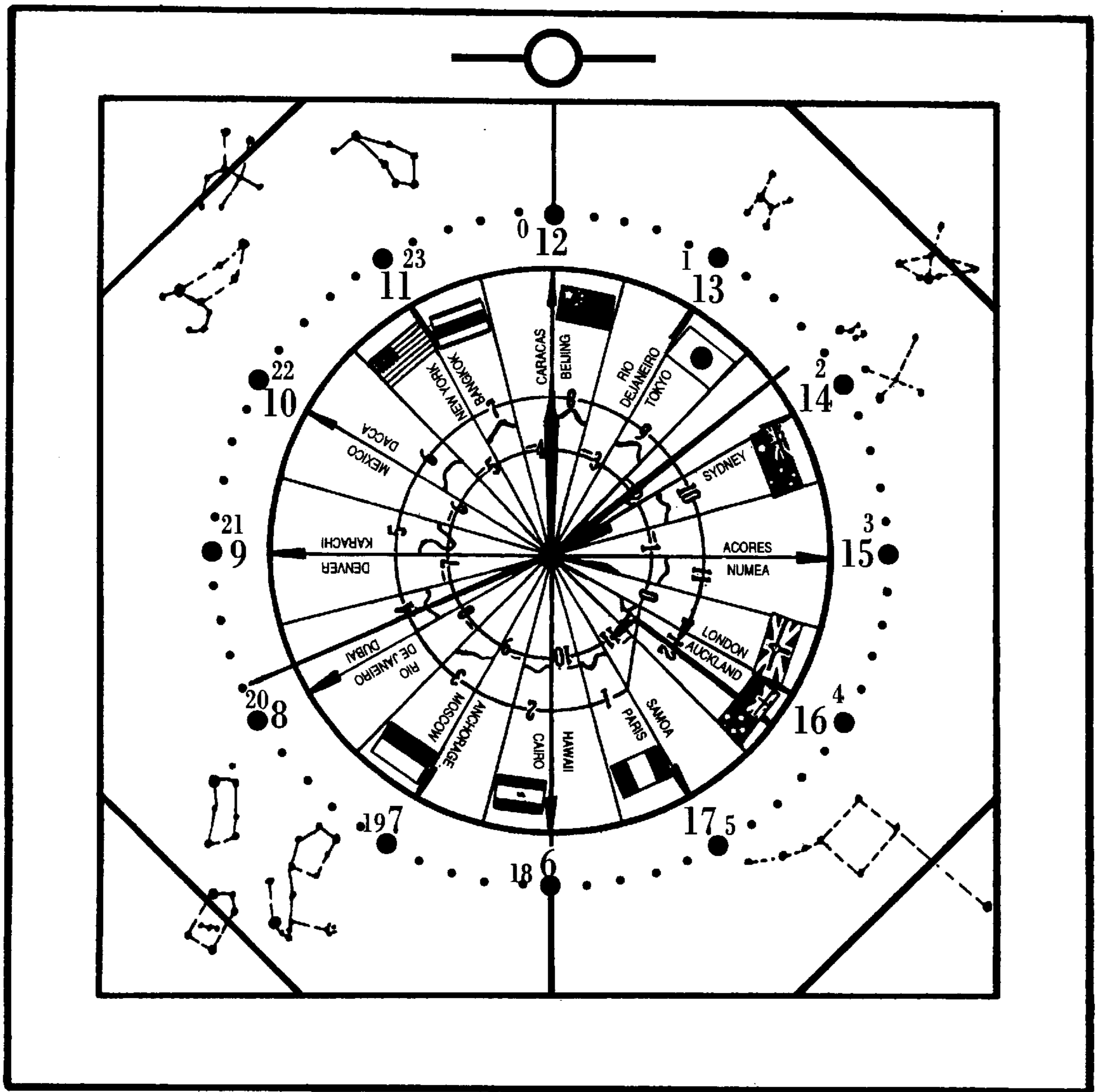


FIG. 1

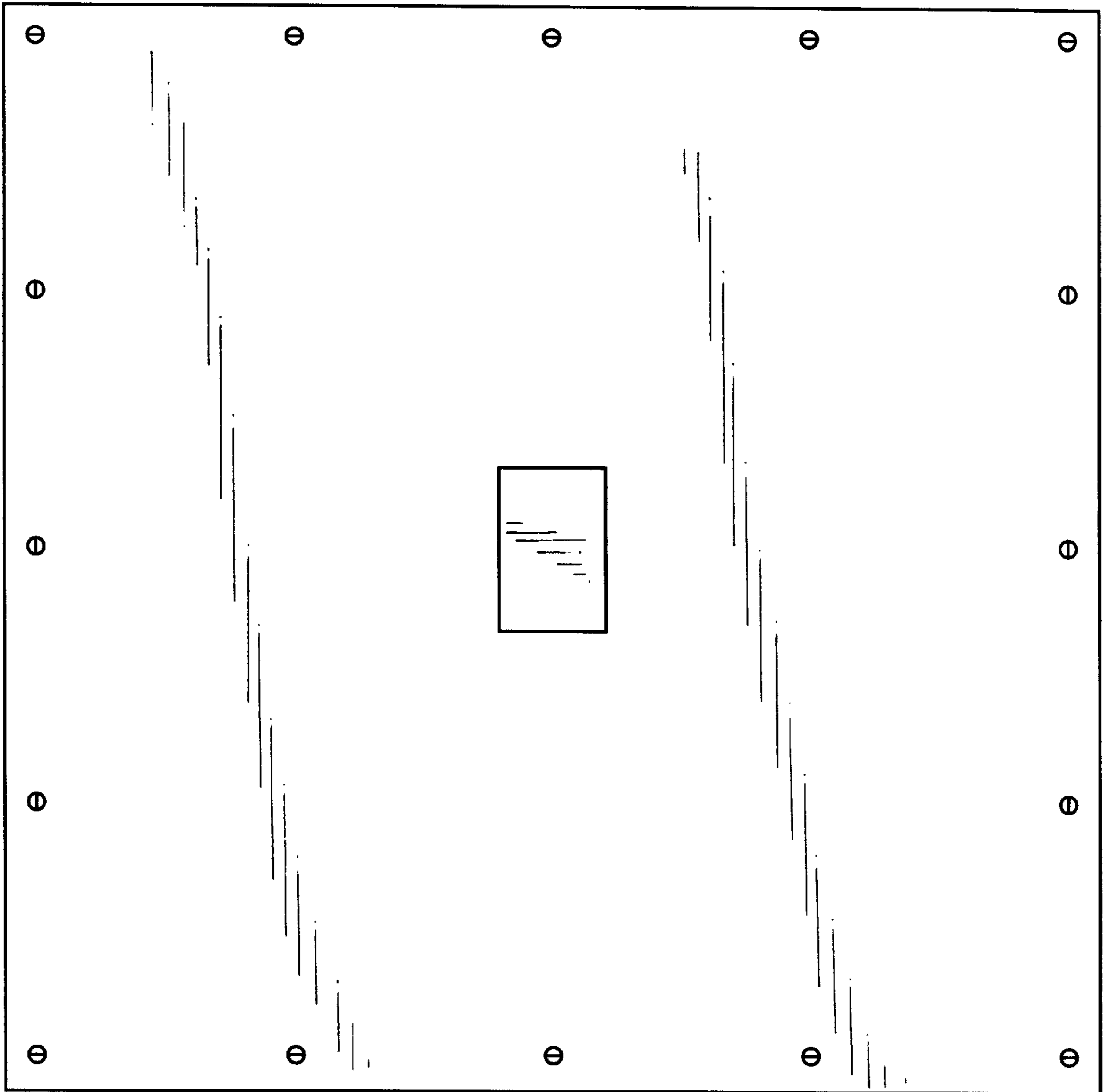


FIG. 2

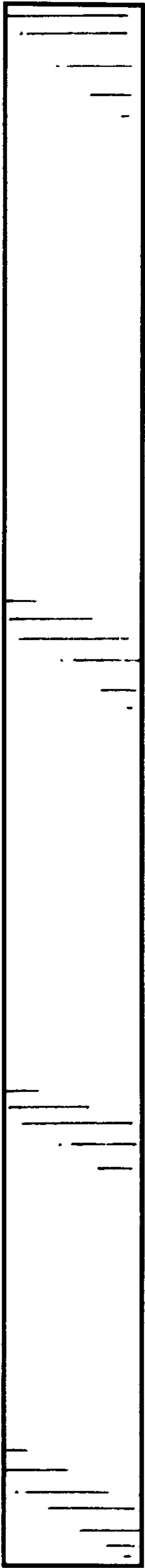


FIG. 3

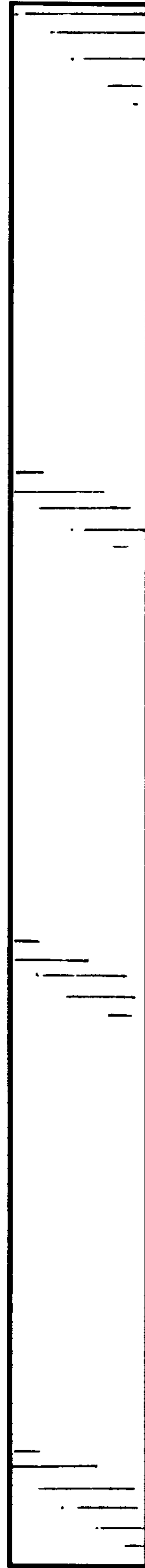


FIG. 4

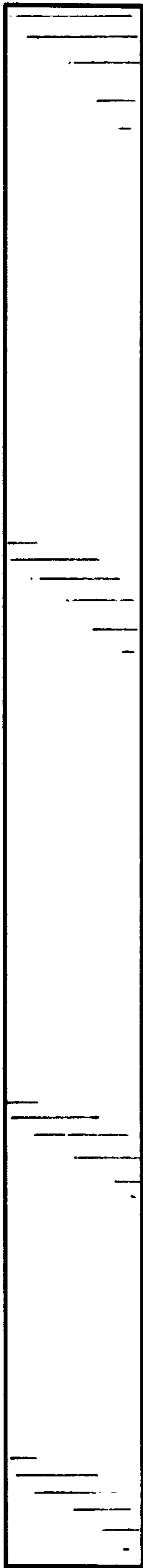


FIG. 5

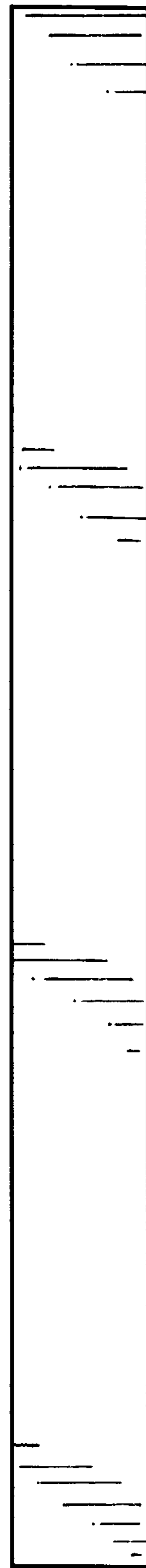


FIG. 6