



US00D412887S

United States Patent [19] Johnston

[11] Patent Number: **Des. 412,887**

[45] Date of Patent: **** Aug. 17, 1999**

[54] **SCALABLE PROCESSING ARCHITECTURE (SPARC) COMPUTER MOTHERBOARD**

4,031,371	6/1977	Devries	174/253 X
5,287,009	2/1994	Heung	361/695 X
5,397,919	3/1995	Tata et al.	257/706 X

[75] Inventor: **Mark L. Johnston**, Los Altos, Calif.

OTHER PUBLICATIONS

[73] Assignee: **Cycle Computer Corporation**,
Cupertino, Calif.

Circuit boards on p. 233 of *EDN*, Jan. 21, 1991.

[**] Term: **14 Years**

Primary Examiner—Joel Sincavage
Attorney, Agent, or Firm—Morrison & Foerster LLP

[21] Appl. No.: **29/061,332**

[57] CLAIM

[22] Filed: **Oct. 22, 1996**

The ornamental design for a scalable processing architecture (SPARC) computer motherboard, as shown and described.

Related U.S. Application Data

DESCRIPTION

[63] Continuation of application No. 29/038,145, Apr. 27, 1995,
Pat. No. Des. 374,866.

FIG. 1 is a perspective view of a first embodiment of the scalable processing architecture (sparc) computer motherboard embodying my new design;
FIG. 2 is a top elevational view thereof;
FIG. 3 is a back side view thereof;
FIG. 4 is a front side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a left side view thereof; and,
FIG. 7 is a bottom view thereof.

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/182**

[58] **Field of Search** D13/182; 174/250,
174/252, 255, 260, 261; 361/695, 718,
719, 720, 736, 748, 785

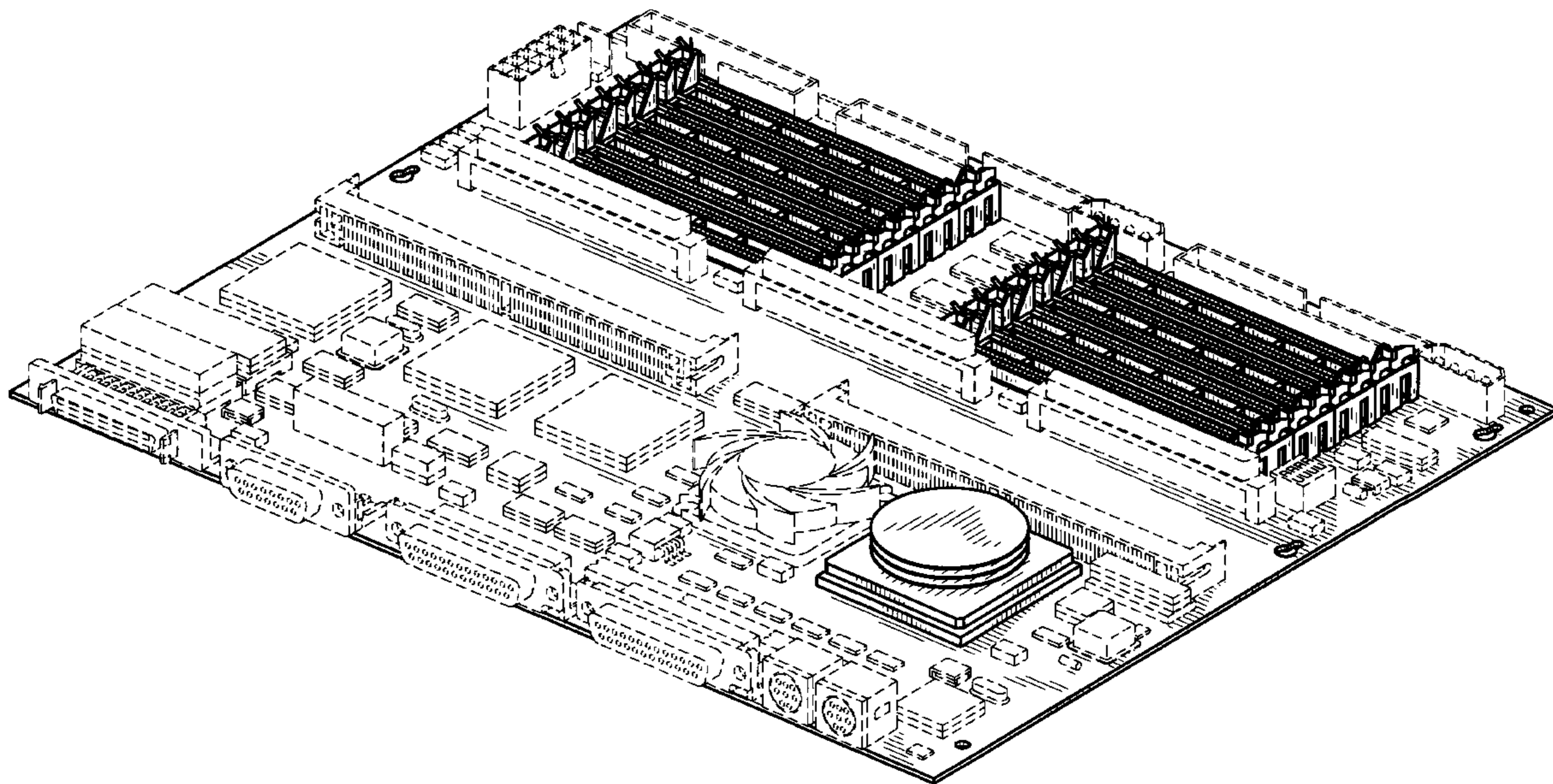
The broken line showing throughout the drawing views is included for the purpose of illustrating environmental structure.

[56] References Cited

U.S. PATENT DOCUMENTS

D. 275,279	8/1984	Littlefield et al.	D13/182
D. 304,442	11/1989	Edmmonds	D13/147

1 Claim, 5 Drawing Sheets



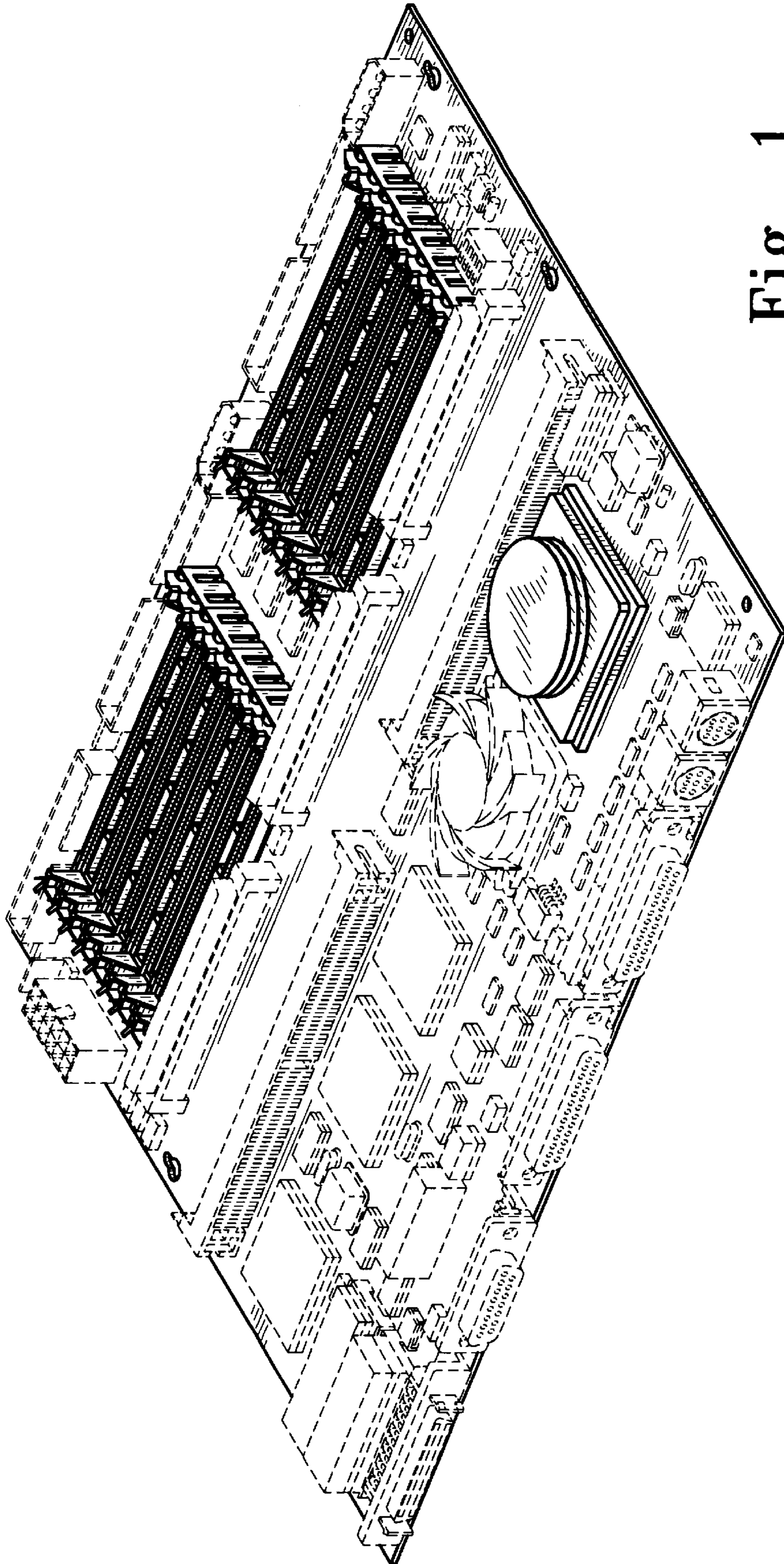


Fig. 1

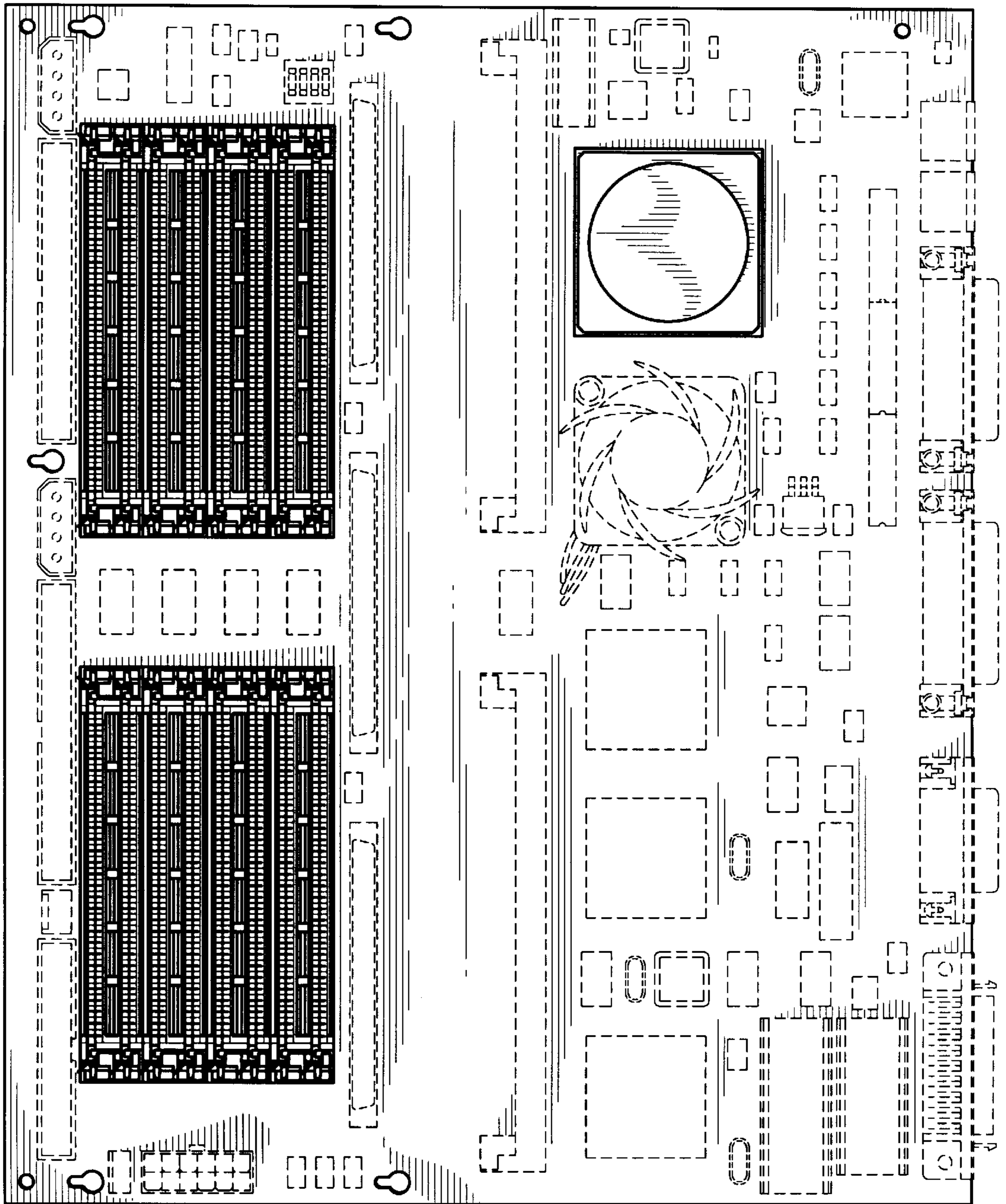


Fig. 2

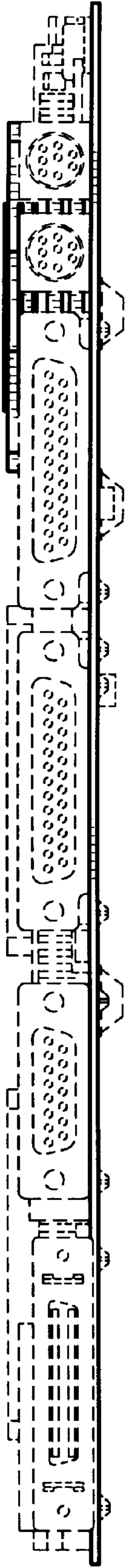


Fig. 3

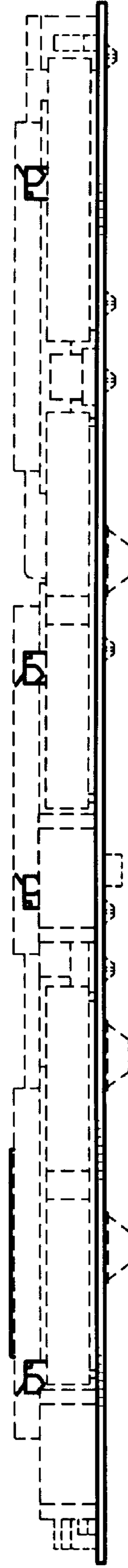


Fig. 4

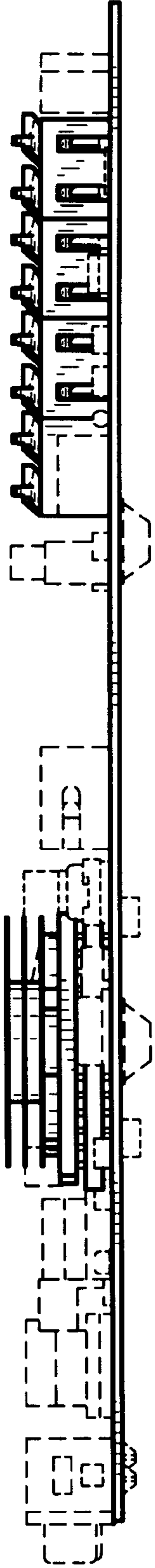


Fig. 5

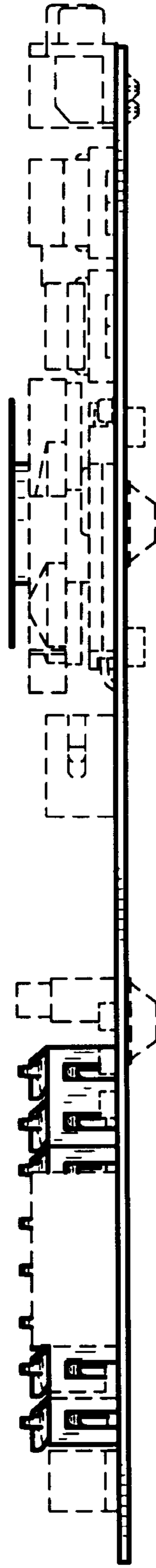


Fig. 6



Fig. 7