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United States Patent [19]

Laviron et al.

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[54] **CONNECTING TERMINAL FOR CHIP CARDS**

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[73] Assignee: **Gemplus, Gemenos, France**

[**] Term: **14 Years**

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Related U.S. Application Data

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[30] Foreign Application Priority Data

Jun. 19, 1995 [FR] France 953394

[51] LOC (6) Cl. **14-02**

[52] U.S. Cl. **D14/117**

[58] Field of Search D14/114, 117; D13/182; 235/378-390, 492; 439/43, 52, 64; 361/737-739

[56] References Cited

U.S. PATENT DOCUMENTS

D. 393,458 4/1998 Merlin et al. D14/117

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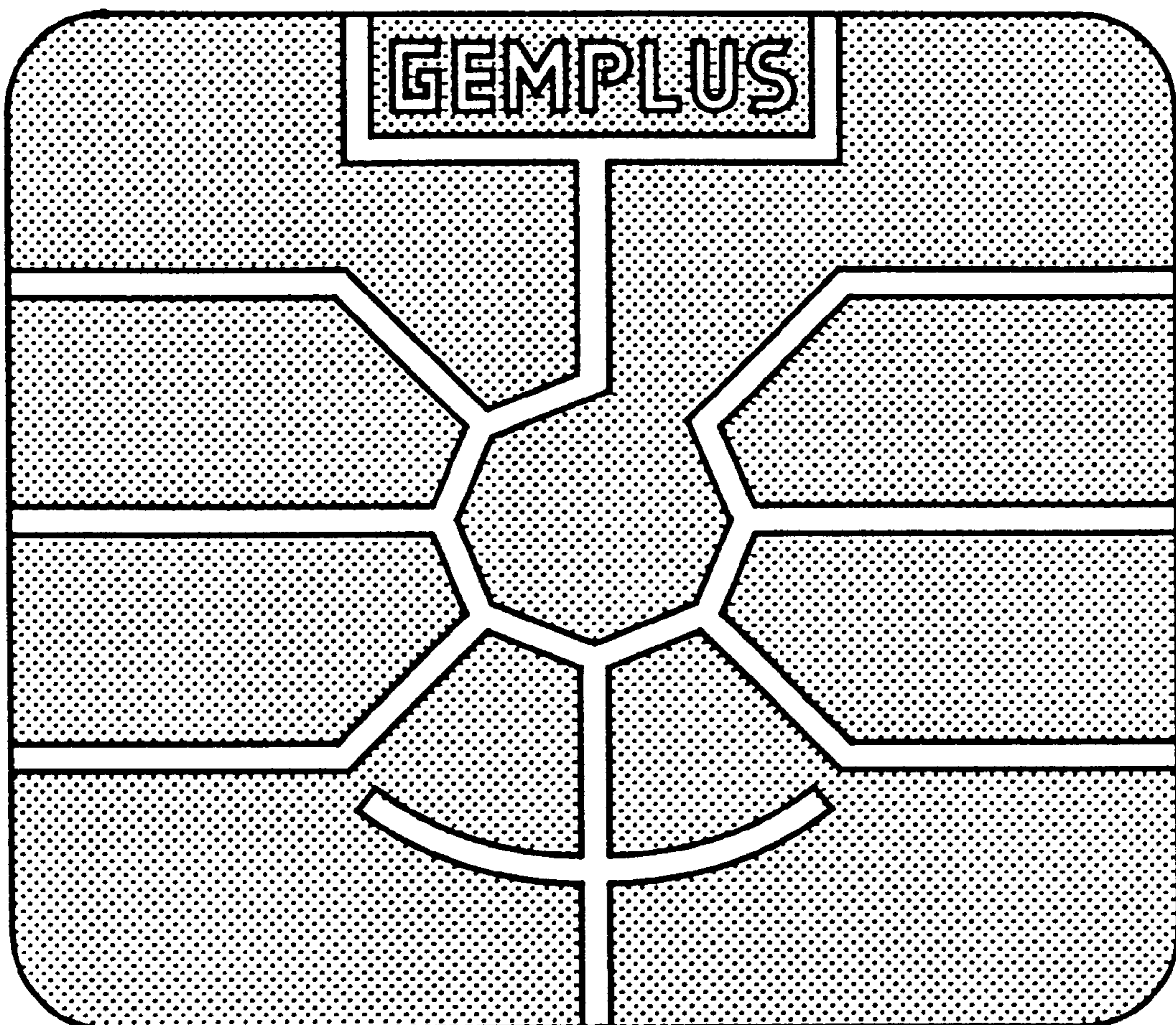
[57] CLAIM

The ornamental design for a connecting terminal for chip cards, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of a connecting terminal for chip cards showing our new design; and, FIG. 2 is a top plan view of a second embodiment thereof. The bottom plan view is a mirror image of the top and the terminals shown are of no substantial thickness.

1 Claim, 2 Drawing Sheets



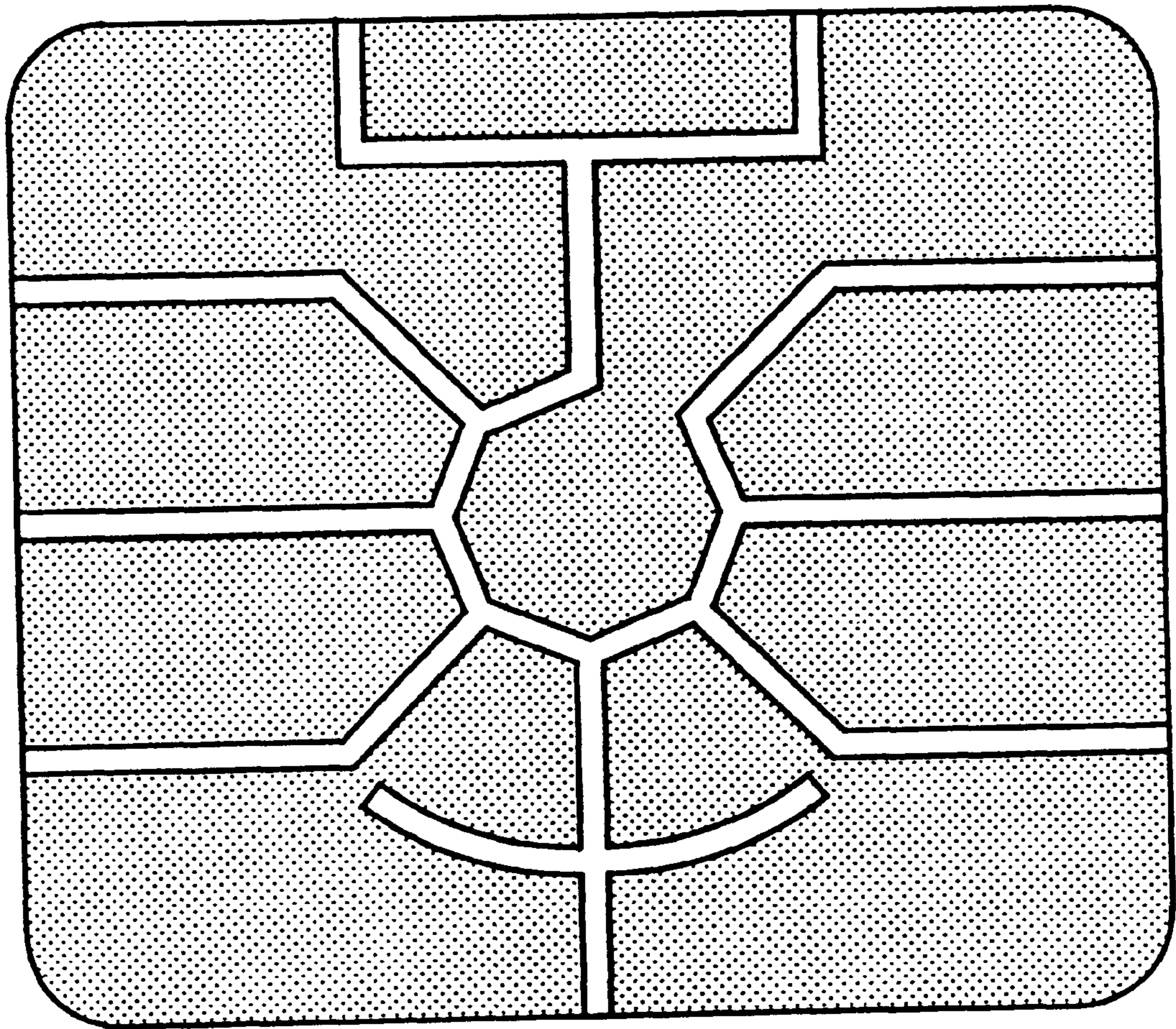


FIG. 1

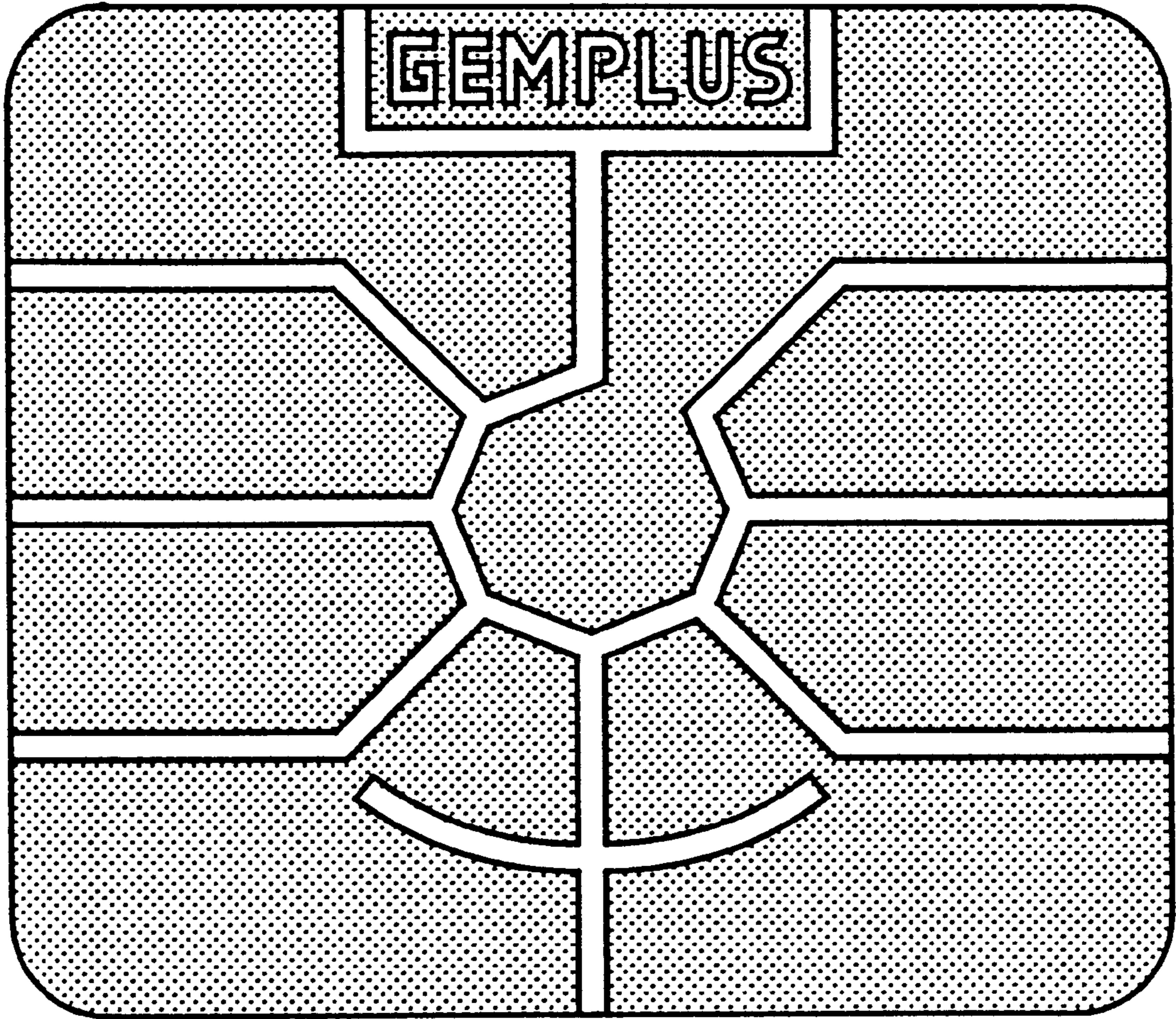


FIG. 2