

US00D408356S

**United States Patent** [19]  
**Siemon et al.**

[11] **Patent Number:** **Des. 408,356**  
[45] **Date of Patent:** **\*\*Apr. 20, 1999**

[54] **PATCH CONNECTOR**

[75] **Inventors:** **John A. Siemon, Woodbury; Robert C. Carlson, Jr., Torrington; Carl H. Weymouth, Riverton, all of Conn.**

[73] **Assignee:** **The Siemon Company, Watertown, Conn.**

[\*\*] **Term:** **14 Years**

[21] **Appl. No.:** **29/075,888**

[22] **Filed:** **Sep. 2, 1997**

**Related U.S. Application Data**

[62] Division of application No. 29/063,461, Dec. 9, 1996, which is a continuation of application No. 08/488,126, Jun. 7, 1995, Pat. No. 5,634,817, which is a continuation-in-part of application No. 08/144,768, Oct. 28, 1993, Pat. No. 5,460,545.

[51] **LOC (6) CL.** ..... **13-03**

[52] **U.S. Cl.** ..... **D13/146**

[58] **Field of Search** ..... **D13/146, 147;**  
**439/608, 404, 731**

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

D. 254,847	4/1980	Healey	.....	D13/147
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[57] **CLAIM**

The ornamental design for a patch connector, as shown and described.

**DESCRIPTION**

FIG. 1 is a top plan view of a patch connector in a first embodiment of the invention;

FIG. 2 is a right side elevational view of the patch connector in the first embodiment of the invention; the left side being a mirror image thereof;

FIG. 3 is a rear elevational view of the patch connector in the first embodiment of the invention;

FIG. 4 is a front elevational view of the patch connector in the first embodiment of the invention;

FIG. 5 is a bottom plan view of the patch connector in the first embodiment of the invention;

FIG. 6 is a top plan view of a patch connector in a second embodiment of the invention;

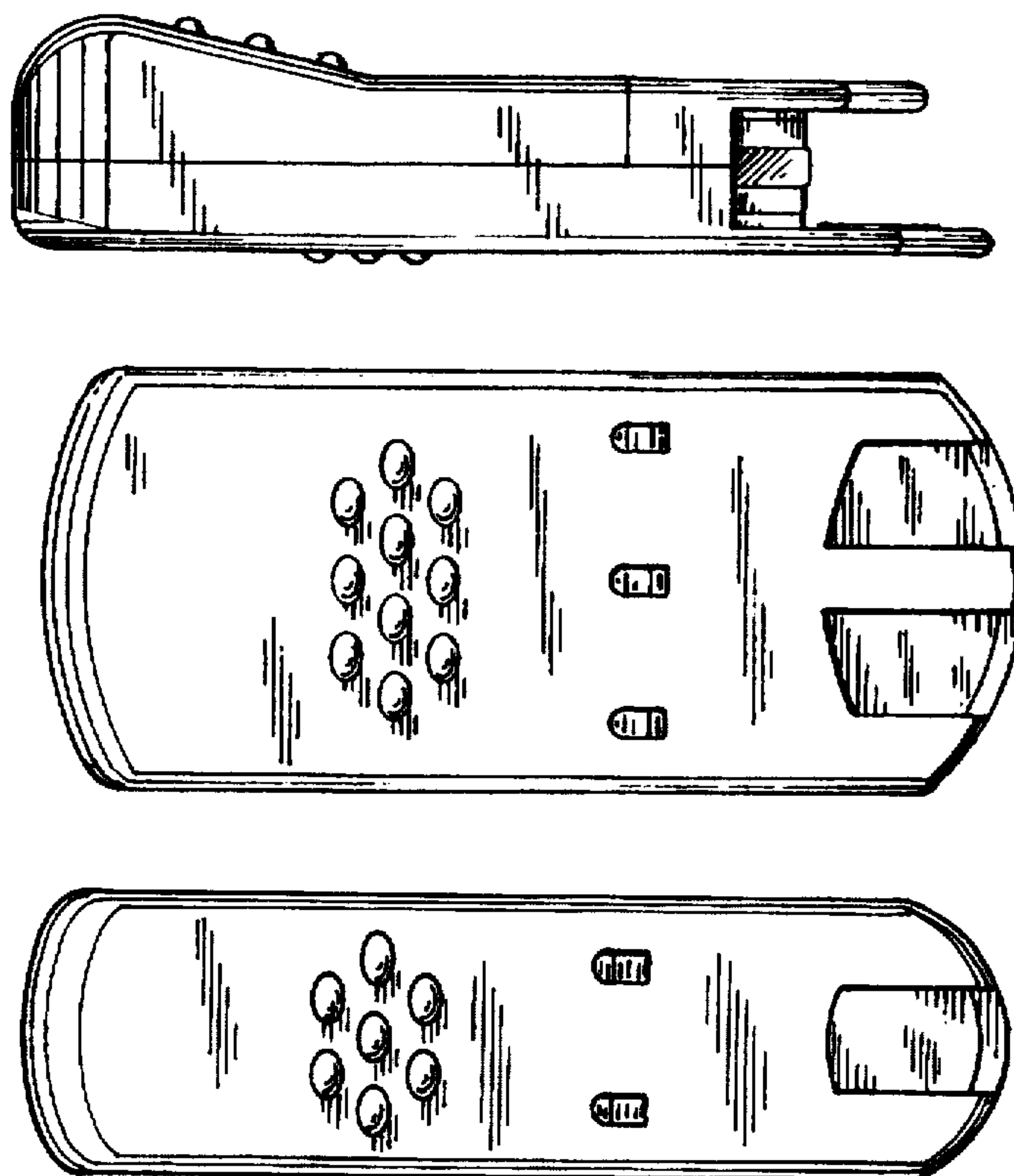
FIG. 7 is a right side elevational view of the patch connector in the second embodiment of the invention; the left side being a mirror image thereof;

FIG. 8 is a rear elevational view of the patch connector in the second embodiment of the invention;

FIG. 9 is a front elevational view of the patch connector in the second embodiment of the invention; and,

FIG. 10 is a bottom plan view of the patch connector in the second embodiment of the invention.

**1 Claim, 2 Drawing Sheets**



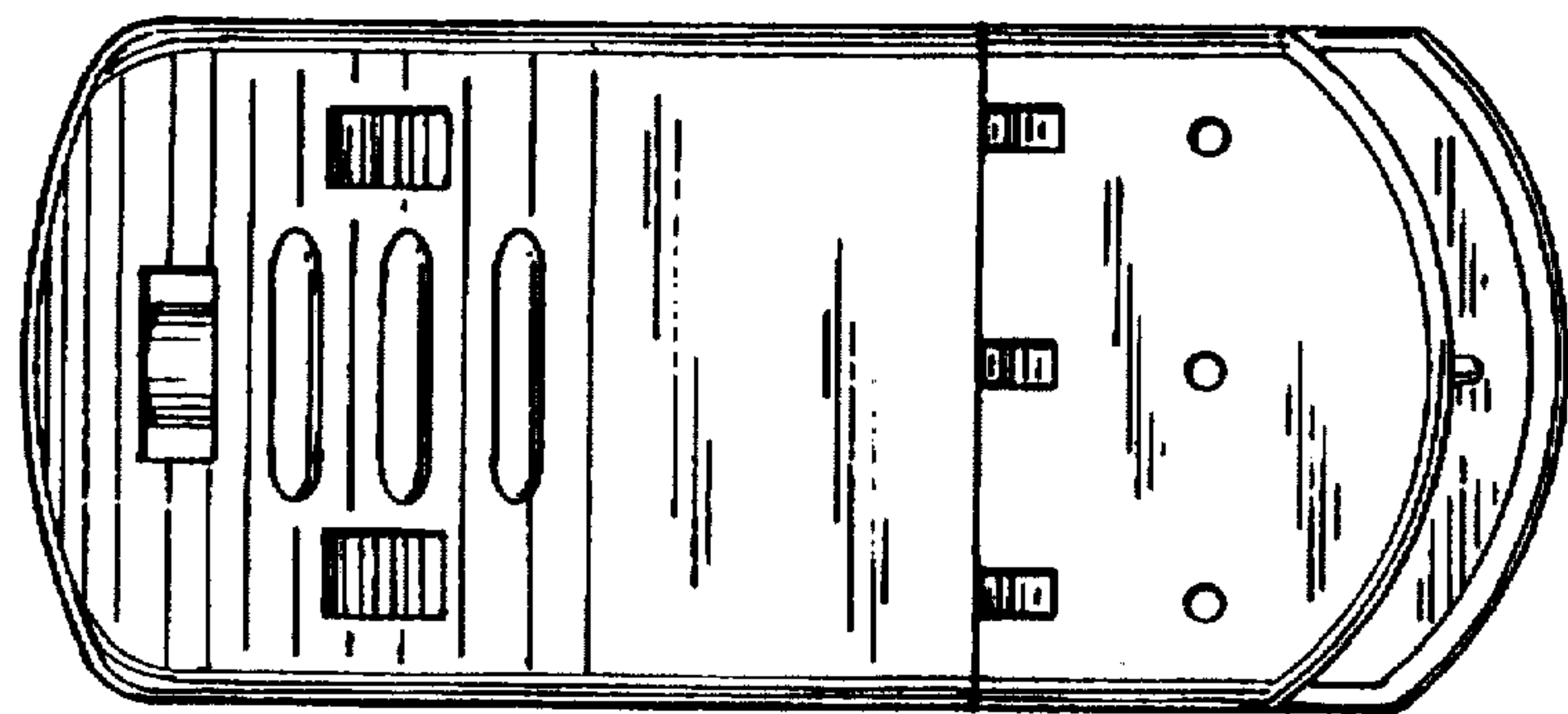


FIG. 1

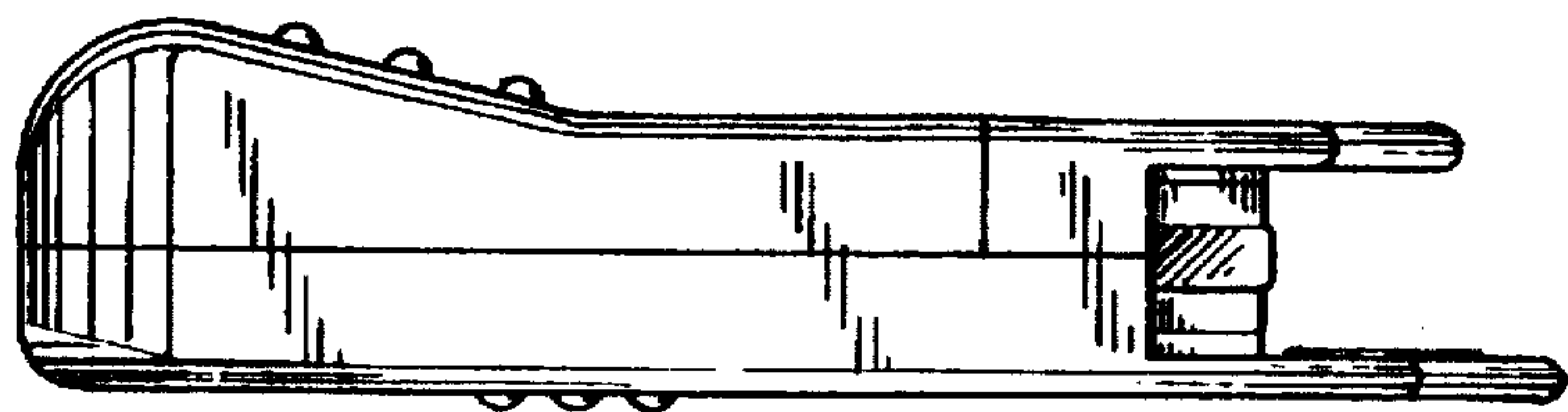


FIG. 2

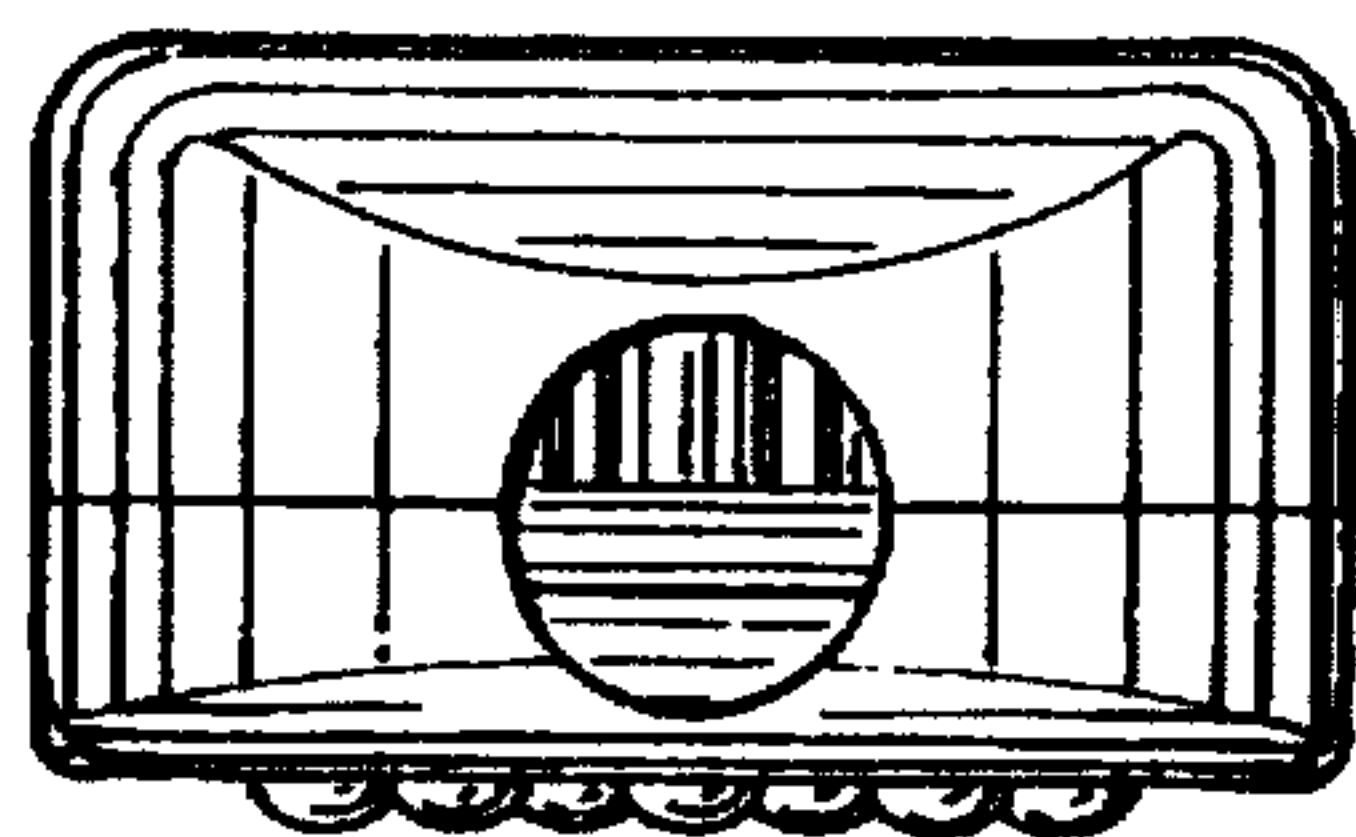


FIG. 3

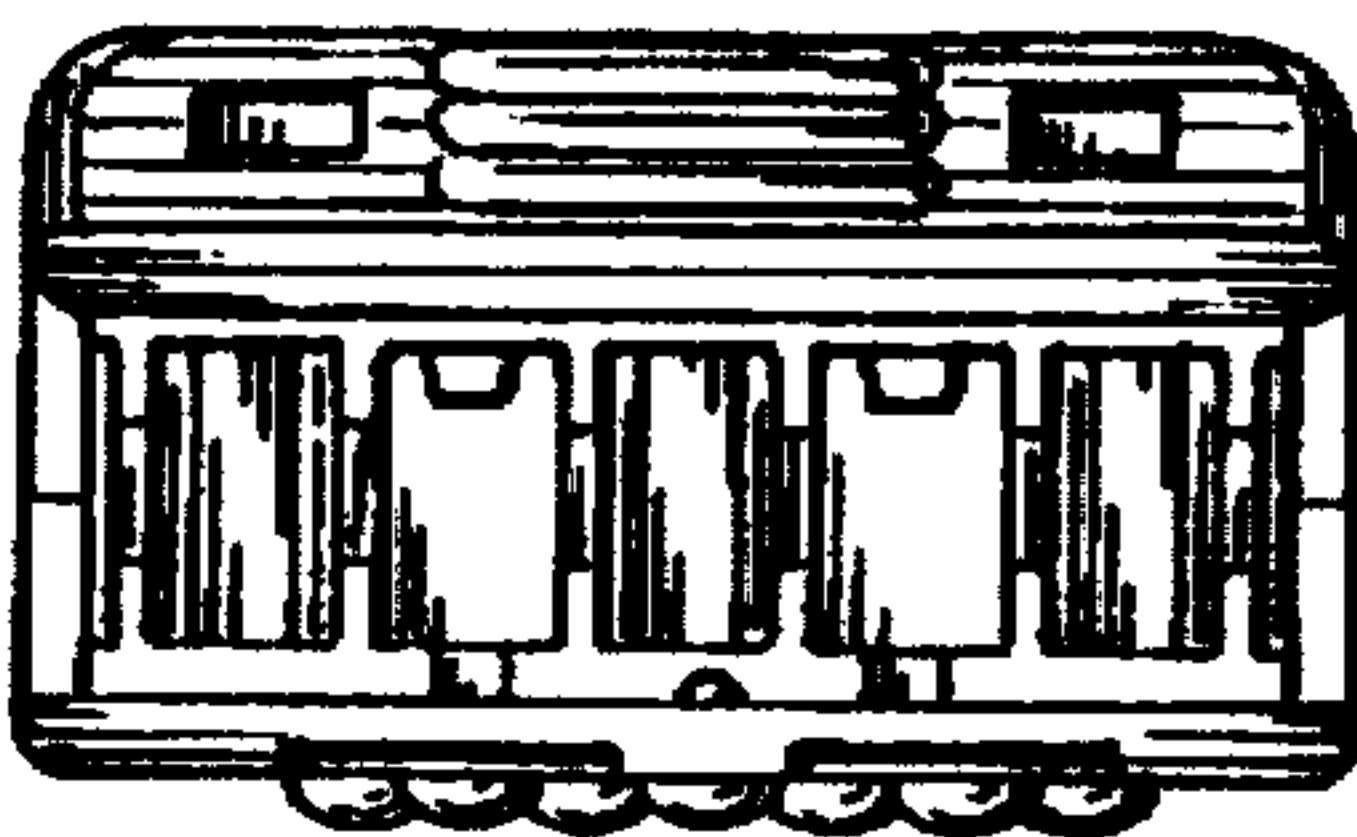


FIG. 4

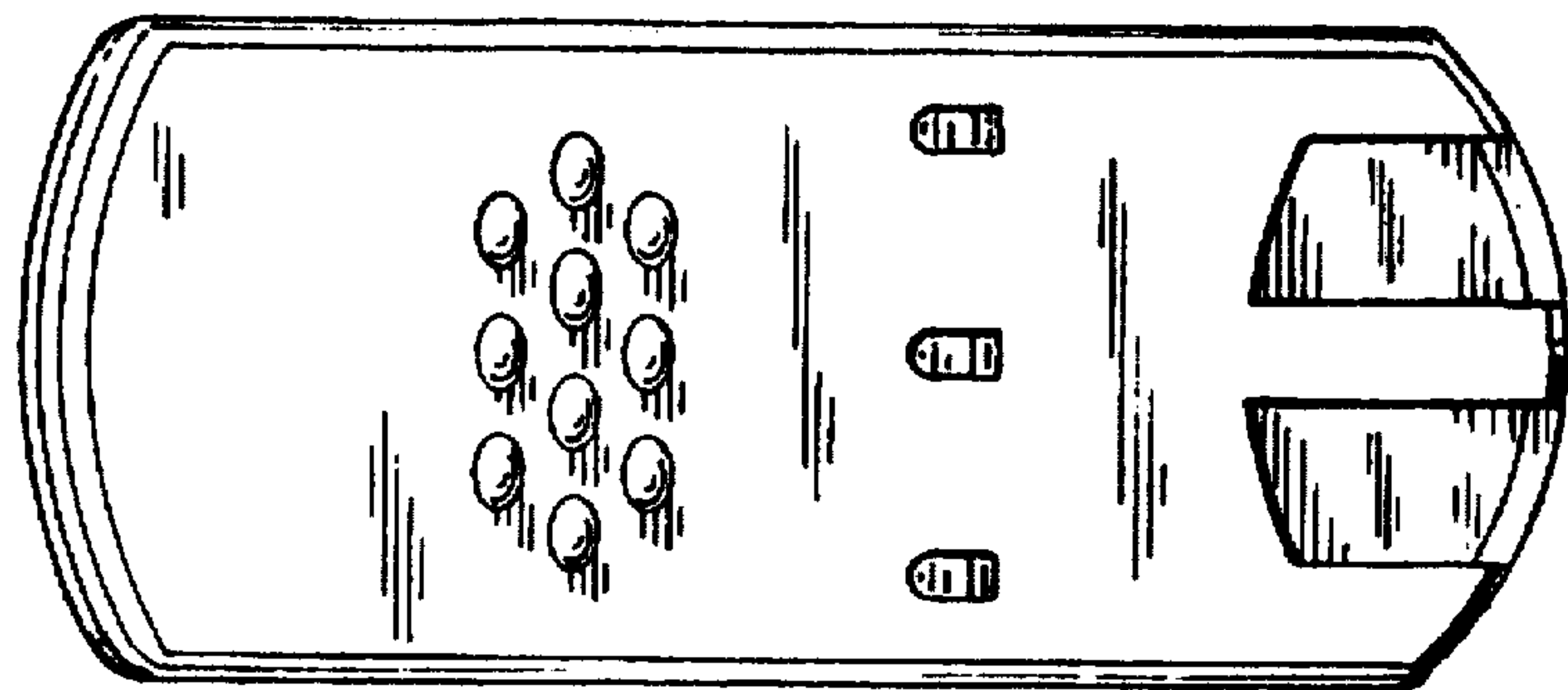


FIG. 5

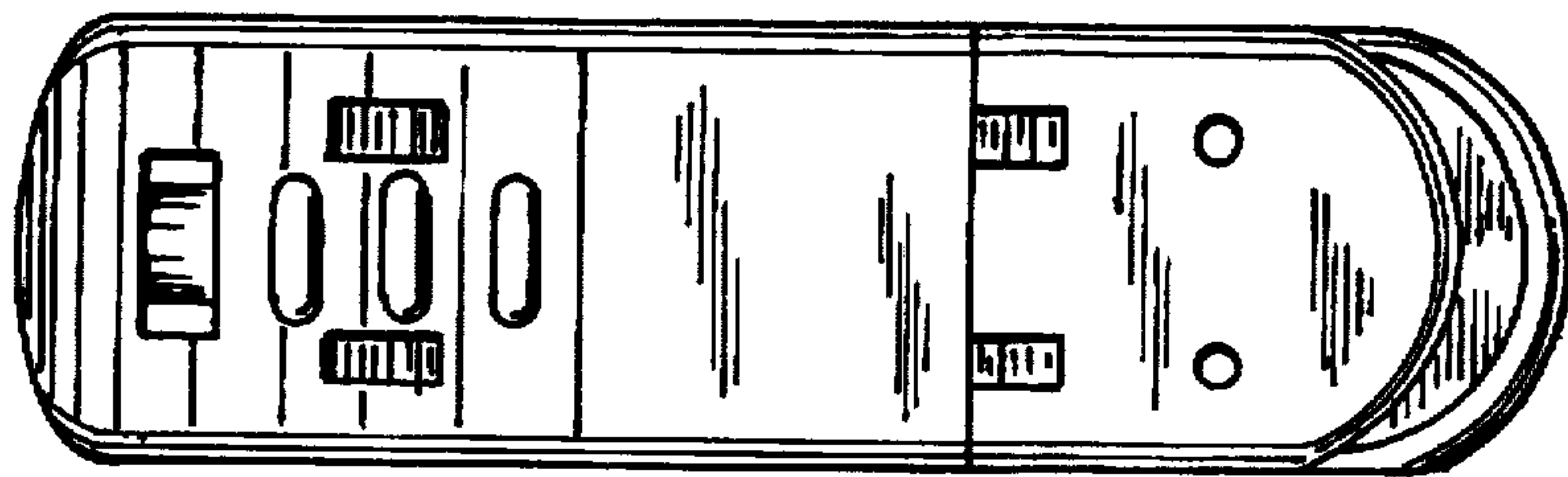


FIG. 6

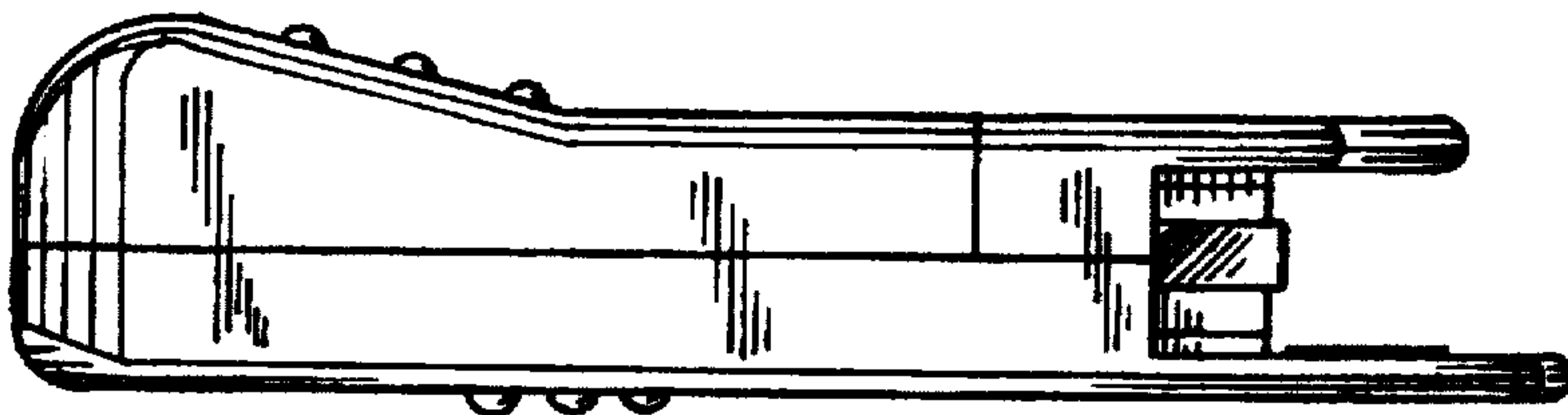


FIG. 7

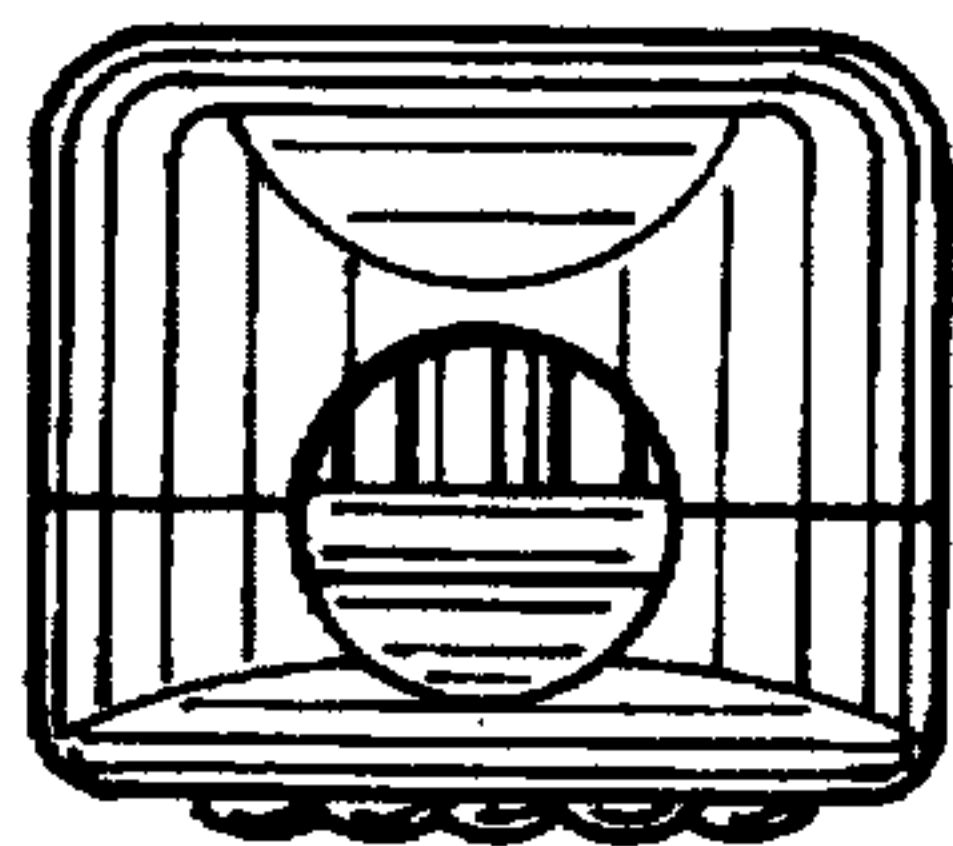


FIG. 8

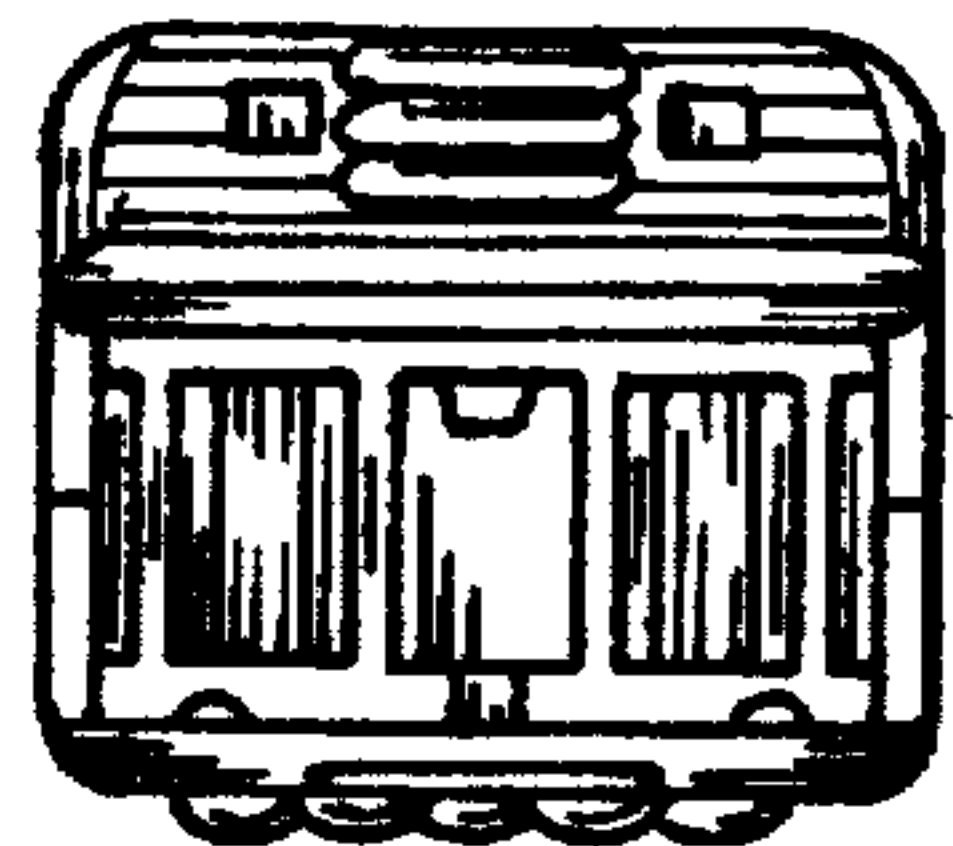


FIG. 9

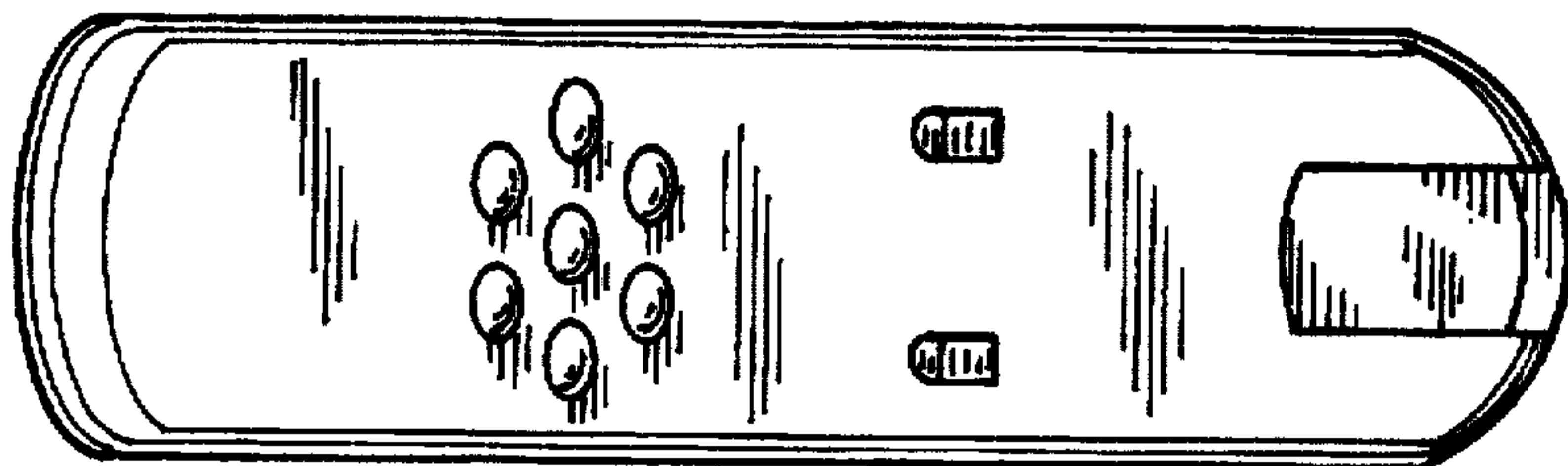


FIG. 10