



US00D405440S

# United States Patent [19] Klinker

[11] **Patent Number: Des. 405,440**  
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[54] **PROCESSOR CARD ASSEMBLY**

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[73] Assignee: **Intel Corporation**, Santa Clara, Calif.

[\*\*] Term: **14 Years**

[21] Appl. No.: **61,870**

[22] Filed: **Nov. 1, 1996**

[51] **LOC (6) Cl.** ..... **14-02**

[52] **U.S. Cl.** ..... **D14/117; D14/114**

[58] **Field of Search** ..... D14/114, 115, D14/120, 117; D13/182; D18/12; 206/706, 709; 340/825.22; 235/492; 361/816, 753, 737, 424; 257/678; 364/200, 239.3, 401, 479, 578; 370/364; 379/269, 279, 399; 380/23, 24; 395/310, 376, 393, 325, 652, 670, 672, 675, 568, 800; 455/3.1

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[57] **CLAIM**

The ornamental design for a processor card assembly, as shown and described.

**DESCRIPTION**

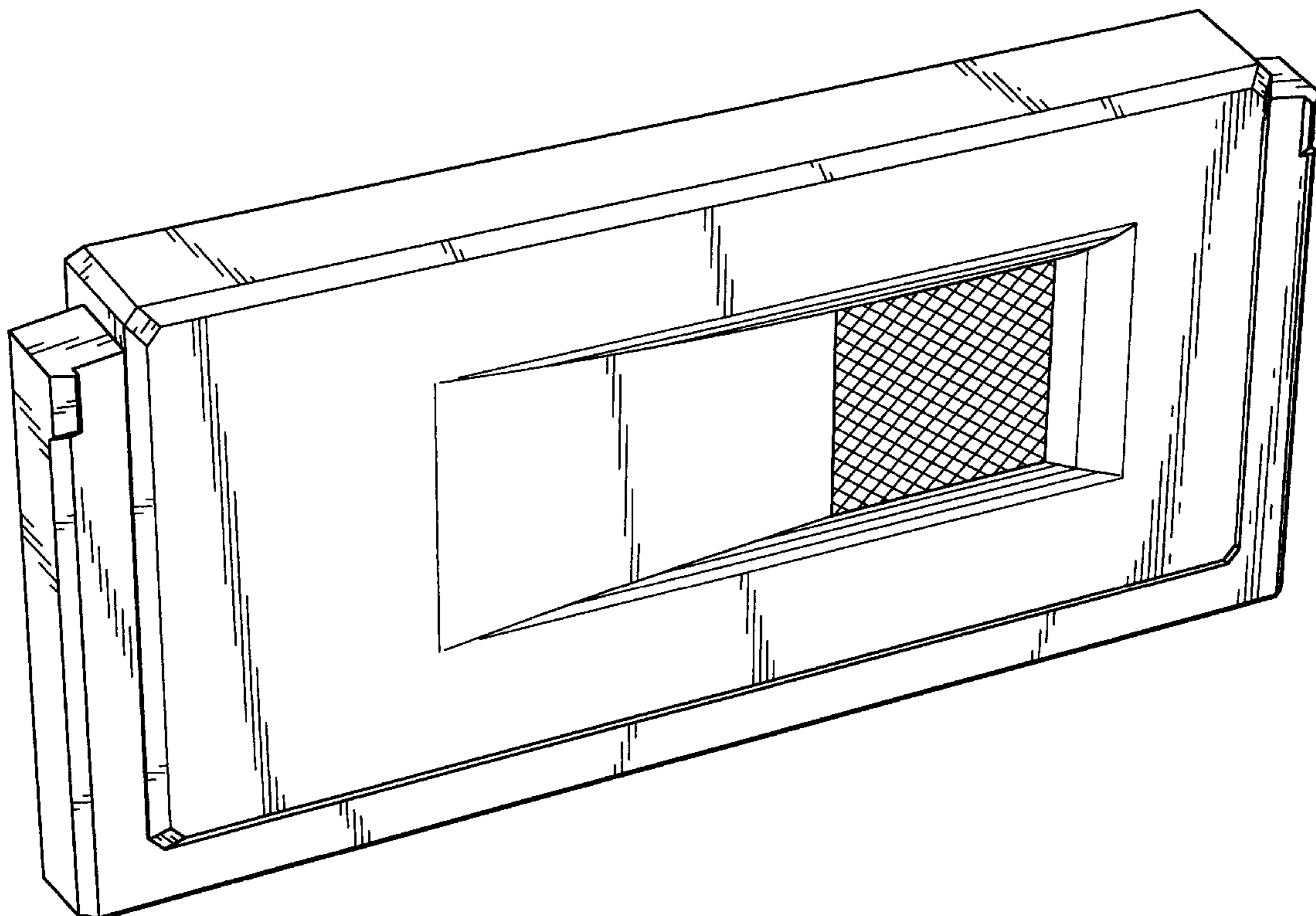
FIG. 1 is an enlarged perspective view of a processor card assembly including a cover having an indented area on a surface and a hologram represented by cross hatching affixed therein; FIGS. 2-3 are front and left side views respectively of the processor card assembly; and, FIGS. 4-7 are back, right side, top, and bottom views respectively of the processor card assembly.

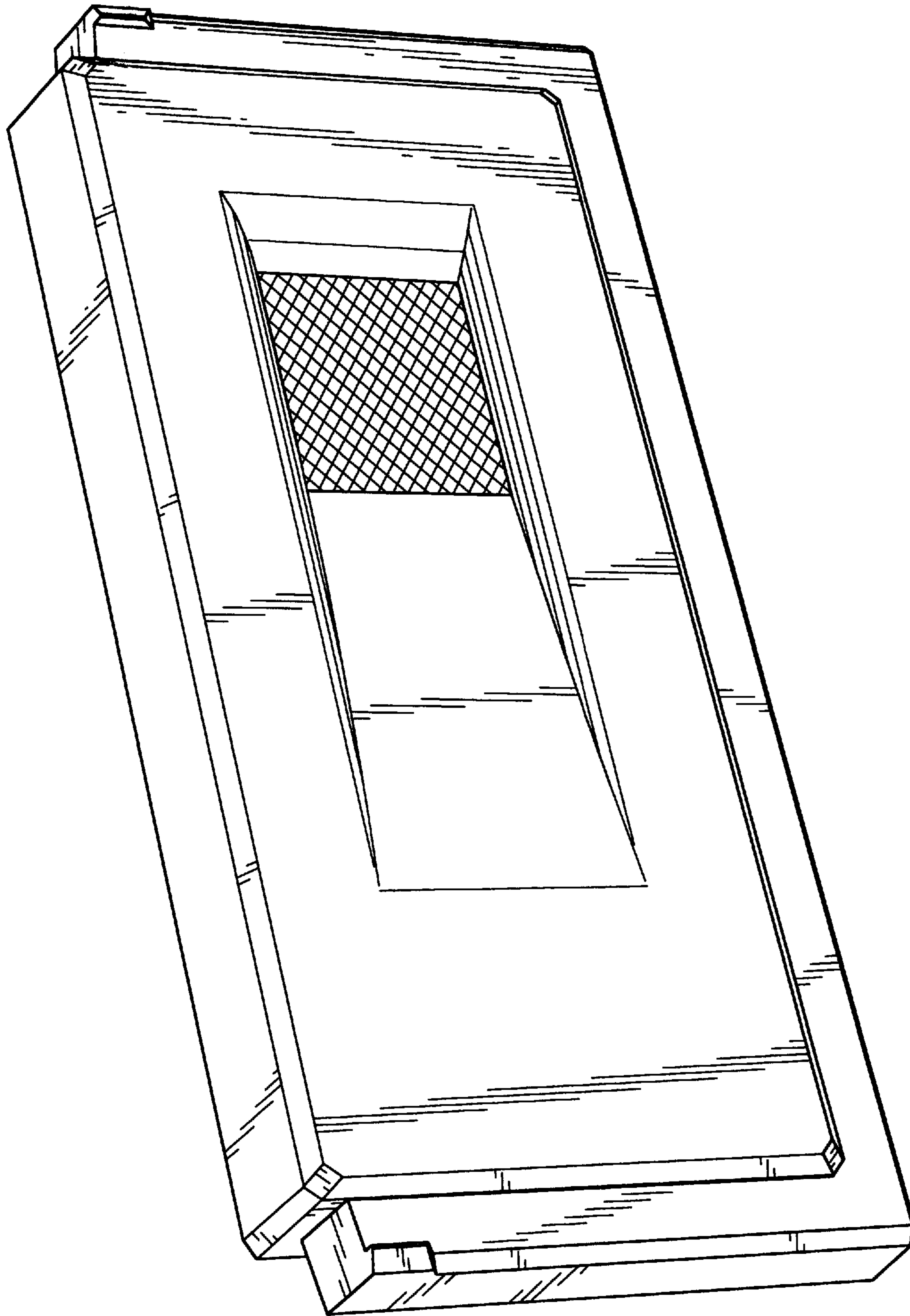
**1 Claim, 3 Drawing Sheets**

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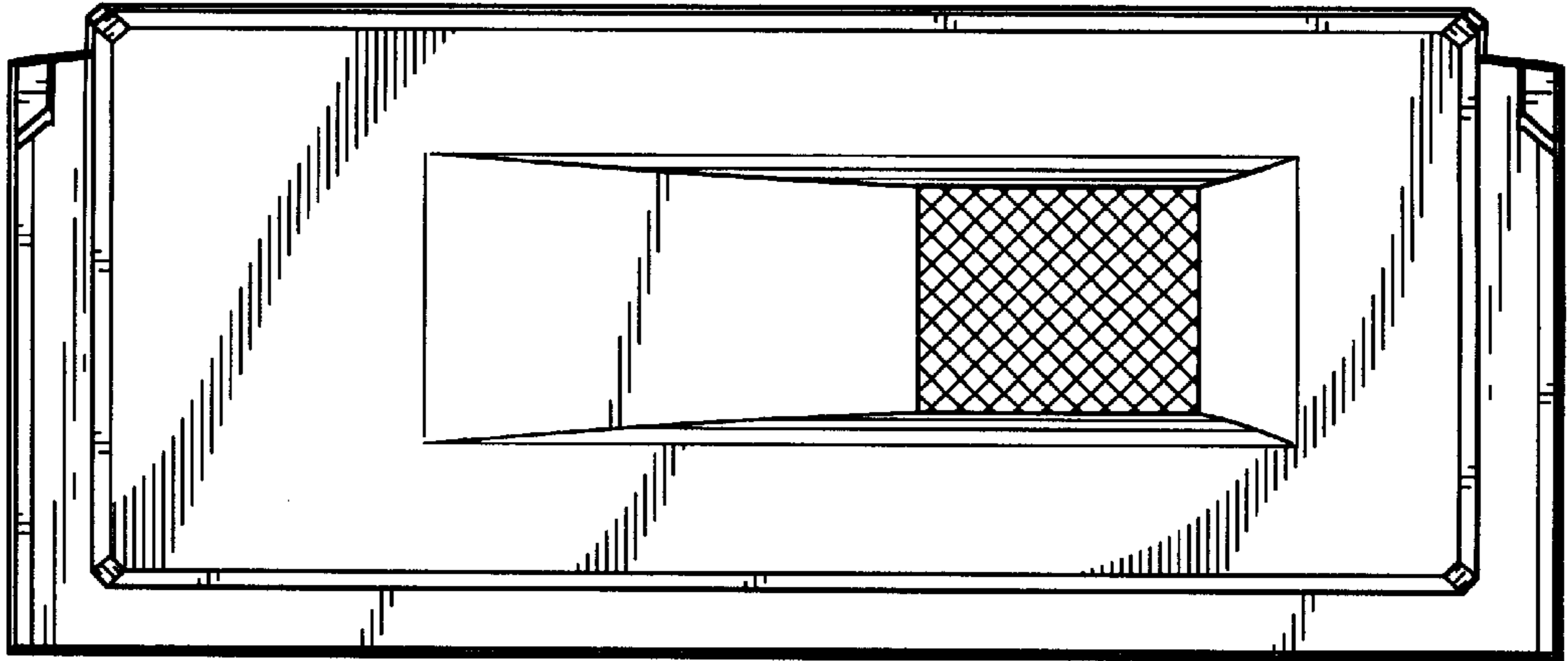
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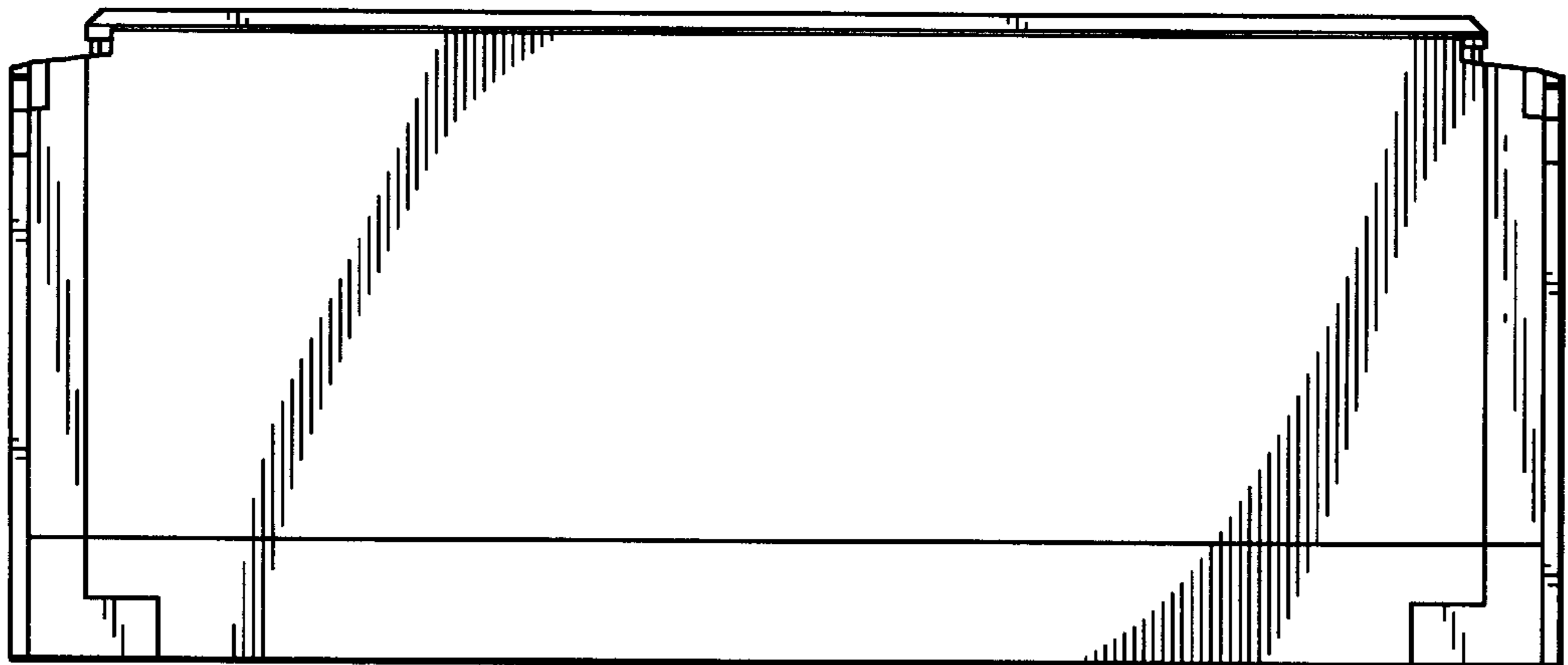
**FIG. 1**



**FIG. 2**



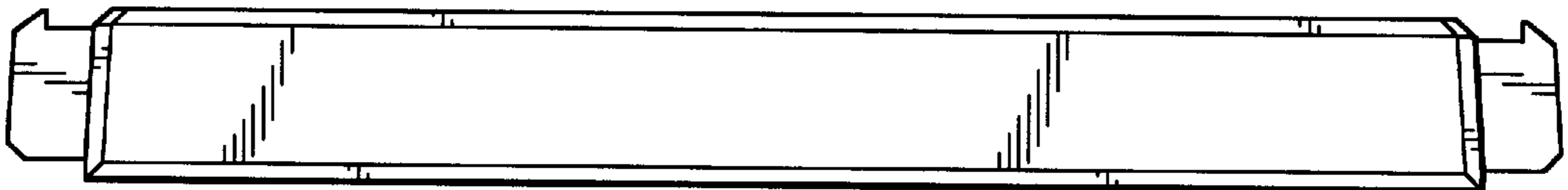
**FIG. 3**



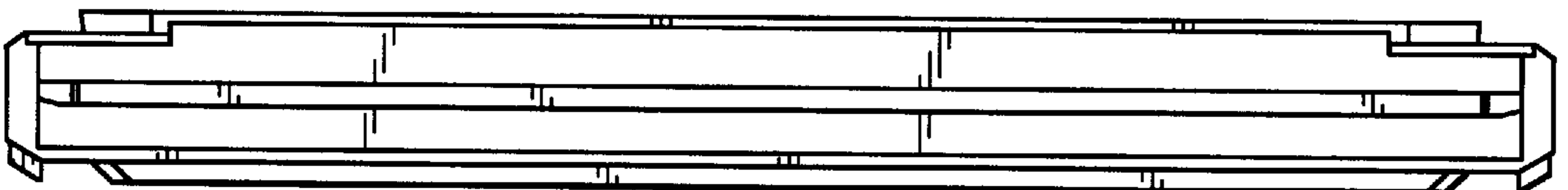
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**