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# United States Patent [19] Klinker

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[54] **PROCESSOR COVER PLATE**

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[73] Assignee: **Intel Corporation**, Santa Clara, Calif.

[\*\*] Term: **14 Years**

[21] Appl. No.: **70,912**

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[51] **LOC (6) Cl.** ..... **14-02**

[52] **U.S. Cl.** ..... **D14/114; D14/117**

[58] **Field of Search** ..... D14/100, 114, D14/115, 120; D13/182; D18/12; 206/706, 709; 235/492; 257/678; 340/825.22; 361/737, 753, 816; 364/401, 479, 239.3, 200, 578; 370/364; 379/269, 279, 399; 380/23, 24; 395/376, 568, 310, 393, 652, 325, 800, 670, 672, 675; 455/3.1

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### [57] CLAIM

The ornamental design for a processor cover plate, as shown and described.

### DESCRIPTION

FIG. 1 is a perspective view of a processor cover plate including an indented area on a surface of the processor cover plate;

FIGS. 2-4 are front, top, and back views, respectively, of the processor cover plate;

FIGS. 5-7 are right, left, and bottom views, respectively, of the processor cover plate; and,

FIG. 8 is a cross-sectional view of the processor cover plate taken along line 8-8 of FIG. 2.

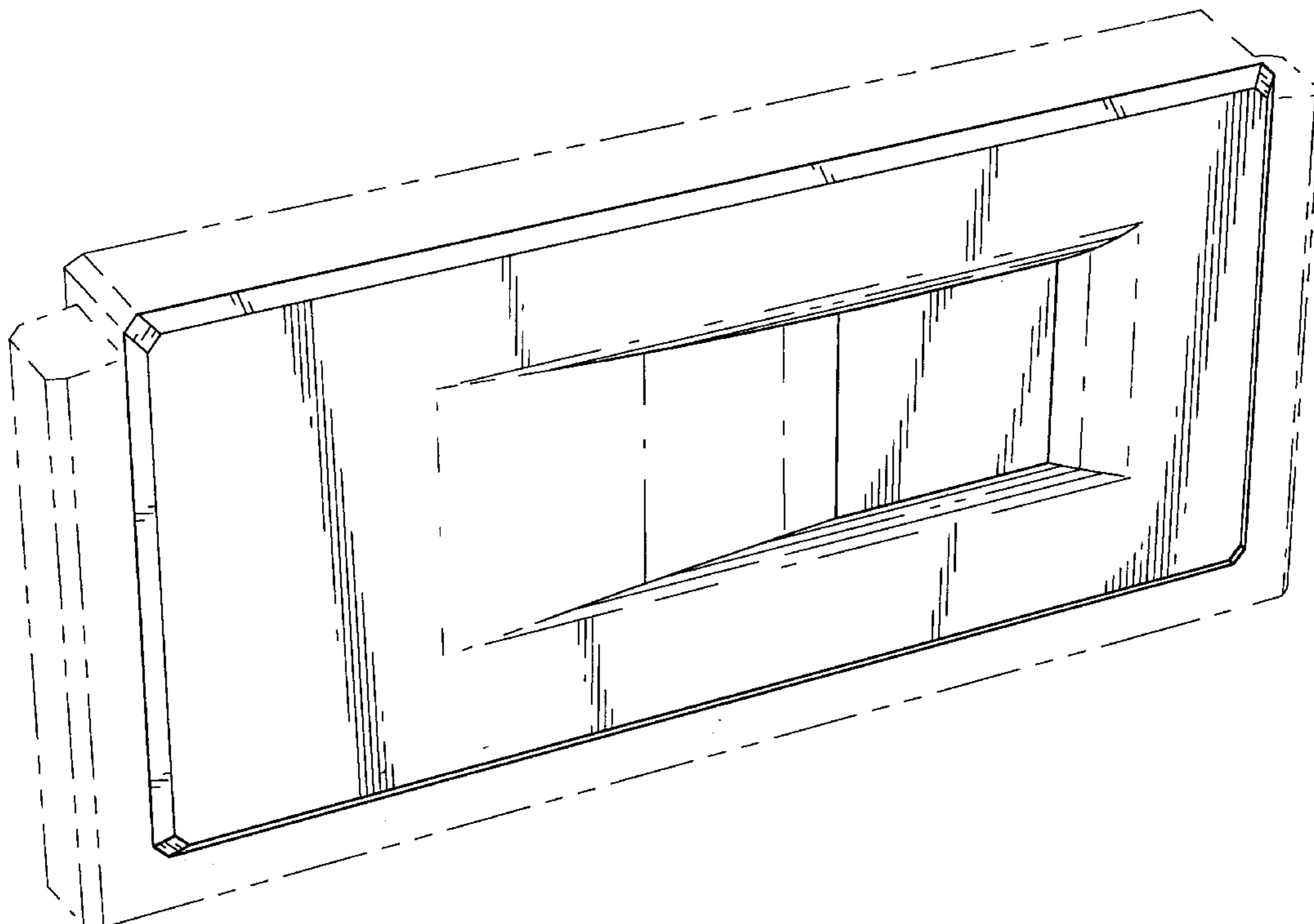
The processor is shown in broken lines in the views for illustrative purposes only and forms no part of the claimed design.

**1 Claim, 3 Drawing Sheets**

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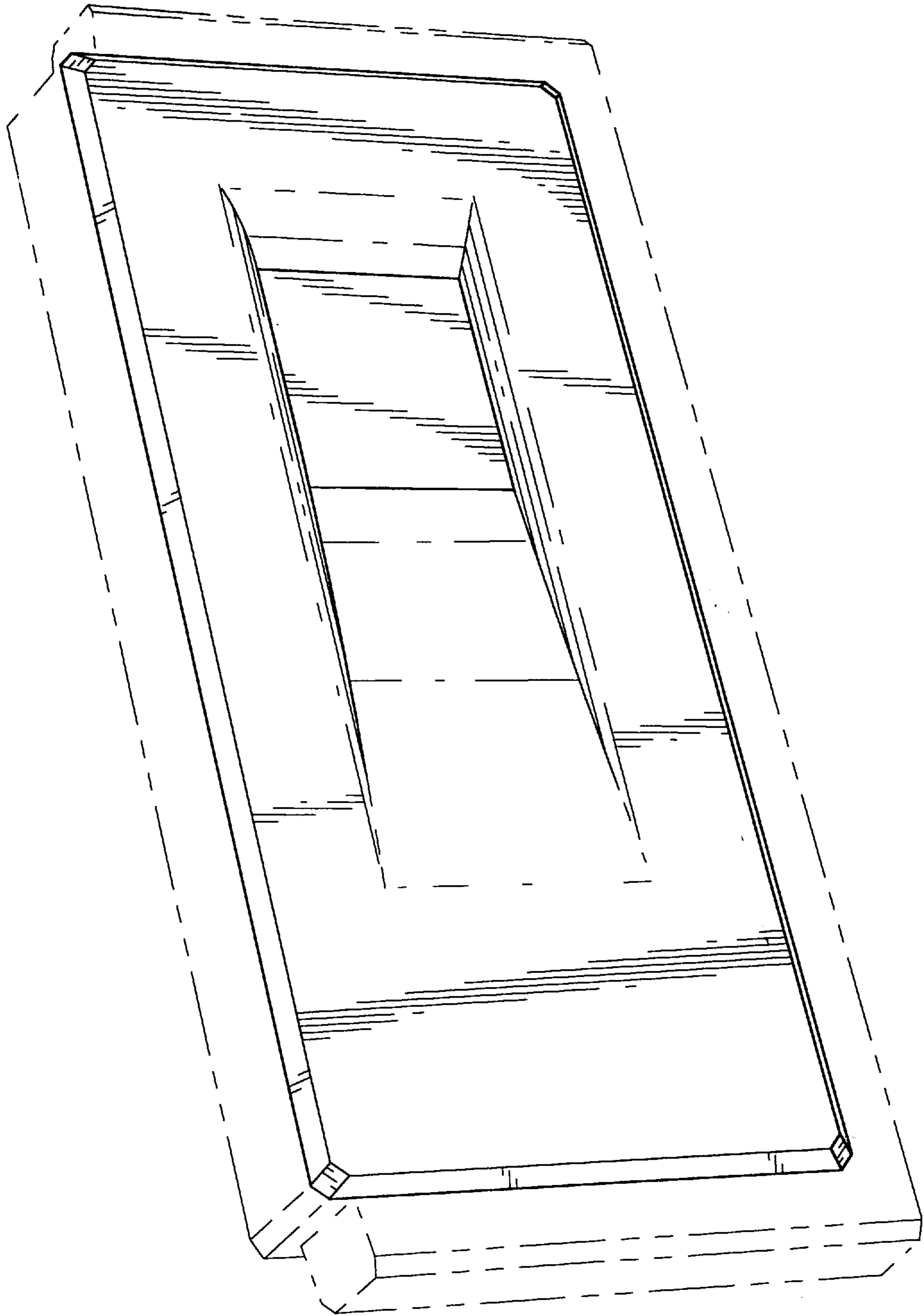
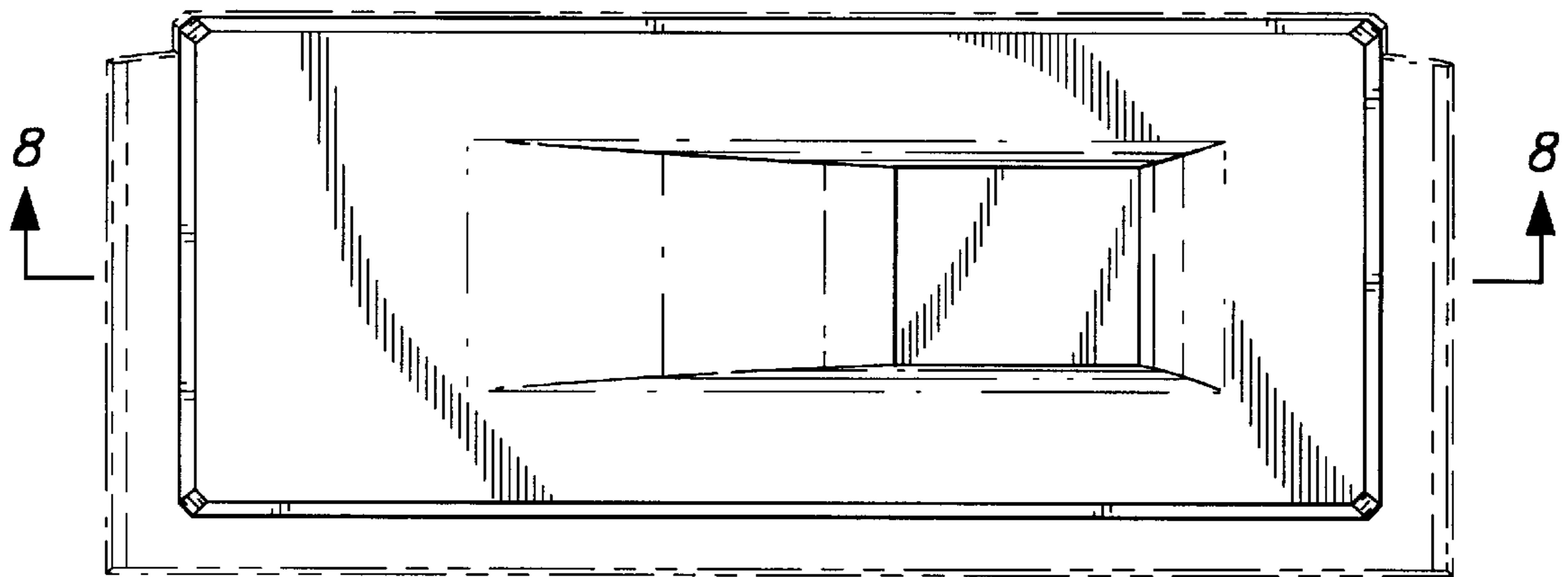
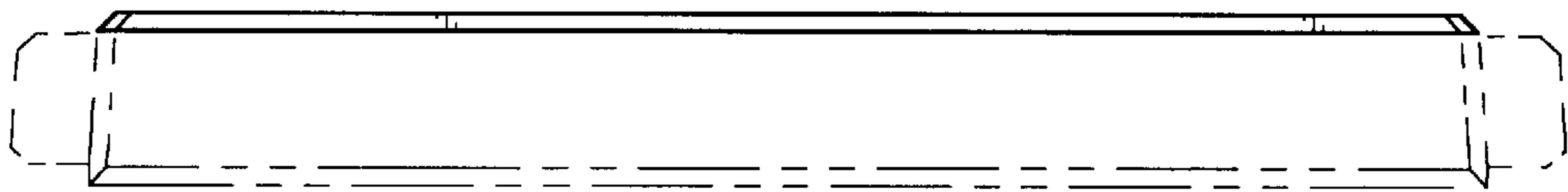


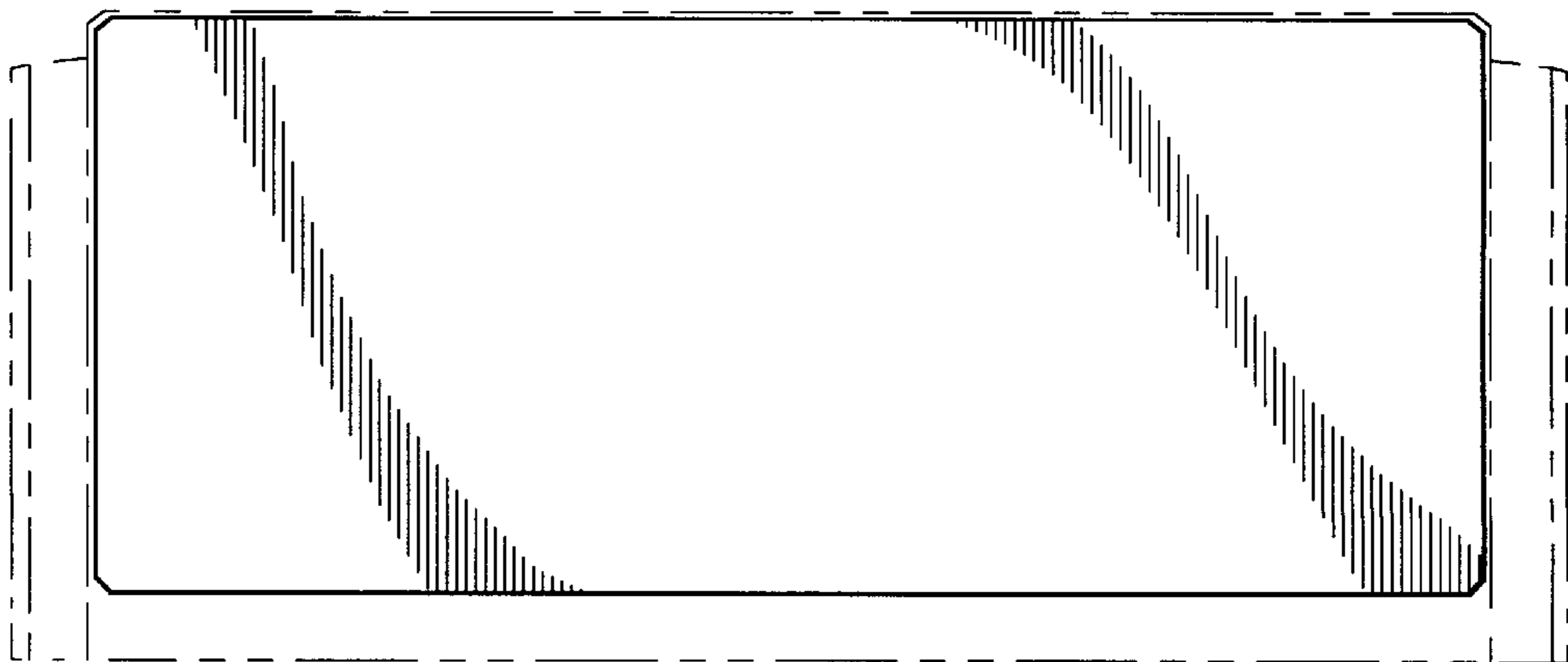
FIG. 1



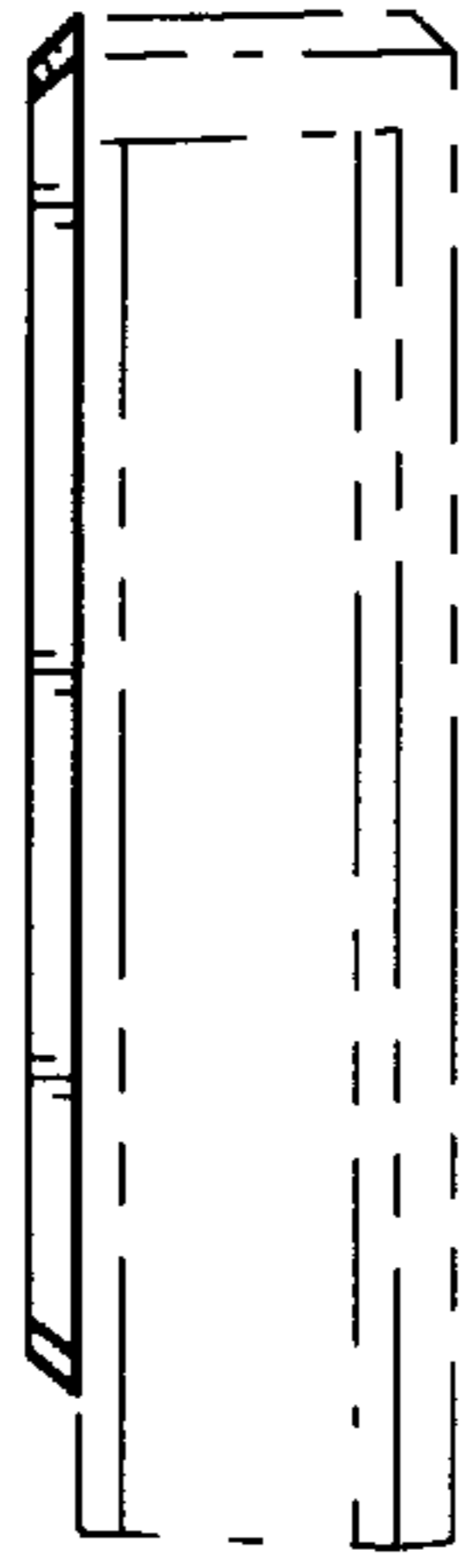
**FIG. 2**



**FIG. 3**



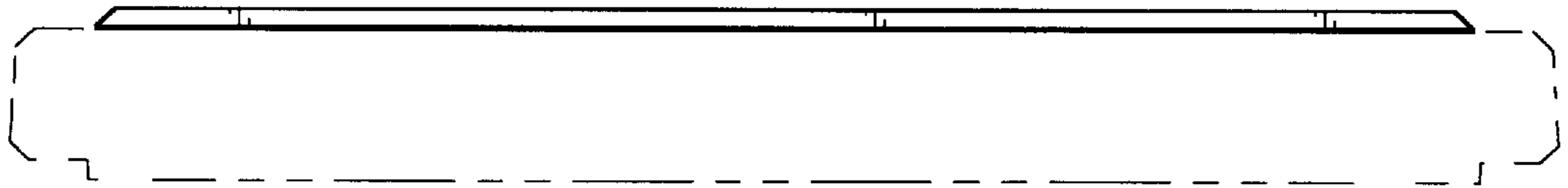
**FIG. 4**



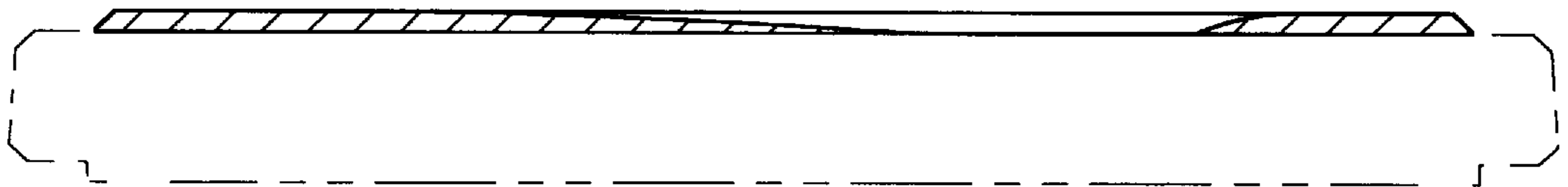
**FIG. 5**



**FIG. 6**



**FIG. 7**



**FIG. 8**