

US00D405062S

United States Patent [19]
Shimazu

[11] **Patent Number:** **Des. 405,062**
[45] **Date of Patent:** ****Feb. 2, 1999**

[54] **PROCESSING TUBE FOR USE IN A SEMICONDUCTOR WAFER HEAT PROCESSING APPARATUS**

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[**] Term: **14 Years**

[21] Appl. No.: **83,422**

[22] Filed: **Feb. 5, 1998**

[30] **Foreign Application Priority Data**

Aug. 20, 1997 [JP] Japan 9-65092

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/182**

[58] **Field of Search** D13/182; D15/144, D15/144.1, 199; 414/935-941, 217, 147; 437/247, 946

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Primary Examiner—Brian N. Vinson
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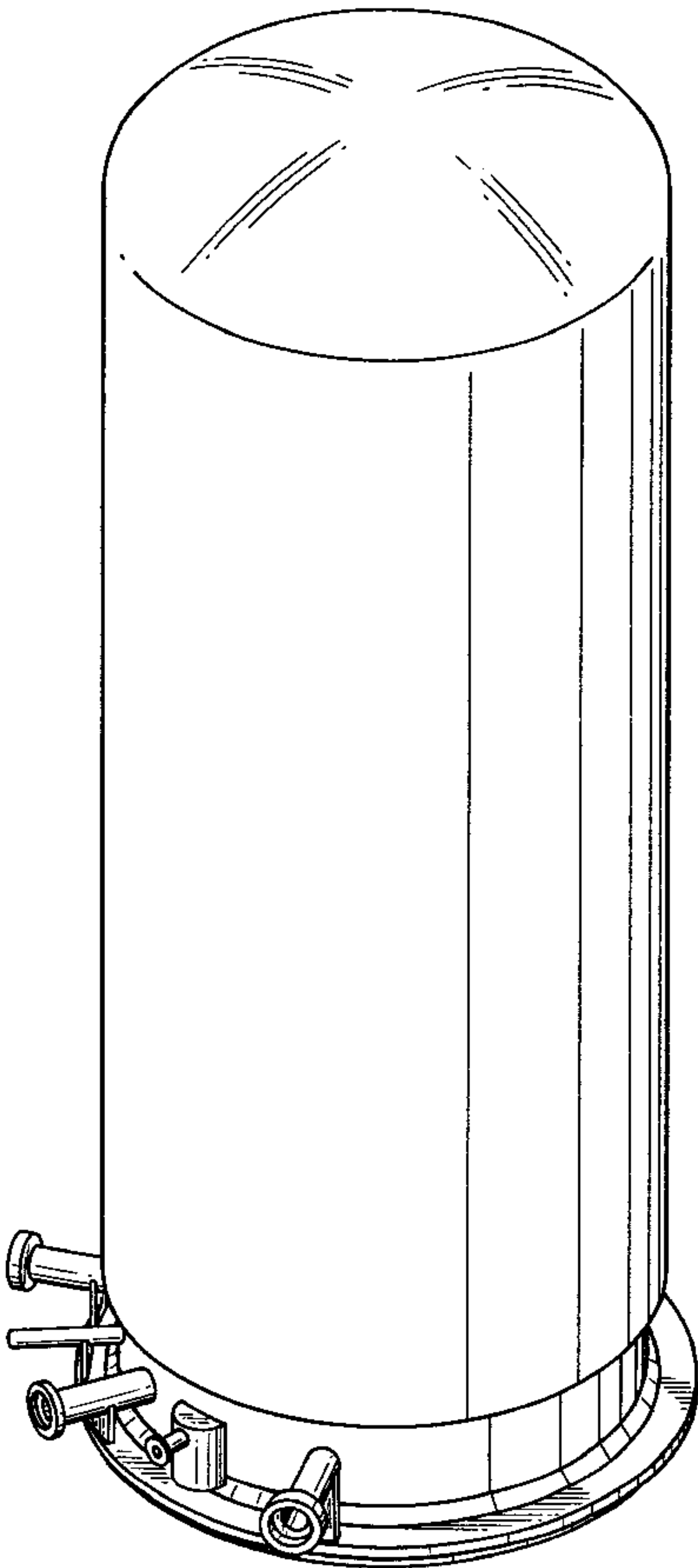
[57] **CLAIM**

I claim the ornamental design for processing tube for use in a semiconductor wafer heat processing apparatus, as shown and described.

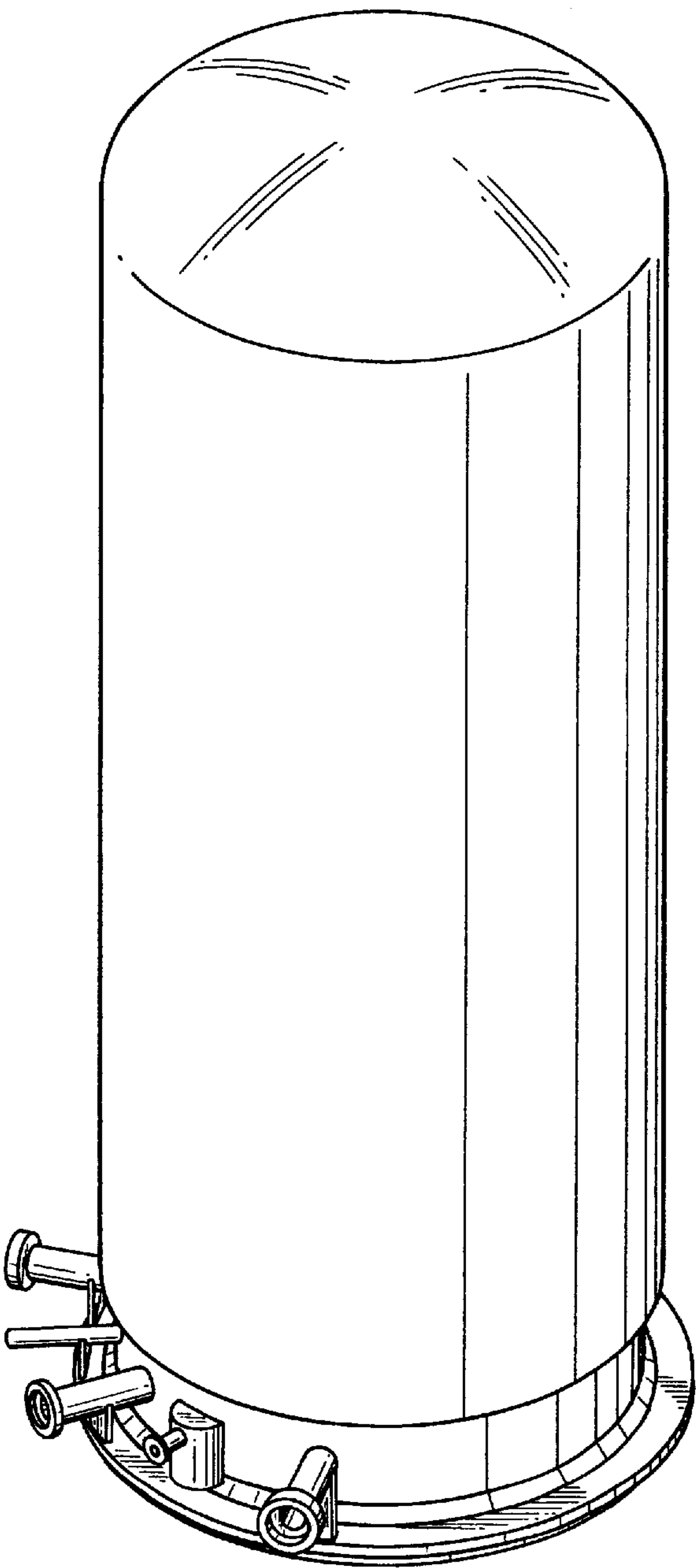
DESCRIPTION

FIG. 1 a perspective view of a processing tube for use in a semiconductor wafer heat processing apparatus;
FIG. 2 a front elevational view thereof;
FIG. 3 a top plan view thereof;
FIG. 4 a bottom plan view thereof;
FIG. 5 a rear elevational view thereof;
FIG. 6 a left side view thereof;
FIG. 7 a right side view thereof;
FIG. 8 a cross-sectional view taken along line VIII-VIII in FIG. 3;
FIG. 9 a cross-sectional view taken along line IX-IX in FIG. 2; and,
FIG. 10 a cross-sectional view taken along line X-X in FIG. 3.

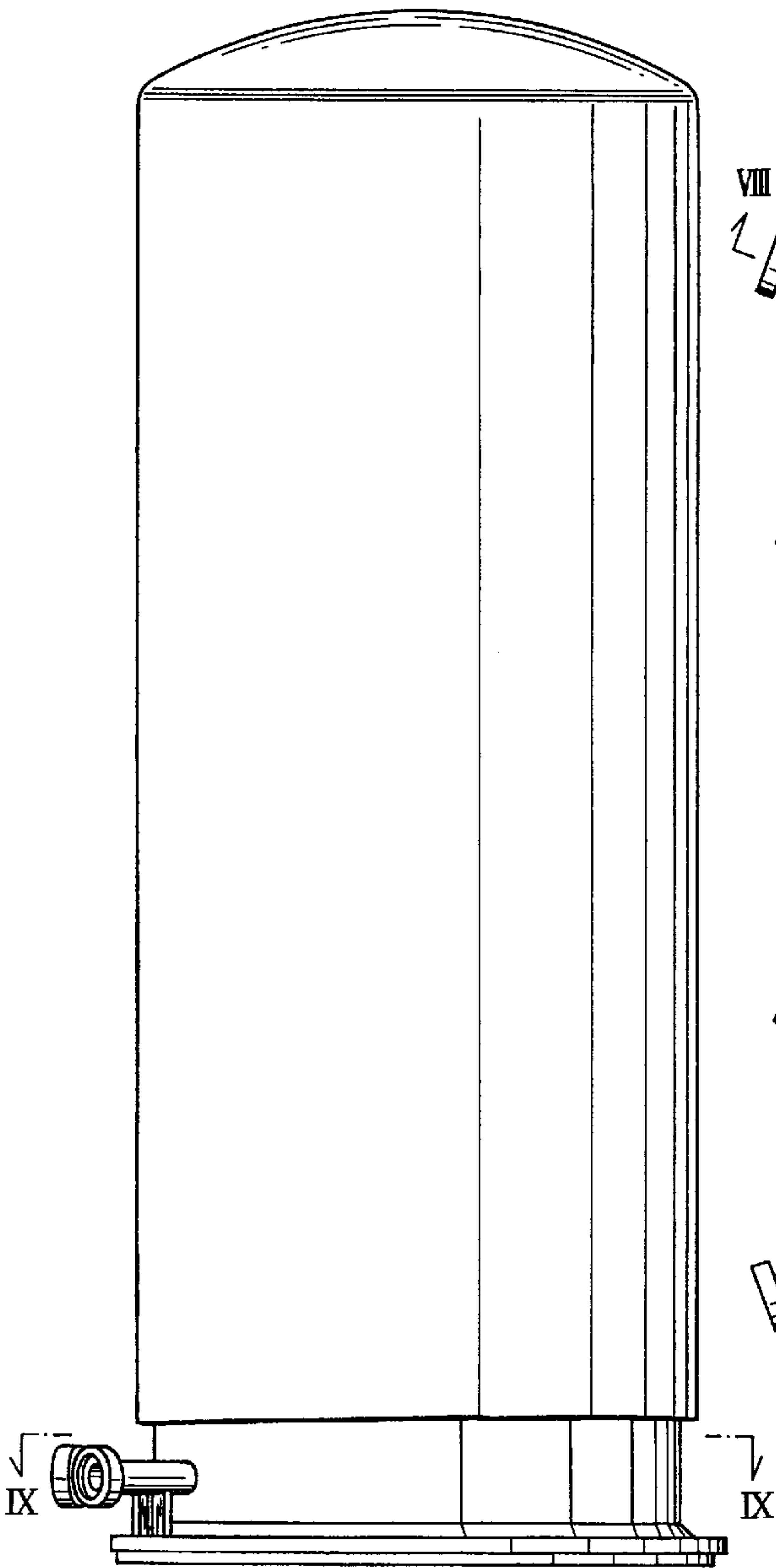
1 Claim, 5 Drawing Sheets



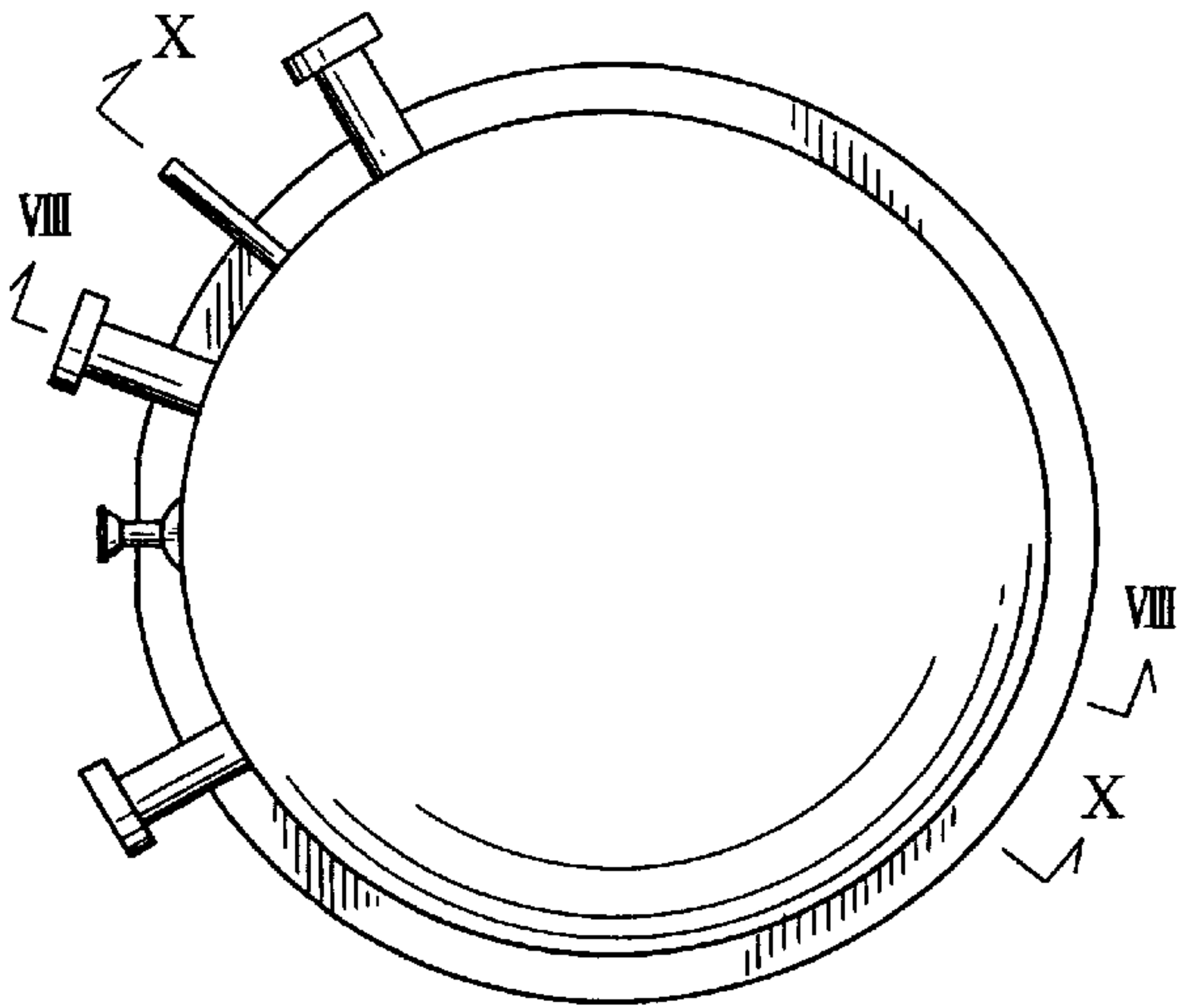
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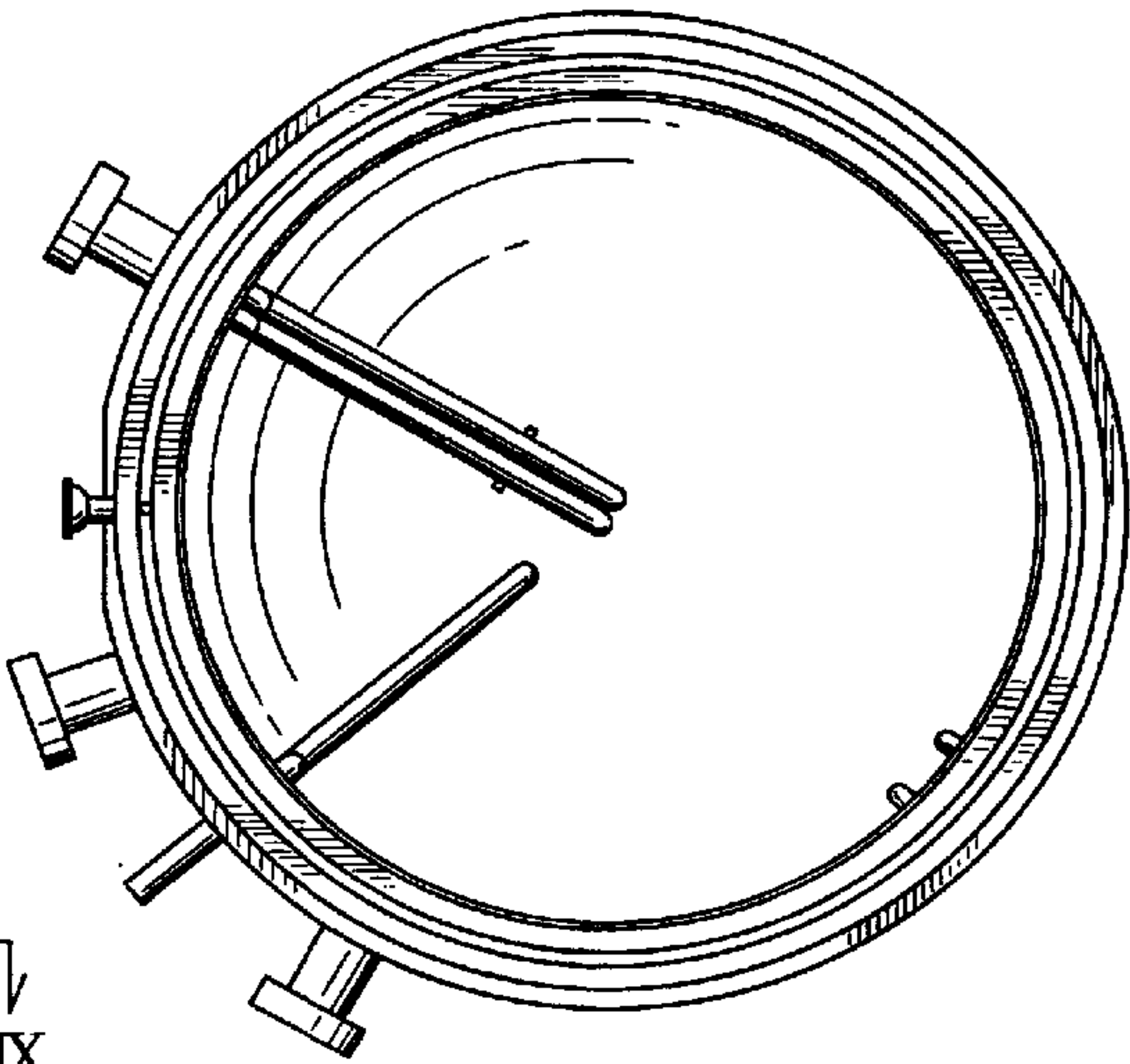
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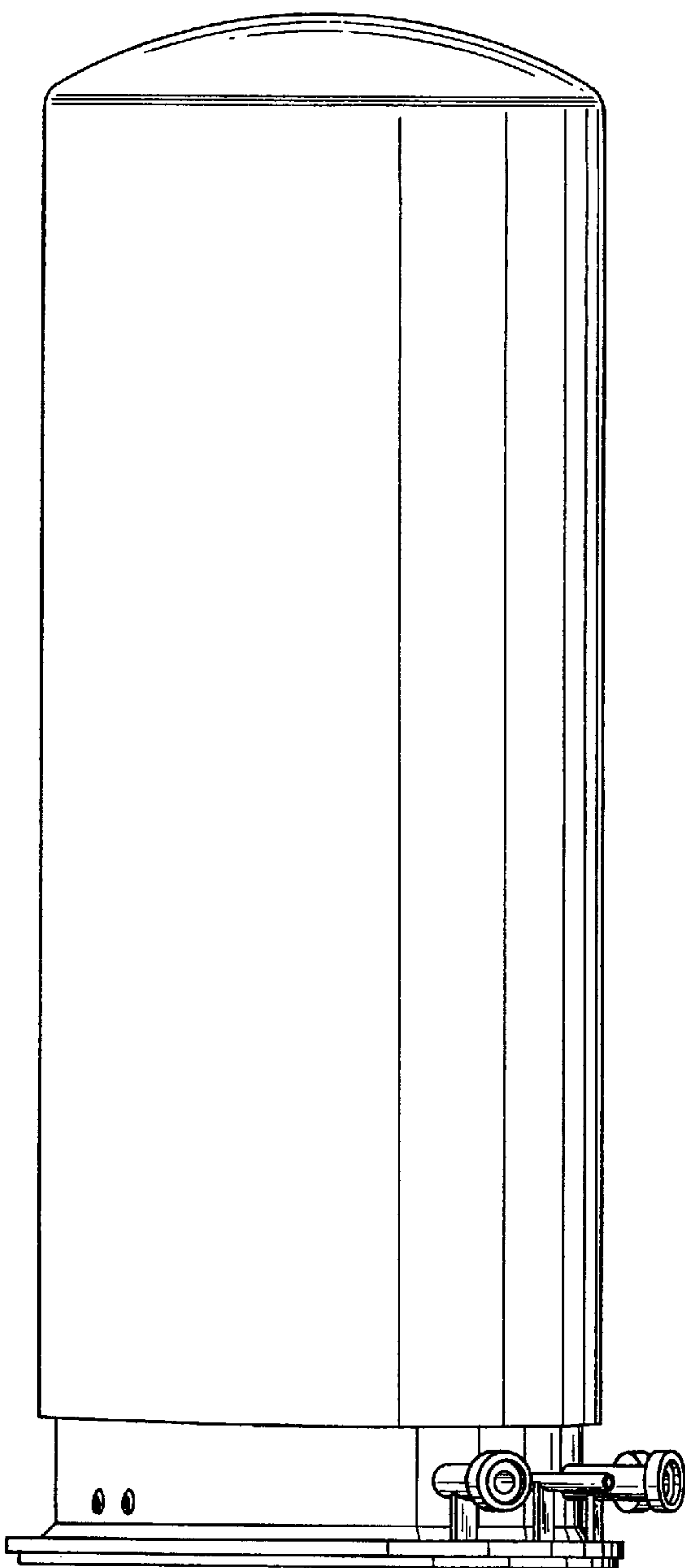
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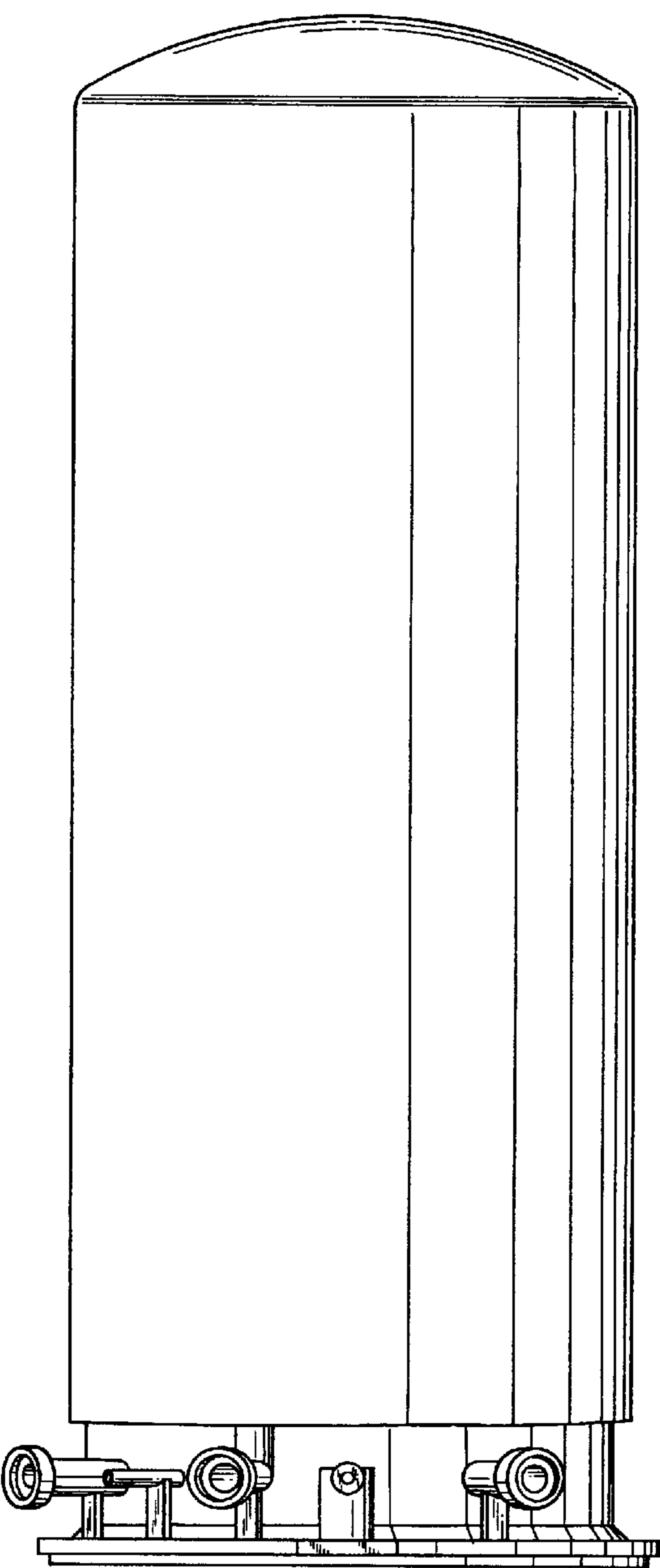
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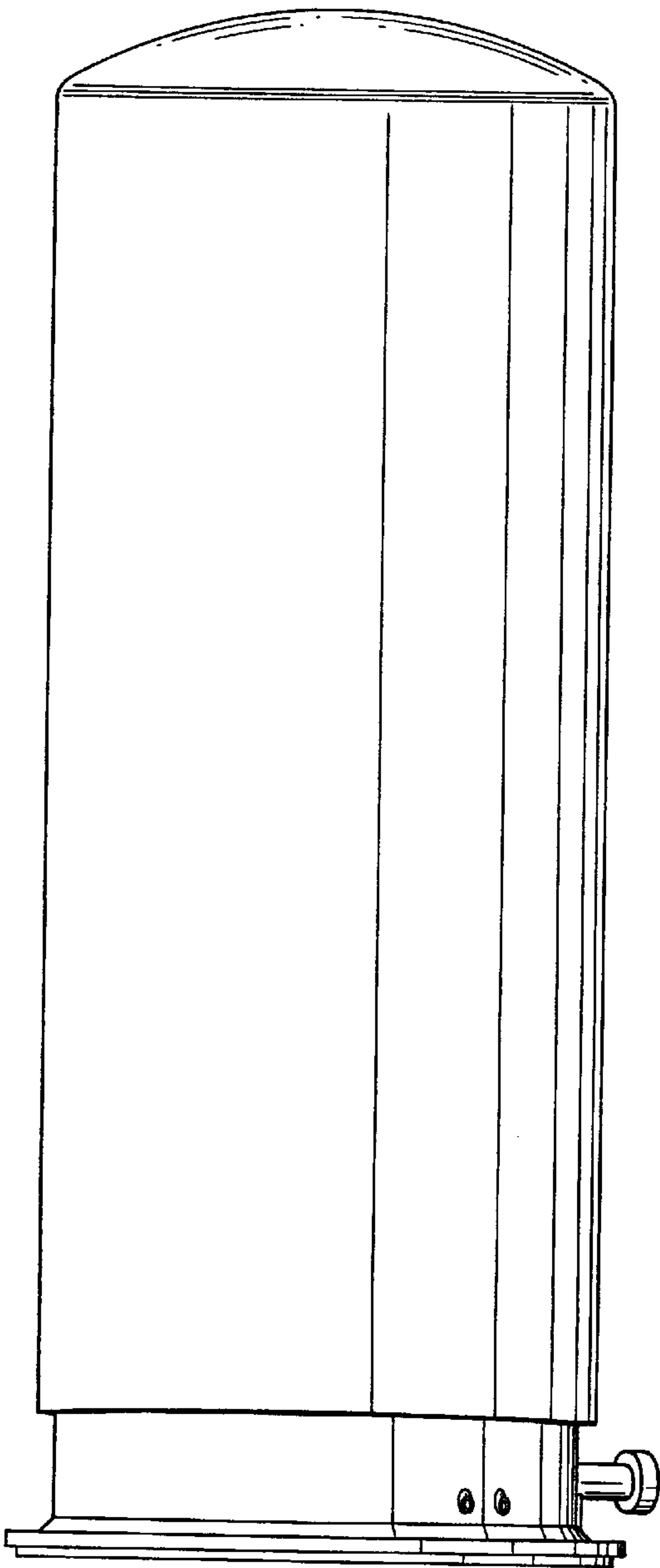
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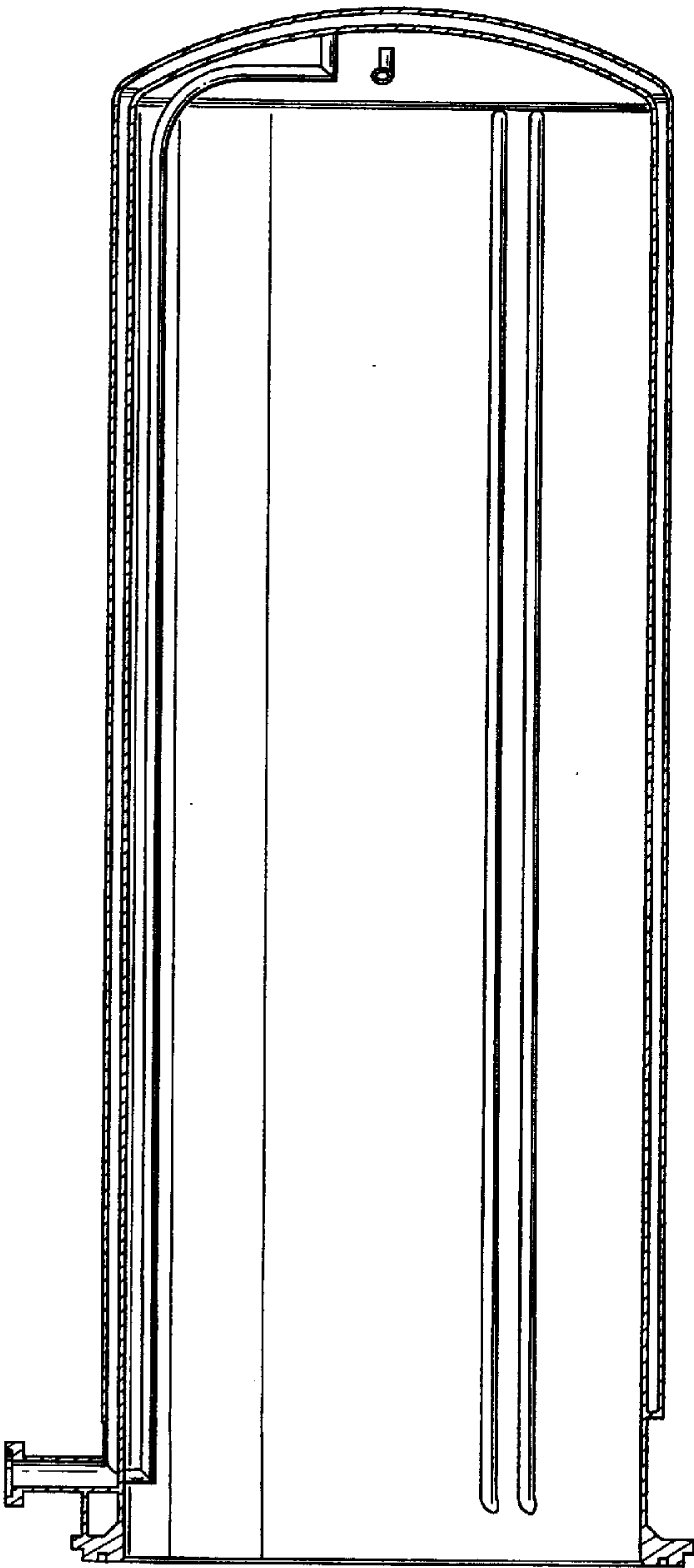
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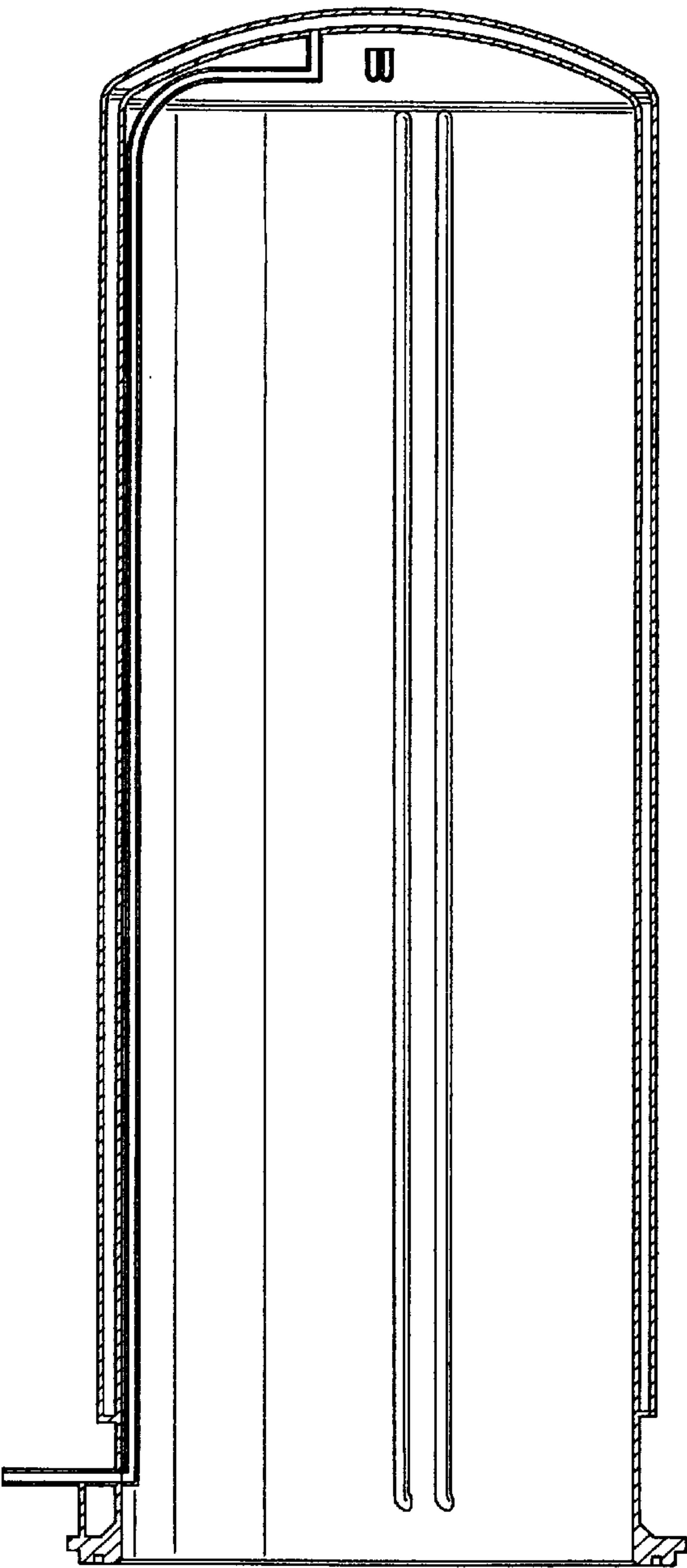
F I G . 7



F I G . 8



F I G . 1 0



F I G . 9

