



US00D404720S

United States Patent [19] Gayle

[11] **Patent Number: Des. 404,720**

[45] **Date of Patent: **Jan. 26, 1999**

[54] **HEAT SINK AND MOUNTING PLATE FOR INTEGRATED CIRCUITS**

4,807,018 2/1989 Cellai 357/70
5,049,973 9/1991 Satriano et al. 357/70

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Primary Examiner—Brian N. Vinson
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[**] Term: **14 Years**

[57] **CLAIM**

[21] Appl. No.: **66,146**

The ornamental design for a heat sink and mounting plate for integrated circuits, shown and described.

[22] Filed: **Feb. 6, 1997**

DESCRIPTION

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/179**

[58] **Field of Search** D13/179, 182;
357/70, 81; 165/185; 361/690, 697, 702,
709, 710, 711, 514

FIG. 1 is a perspective view of the heat sink and mounting plate for integrated circuits showing my new design;
FIG. 2 is a front elevational view thereof, the rear elevational view being identical in appearance;
FIG. 3 is a top plan view thereof, the bottom plan view being identical in appearance; and,
FIG. 4 is a side elevational view thereof, the opposite side view being identical in appearance.

[56] **References Cited**

U.S. PATENT DOCUMENTS

4,783,428 11/1988 Kalfus 357/81

1 Claim, 1 Drawing Sheet

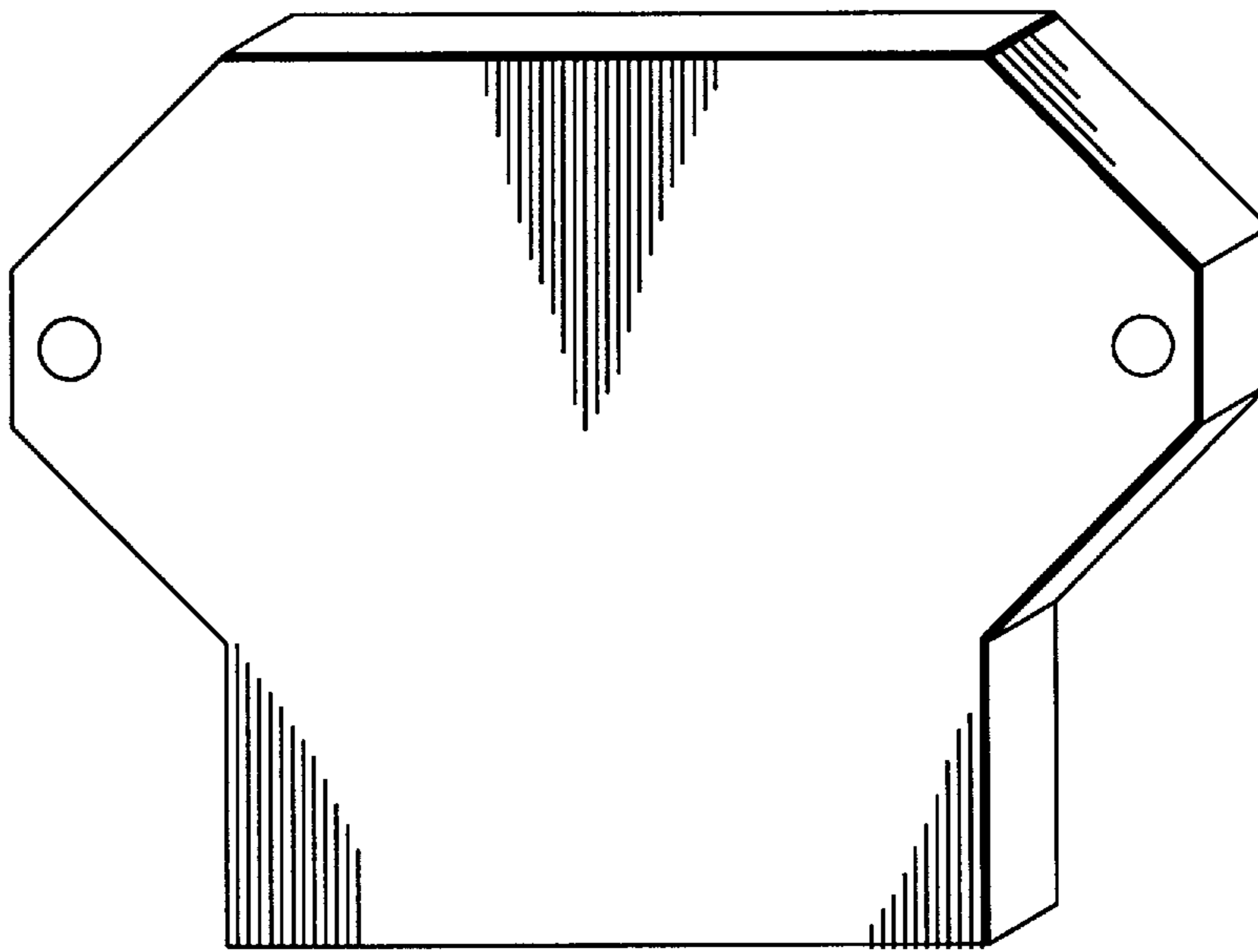




FIG. 3

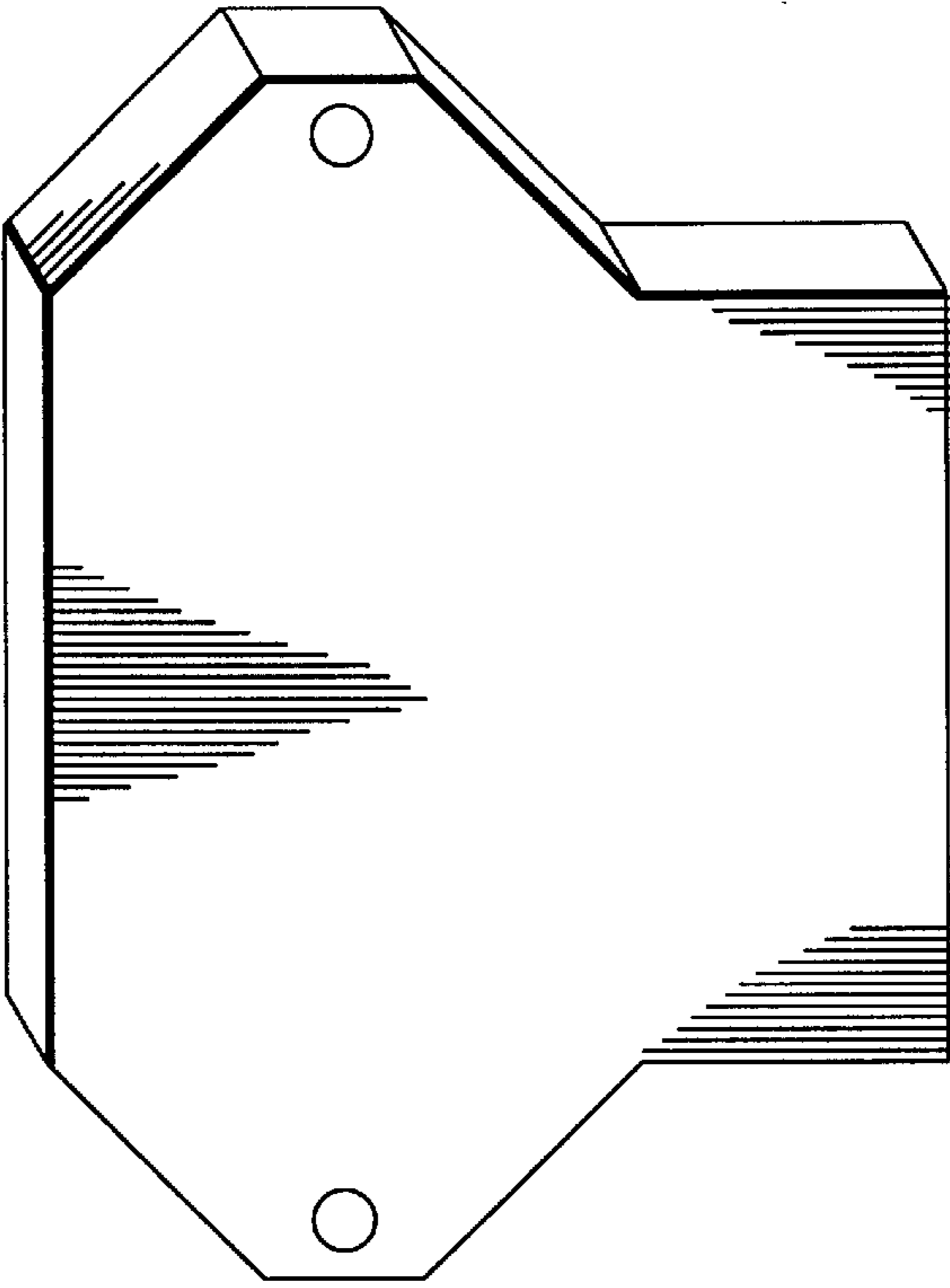


FIG. 1

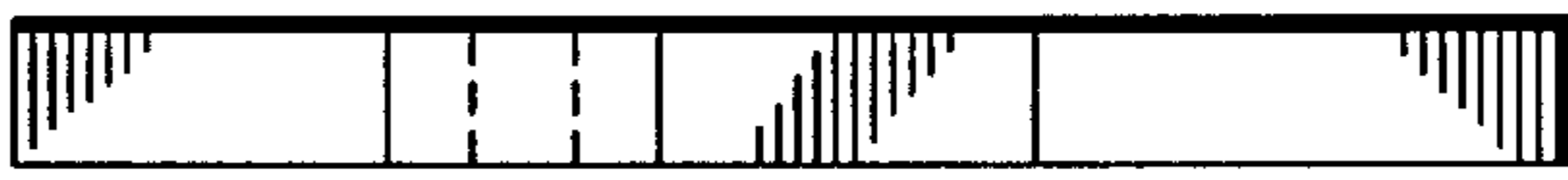


FIG. 4

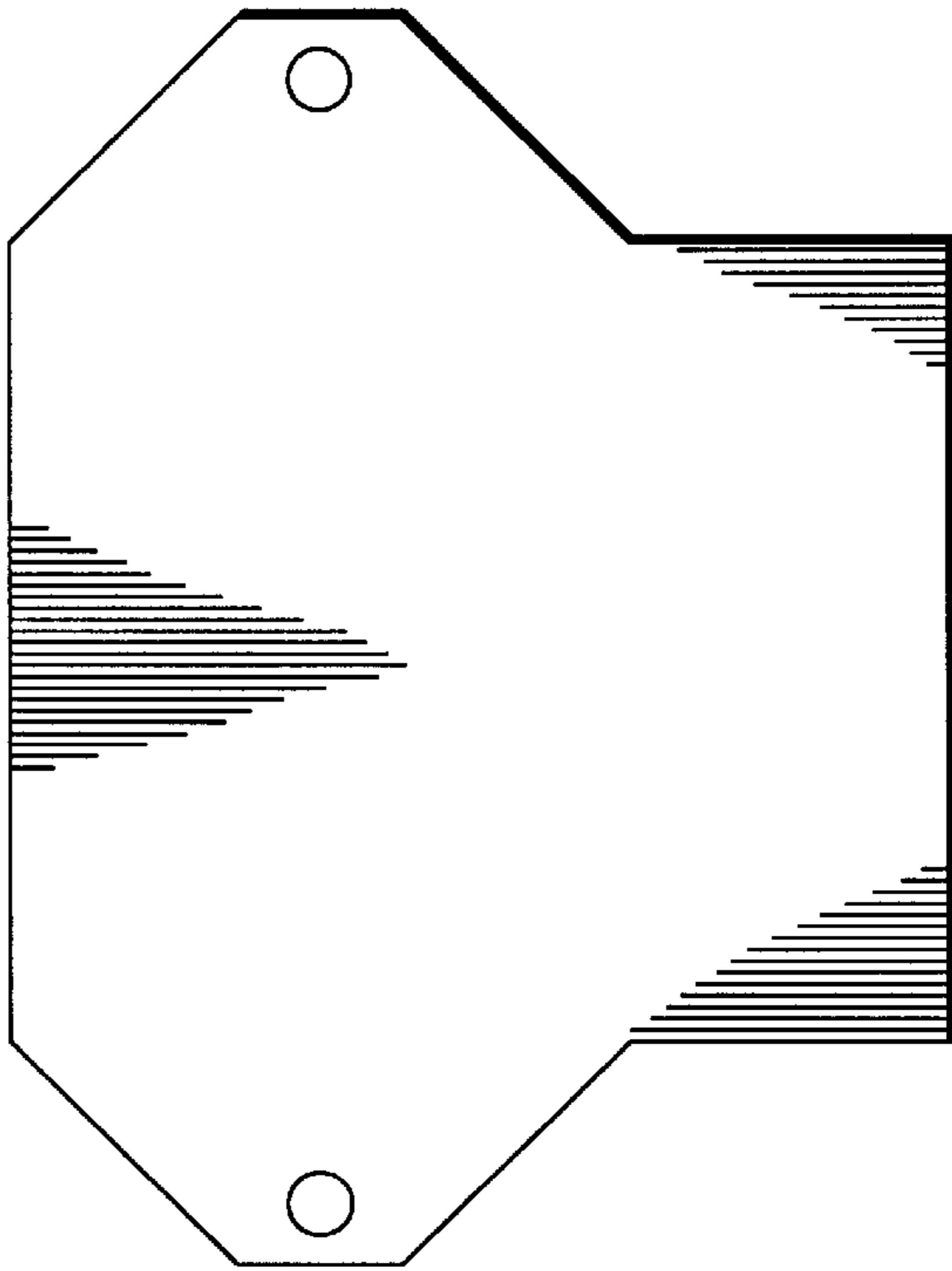


FIG. 2